PJC7428-AU	
30V N-Channel Enhancement Mode MOSFET	
Voltage30 VCurrent300mA	SOT-323
Features	
Advanced Trench Process Technology	el metre
 ESD Protected Specially Designed for Relay driver, Speed line drive, etc AEC-Q101 qualified 	
 Lead free in compliance with EU RoHS 2.0 	
 Green molding compound as per IEC61249 standard 	
Mechanical Data	
 Case : SOT-323 Package Terminals : Solderable per MIL-STD-750, Method 2026 Approx. Weight : 0.005 grams 	$\begin{array}{c c} & & & \\ & & & \\ \hline & & & \\ \hline & & & \\ \hline \\ \hline$

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V _{DS}	30			
Gate-Source Voltage	V _{GS}	<u>+</u> 10	V			
Continuous Drain Current ^(Note 4)	ous Drain Current ^(Note 4)		300			
Pulsed Drain Current ^(Note 1)		Ідм	600	mA		
Power Dissipation	T _A =25°C	_	350	mW		
	Derate above 25°C	PD	2.8	mW/∘C		
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C		
Thermal Resistance - Junction to Ambient ^(Note 3,4)		Reja	357	°C/W		

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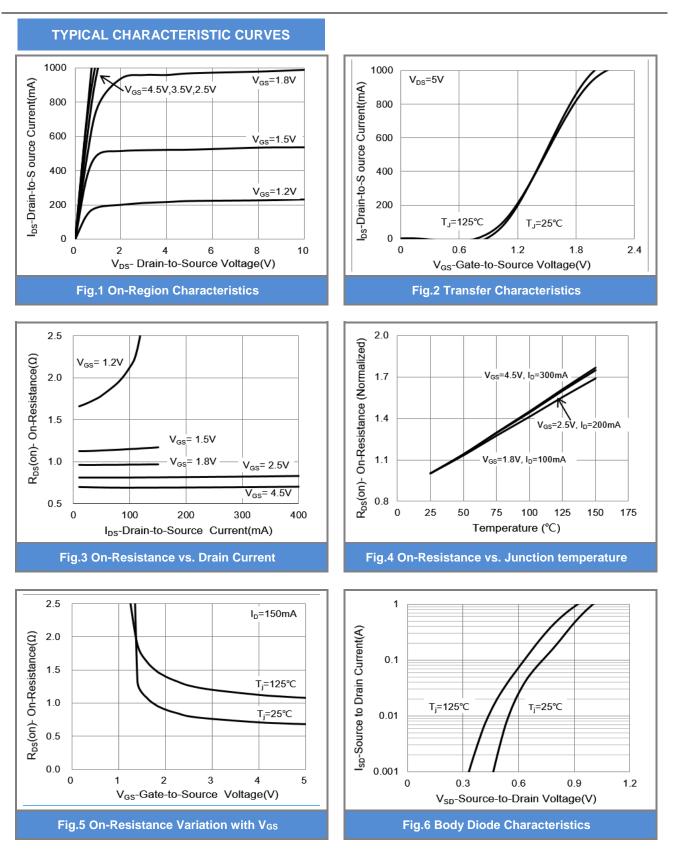
Electrical Characteristics (TA=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static		·					
Drain-Source Breakdown Voltage	BV _{DSS}	/dss Vgs=0V, Id=250uA		-	-	V	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.4	0.75	1	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =300mA	-	0.7	1.2	1.2	
		V _{GS} =2.5V, I _D =200mA	-	0.8	1.6		
		V _{GS} =1.8V, I _D =100mA	-	0.9	2	Ω	
		V _{GS} =1.5V, I _D =50mA	-	1.1	3		
		V _{GS} =1.2V, I _D =20mA	-	1.5	4		
Zero Gate Voltage Drain Current	IDSS	V _{DS} =24V, V _{GS} =0V	-	-	1		
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	-	<u>+</u> 10	uA	
Dynamic ^(Note 5)							
Total Gate Charge	Qg		-	0.9	-	nC	
Gate-Source Charge	Qgs	V _{DS} =10V, I _D =300mA,	-	0.3	-		
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V	-	0.2	-		
Input Capacitance	Ciss		-	45	-		
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V,	-	14	-	pF	
Reverse Transfer Capacitance	Crss	f=1MHz	-	0.8	-		
Turn-On Delay Time	td _(on)		-	8.3	-		
Turn-On Rise Time	tr	V _{DD} =10V, I _D =300mA,	-	5.7	-		
Turn-Off Delay Time	td _(off)	$V_{GS}=4V$,	-	35	-	ns	
Turn-Off Fall Time	tf	R _G =10Ω ^(Note 1,2)	-	12	-		
Drain-Source Diode							
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	300	mA	
Diode Forward Voltage	V _{SD}	Is=300mA, V _{GS} =0V	-	0.9	1.3	V	

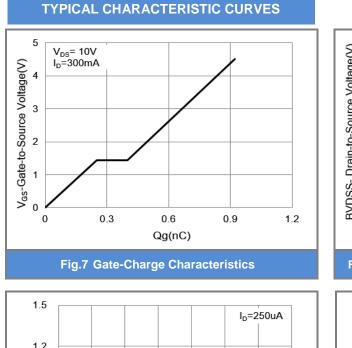
NOTES :

- 1. Pulse width <300us, Duty cycle <2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.









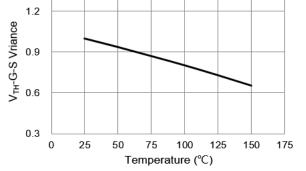
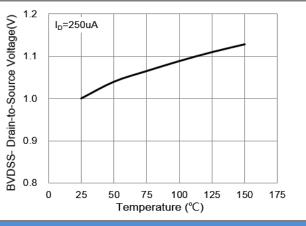


Fig.9 Threshold Voltage Variation with Temperature





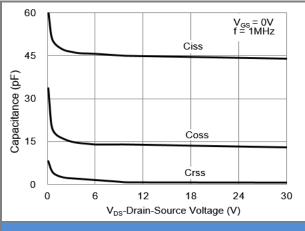


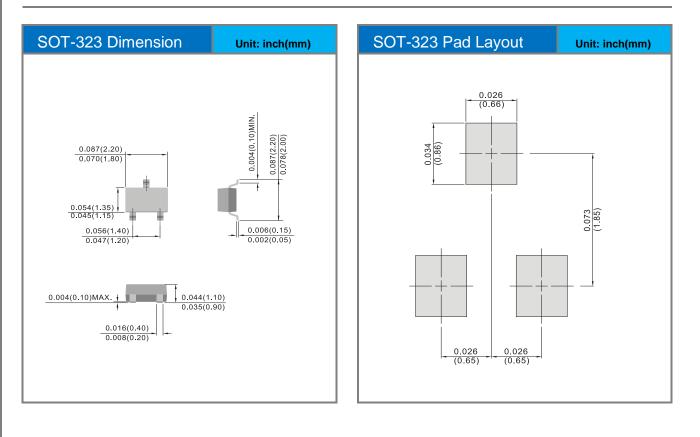
Fig.10 Capacitance vs. Drain-Source Voltage



Product and Packing Information

Part No.	Package Type	Packing Type	Marking	
PJC7428-AU	SOT-323	3K pcs / 7" reel	C28	

Packaging Information & Mounting Pad Layout





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