PJC7428-AU	
30V N-Channel Enhancement Mode MOSFET	
Voltage30 VCurrent300mA	SOT-323
Features	
Advanced Trench Process Technology	el metre
<ul> <li>ESD Protected</li> <li>Specially Designed for Relay driver, Speed line drive, etc</li> <li>AEC-Q101 qualified</li> </ul>	
<ul> <li>Lead free in compliance with EU RoHS 2.0</li> </ul>	
<ul> <li>Green molding compound as per IEC61249 standard</li> </ul>	
Mechanical Data	
<ul> <li>Case : SOT-323 Package</li> <li>Terminals : Solderable per MIL-STD-750, Method 2026</li> <li>Approx. Weight : 0.005 grams</li> </ul>	$\begin{array}{c c} & & & \\ & & & \\ \hline & & & \\ \hline & & & \\ \hline \\ \hline$

#### **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V <sub>DS</sub>	30			
Gate-Source Voltage	V <sub>GS</sub>	<u>+</u> 10	V			
Continuous Drain Current <sup>(Note 4)</sup>	ous Drain Current <sup>(Note 4)</sup>		300			
Pulsed Drain Current <sup>(Note 1)</sup>		Ідм	600	mA		
Power Dissipation	T <sub>A</sub> =25°C	_	350	mW		
	Derate above 25°C	PD	2.8	mW/∘C		
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C		
Thermal Resistance - Junction to Ambient <sup>(Note 3,4)</sup>		Reja	357	°C/W		

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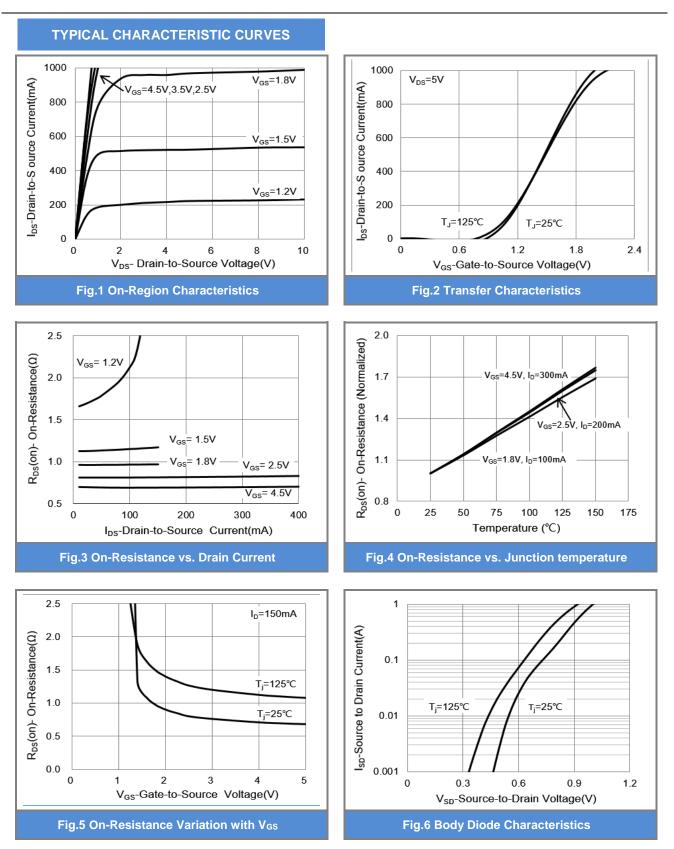
#### Electrical Characteristics (TA=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static		·					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	/dss Vgs=0V, Id=250uA		-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.4	0.75	1	V	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =300mA	-	0.7	1.2	1.2	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =200mA	-	0.8	1.6		
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =100mA	-	0.9	2	Ω	
		V <sub>GS</sub> =1.5V, I <sub>D</sub> =50mA	-	1.1	3		
		V <sub>GS</sub> =1.2V, I <sub>D</sub> =20mA	-	1.5	4		
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V	-	-	1		
Gate-Source Leakage Current	Igss	V <sub>GS</sub> = <u>+</u> 8V, V <sub>DS</sub> =0V	-	-	<u>+</u> 10	uA	
Dynamic <sup>(Note 5)</sup>							
Total Gate Charge	Qg		-	0.9	-	nC	
Gate-Source Charge	Qgs	V <sub>DS</sub> =10V, I <sub>D</sub> =300mA,	-	0.3	-		
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =4.5V	-	0.2	-		
Input Capacitance	Ciss		-	45	-		
Output Capacitance	Coss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V,	-	14	-	pF	
Reverse Transfer Capacitance	Crss	f=1MHz	-	0.8	-		
Turn-On Delay Time	td <sub>(on)</sub>		-	8.3	-		
Turn-On Rise Time	tr	V <sub>DD</sub> =10V, I <sub>D</sub> =300mA,	-	5.7	-		
Turn-Off Delay Time	td <sub>(off)</sub>	$V_{GS}=4V$ ,	-	35	-	ns	
Turn-Off Fall Time	tf	R <sub>G</sub> =10Ω <sup>(Note 1,2)</sup>	-	12	-		
Drain-Source Diode							
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	300	mA	
Diode Forward Voltage	V <sub>SD</sub>	Is=300mA, V <sub>GS</sub> =0V	-	0.9	1.3	V	

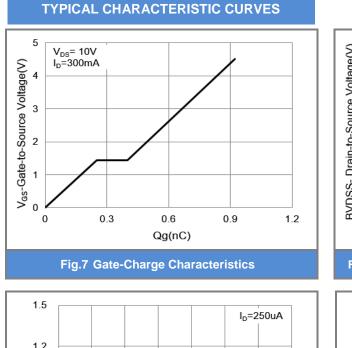
NOTES :

- 1. Pulse width <300us, Duty cycle <2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R<sub>0JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.









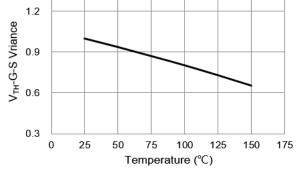
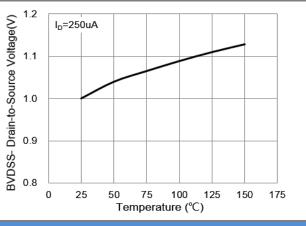


Fig.9 Threshold Voltage Variation with Temperature





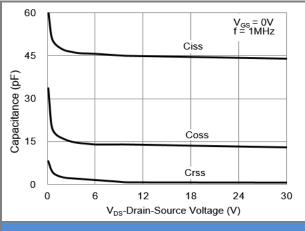


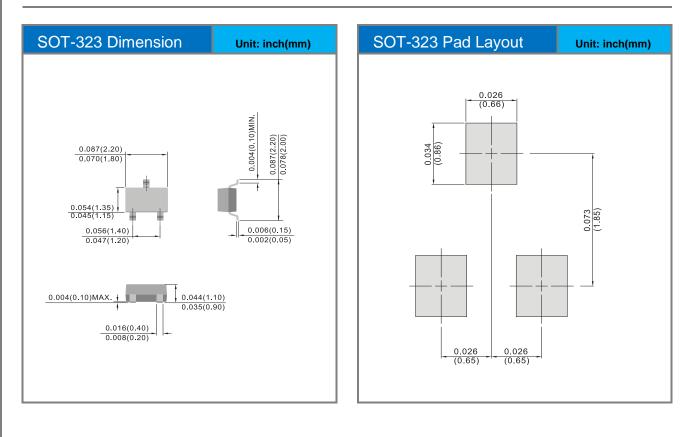
Fig.10 Capacitance vs. Drain-Source Voltage



#### **Product and Packing Information**

Part No.	Package Type	Packing Type	Marking	
PJC7428-AU	SOT-323	3K pcs / 7" reel	C28	

#### **Packaging Information & Mounting Pad Layout**





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