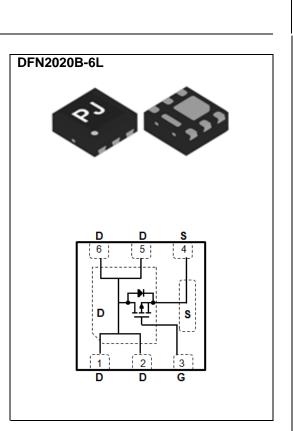
ΡΛΝ	JIT
	SEMI
	CONDUCTOR

PJQ2461-AU 60V P-Channel Enhancement Mode MOSFET Voltage -60 V Current -2.4 A Features • R_{DS(ON)}, V_{GS}@-10V, I_D@-2A<170mΩ • R_{DS(ON)}, V_{GS}@-4.5V, I_D@-1.5A<220mΩ • High switching speed

- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-60		
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V	
Continuous Drain Current (Note 4)	T _A =25°C		-2.4		
	T _A =70°C	ld ld	-1.9	А	
Pulsed Drain Current (Note 1)	Idm	-9.6			
Power Dissipation	T _A =25°C		2	W	
	T _A =70°C	PD	1.3		
Single Pulse Avalanche Energy (Note 6)		Eas	32	mJ	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	٥C	
Typical Thermal Resistance - Junction to Ambient ^(Note 4,5)		Reja	62.5	°C/W	



PJQ2461-AU

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						-
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS}=0V$, $I_{D}=-250uA$	-60	-	-	- v
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-1.0	-1.88	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-2A	-	140	170	mΩ
		V _{GS} =-4.5V, I _D =-1.5A	-	190	220	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	lgss	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_{g}	V _{DS} =-30V, I _D =-2A, V _{GS} =-10V ^(Note 1,2)	-	8.3	-	
Gate-Source Charge	Qgs		-	1.8	-	nC
Gate-Drain Charge	Q_{gd}		-	1.6	-	
Input Capacitance	Ciss		-	430	-	
Output Capacitance	Coss	V _{DS} =-30V, V _{GS} =0V, f=1MHZ	-	33	-	pF
Reverse Transfer Capacitance	Crss		-	29	-	
Turn-On Delay Time	td _(on)	V_{DD} =-30V, I _D =-1A, V _{GS} =-10V, R _G =6Ω ^(Note 1,2)	-	5.1	-	
Turn-On Rise Time	tr		-	20	-	
Turn-Off Delay Time	td _(off)		-	36	-	ns
Turn-Off Fall Time	tf	NG=012 (1000 1,-)	-	11	-	
Drain-Source Diode				•		
Maximum Continuous Drain-Source	ls		_	_	-1.5	А
Diode Forward Current	IS		-	-	-1.5	~
Diode Forward Voltage	V _{SD}	Is=-1A, V _{GS} =0V	-	-0.78	-1	V

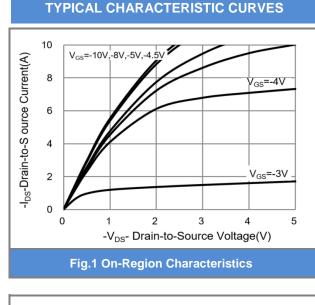
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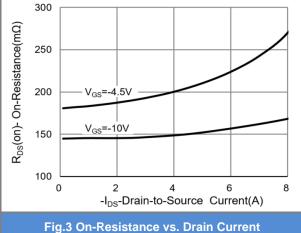
- 1. Pulse width <300us, Duty cycle <2%.
- 2. Essentially independent of operating temperature typical characteristics.
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- R_{⊕JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=1mH, I_{AS}=-8A, V_{DD}=-25V, V_{GS}=-10V
- 7. Guaranteed by design, not subject to production testing.

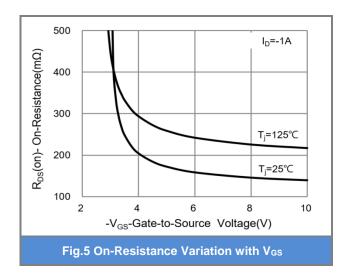
SEMI CONDUCTOR

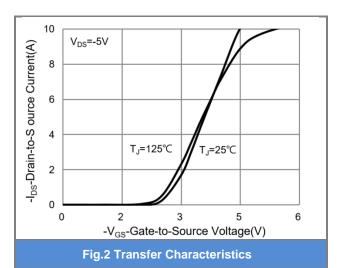
ΡΛΝ

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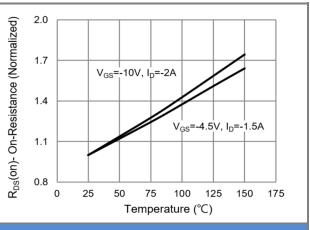
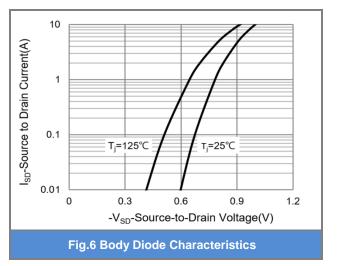
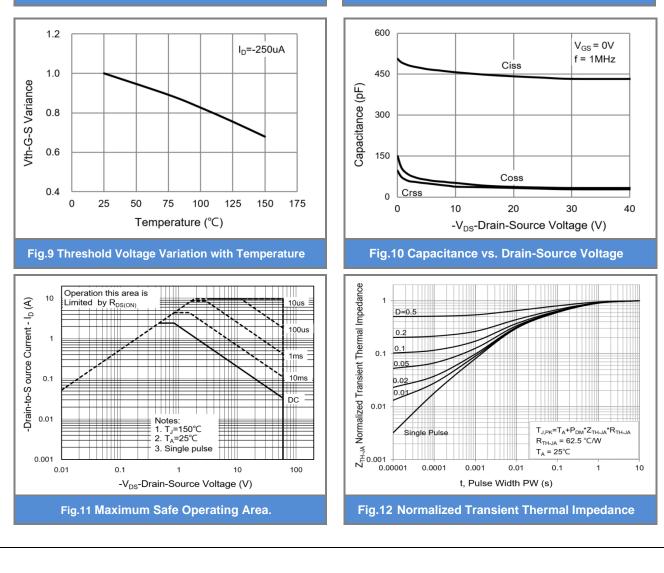


Fig.4 On-Resistance vs. Junction temperature



June 17,2021





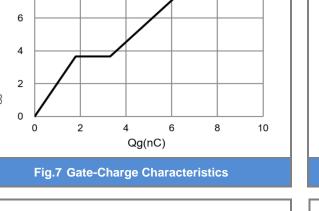
TYPICAL CHARACTERISTIC CURVES

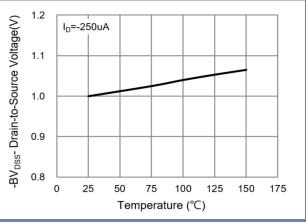
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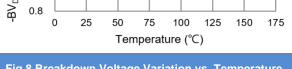
SEMI CONDUCTOR

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10 V_{DS}=-30V -V_{GS}-Gate-to-Source Voltage(V) I_D=-2A 8 6 4 2 0 0 2 6 8 4 10 Qg(nC)









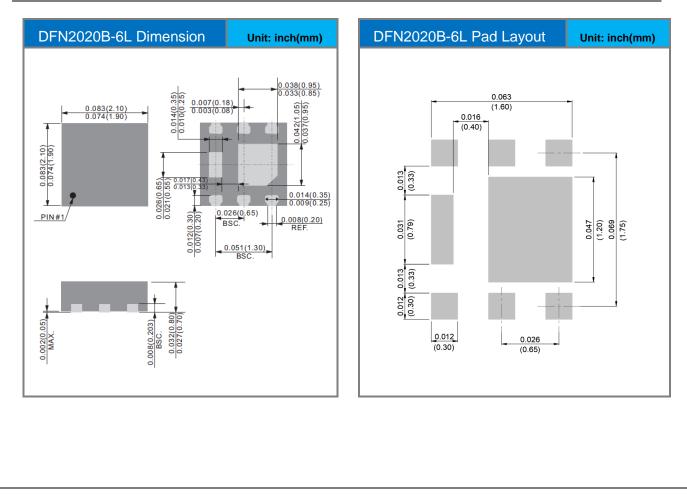


PJQ2461-AU

Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2461-AU_R1_000A1	DFN2020B-6L	3K pcs / 7" reel	461	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout





PJQ2461-AU

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