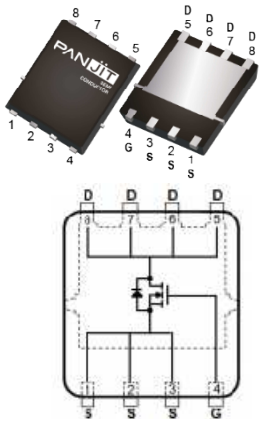
115V N-Channel Enhancement Mode MOSFET

Voltage	**115 V**	**R_{DSON}**	**7.6 mΩ**
Current	**91.4 A**	**$Q_{G\ (TYP)}$**	**77 nC**

DFN5060-8L



Top side view

Feature

- $R_{DS(ON)}$, V_{GS}@10V, I_D@20A<7.6mΩ
- $R_{DS(ON)}$, V_{GS}@4.5V, I_D@10A<11mΩ
- High switching speed
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0028 ounces, 0.08 grams

Application

- SR solutions of Travel Adapter, PD Charger, Gaming Adapter.

Absolute Maximum Ratings (T_A = 25 °C unless otherwise specified)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	115	V
Gate-Source Voltage		V_{GS}	+20/ -12	
Continuous Drain Current	T_C=25°C	I_D	91.4	A
	T_C=100°C		57.8	
Pulsed Drain Current (Note 1)	T_C=25°C	I_{DM}	365	A
Single Pulse Avalanche Current (Note 5)		I_{AS}	37	A
Single Pulse Avalanche Energy (Note 5)		E_{AS}	68	mJ
Power Dissipation	T_C=25°C	P_D	125	W
	T_C=100°C		50	
Operating Junction and & Storage Temperature Range		T_J,T_{STG}	-55~150	°C

Thermal Characteristics

PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNITS
Thermal Resistance (Note.4)	Junction-to-Case (Bottom)	$R_{\theta JC}$	0.9	1	°C/W
	Junction-to-Case (Top)	$R_{\theta JT}$	19.3	23	°C/W
	Junction-to-Ambient	$R_{\theta JA}$	41	50	°C/W

Electrical Characteristics (T_A = 25 °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	115	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	1.2	1.7	2.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=20A	-	6.1	7.6	mΩ
		V_{GS}=4.5V, I_D=10A	-	8.4	11	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=115V, V_{GS}=0V	-	-	100	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=+20V, V_{DS}=0V	-	-	100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	V_{DS}=60V, I_D=50A, V_{GS}=10V	-	77	105	nC
Gate-Source Charge	Q_{gs}		-	15.9	-	
Gate-Drain Charge	Q_{gd}		-	16.9	-	
Plateau Voltage	V_{GP}		-	3.5	-	V
Input Capacitance	Ciss	V_{DS}=60V, V_{GS}=0V, f=1.0MHZ	-	4740	-	pF
Output Capacitance	Coss		-	338	-	
Reverse Transfer Capacitance	Crss		-	36	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=60V, I_D=50A, V_{GS}=10V, R_G=6Ω (Note 2)	-	34	-	ns
Turn-On Rise Time	t_r		-	111	-	
Turn-Off Delay Time	$td_{(off)}$		-	116	-	
Turn-Off Fall Time	t_f		-	119	-	
Gate Resistance	Rg	f=1.0MHz	-	1.6	-	Ω
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	Tc =25 °C	-	-	107	A
Diode Forward Voltage	V_{SD}	I_S=1A, V_{GS}=0V	-	0.68	1	V
Reverse Recovery Time	Trr	VR =100V, Is= 10A di/dt = 100A/us, Tj=25 °C	-	62.7	-	ns
Reverse Recovery Charge	Qrr		-	98	-	nC

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 $inch^2$ with 2oz.square pad of copper.
5. The test condition is L=0.1mH, I_{AS}=37A, V_{DD}=70V, V_{GS}=10V, R_G=25ohm, Starting T_J=25°C
6. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTIC CURVES

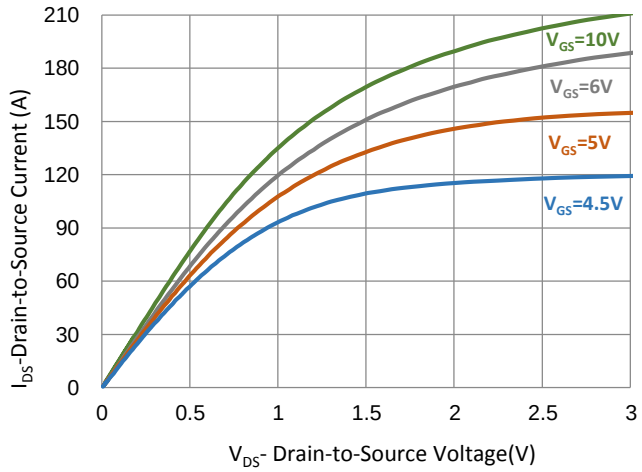


Fig.1 Output Characteristics

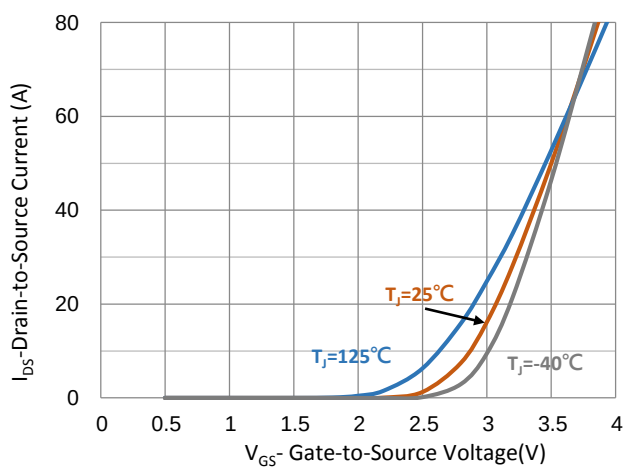


Fig.2 Transfer Characteristics

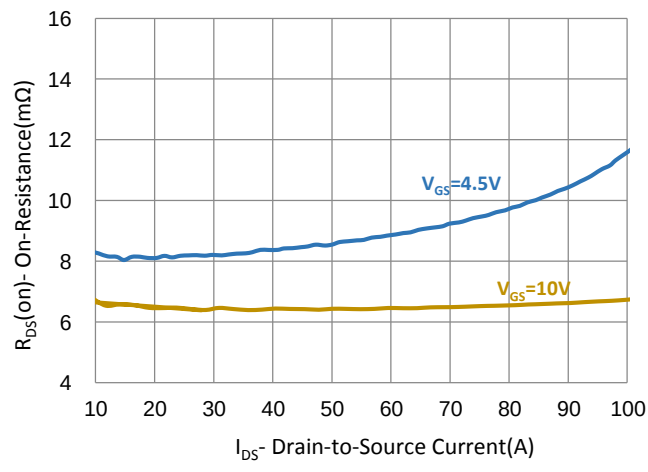


Fig.3 On-Resistance vs. Drain Current

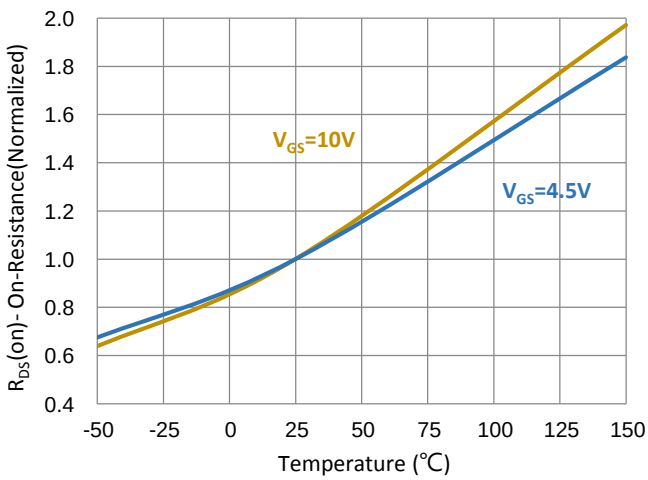


Fig.4 On-Resistance vs. Junction temperature

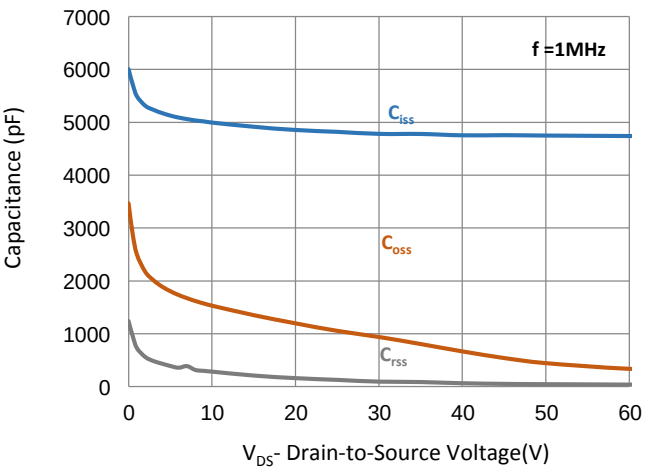


Fig.5 Capacitance vs. Drain-Source Voltage

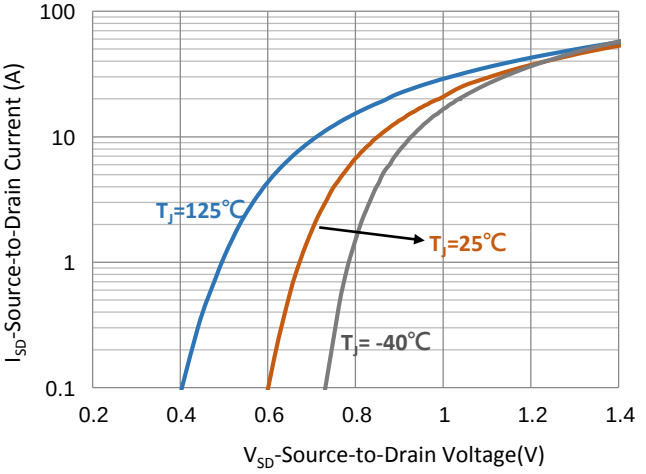


Fig.6 Source-Drain Diode Forward Voltage

TYPICAL CHARACTERISTIC CURVES

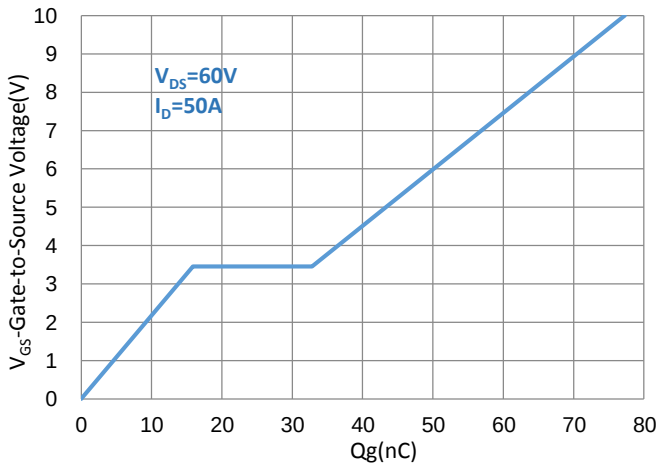


Fig.7 Gate-Charge Characteristics

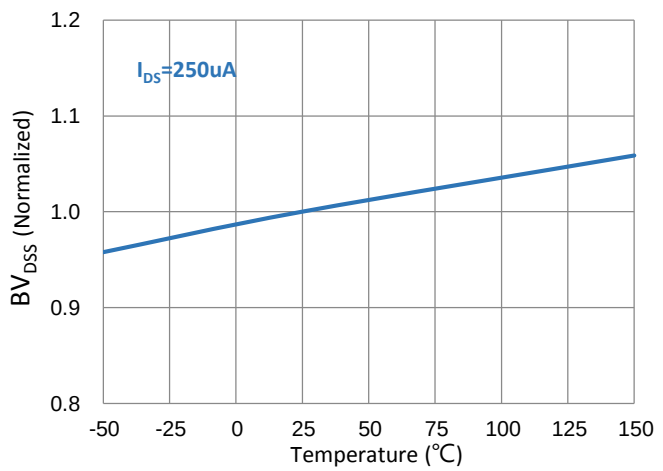


Fig.8 Breakdown Voltage Variation vs. Temperature

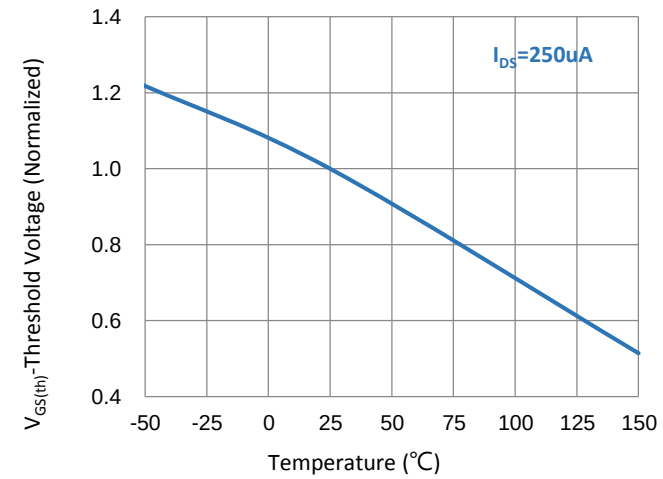


Fig.9 Threshold Voltage Variation with Temperature

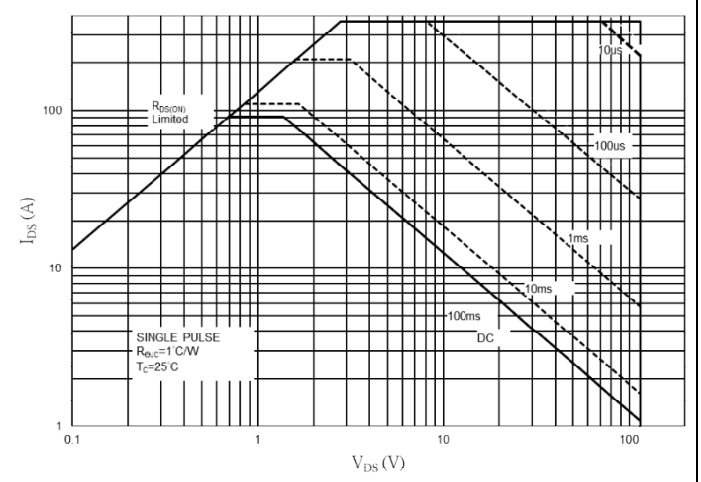


Fig.10 Maximum Safe Operating Area

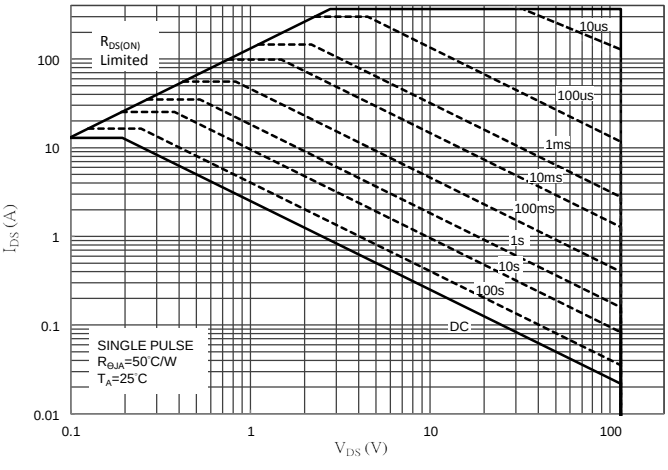


Fig.11 Maximum Safe Operating Area

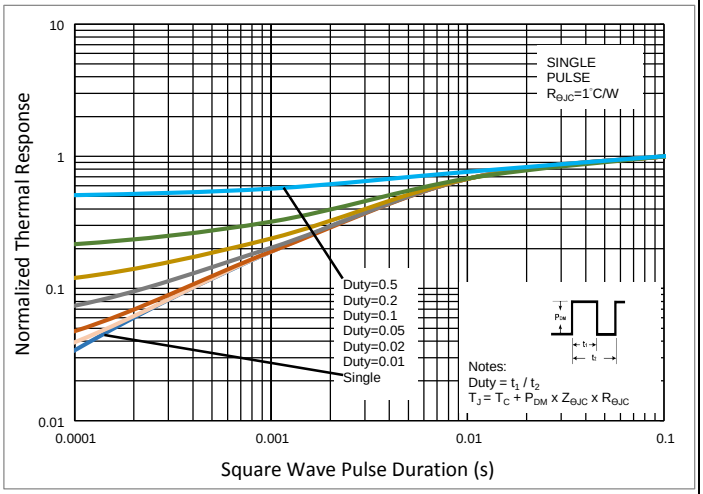


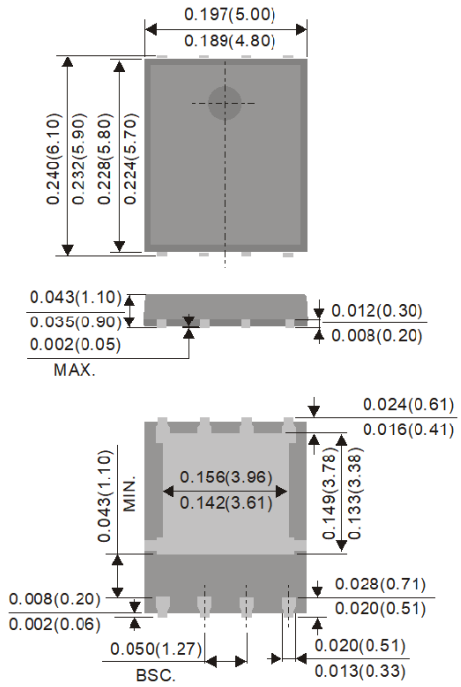
Fig.12 Normalized Transient Thermal Impedance

Product and Packing Information

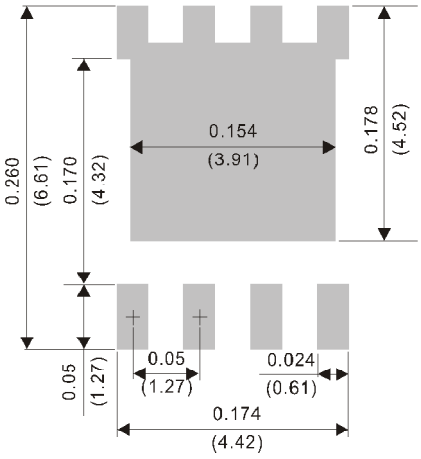
Part No.	Package Type	Packing Type	Marking
PSMQC076N12LS1	DFN5060-8L	3000pcs / 13” reel	076N12LS

Packaging Information & Mounting Pad Layout

DFN5060-8L Dimension — Unit: inch(mm)



DFN5060-8L Pad Layout — Unit: inch(mm)



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