



### 40V N-Channel Enhancement Mode MOSFET

Voltage

40 V

Current

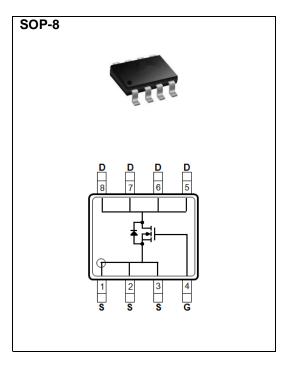
6 A

#### **Features**

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_{D}@5A<32m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_{D}@3A<44m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### **Mechanical Data**

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



# $\textbf{Maximum Ratings and Thermal Characteristics} \; (T_A = 25 ^{\circ} \text{C unless otherwise noted})$

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		$V_{DS}$	40		
Gate-Source Voltage		$V_{GS}$	<u>+</u> 20	V	
Continuous Drain Current	T <sub>A</sub> =25°C		6		
	T <sub>A</sub> =70°C	I <sub>D</sub>	4.8	А	
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	24		
Power Dissipation	T <sub>A</sub> =25°C		2.1	W	
	T <sub>A</sub> =70°C	$P_{D}$	1.3		
Operating Junction and Storage Temperature Range		$T_{J}$ , $T_{STG}$	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 5)		R <sub>θJA</sub>	59.5	°C/W	





# **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	$BV_{DSS}$	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250uA	40	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$		1.2	1.8	2.5			
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS}$ =10 $V$ , $I_D$ =5 $A$	-	27.5	32	mΩ		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V,I <sub>D</sub> =3A	-	35.5	44			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1.0	uA		
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V,V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA		
Dynamic (Note 6)								
Total Gate Charge	$Q_g$	V <sub>DS</sub> =20V, I <sub>D</sub> =5A, V <sub>GS</sub> =4.5V <sup>(Note 3)</sup>	-	4.4	-	nC		
Gate-Source Charge	$Q_gs$		-	1.3	-			
Gate-Drain Charge	$Q_gd$		-	1.7	-			
Input Capacitance	Ciss		-	425	-	pF		
Output Capacitance	Coss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	48	-			
Reverse Transfer Capacitance	Crss	I=1.UIVIMZ	-	36	-			
Turn-On Delay Time	td <sub>(on)</sub>	\/ 00\/ I 4A	-	9.4	-	ns		
Turn-On Rise Time	tr	V <sub>DD</sub> =20V, I <sub>D</sub> =1A,	-	29	-			
Turn-Off Delay Time	td <sub>(off)</sub>	$V_{GS}$ =4.5V, $R_{G}$ =25 $\Omega$ (Note 3)	-	21	-			
Turn-Off Fall Time	tf		-	29	-			
Drain-Source Diode								
Maximum Continuous Drain-Source	_		-	-	6	А		
Diode Forward Current	I <sub>S</sub>							
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.74	1	V		

#### NOTES:

- 1. Pulse width<a>300us</a>, Duty cycle<a>2%</a>.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





### TYPICAL CHARACTERISTIC CURVES

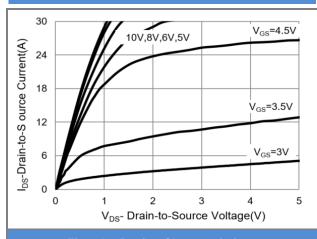


Fig.1 On-Region Characteristics

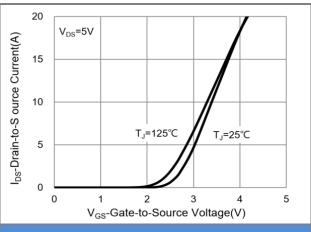


Fig.2 Transfer Characteristics

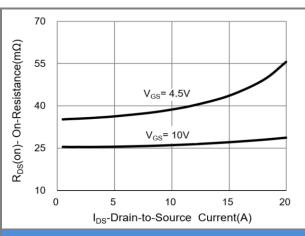


Fig.3 On-Resistance vs. Drain Current

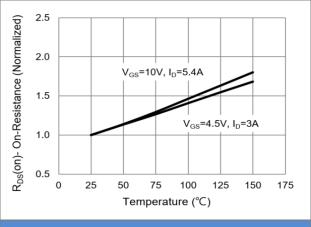


Fig.4 On-Resistance vs. Junction temperature

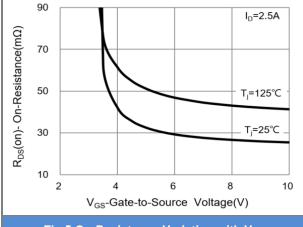
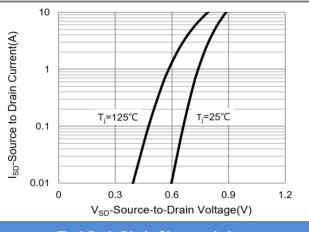


Fig.5 On-Resistance Variation with V<sub>GS</sub>



**Fig.6 Body Diode Characteristics** 





### **TYPICAL CHARACTERISTIC CURVES**

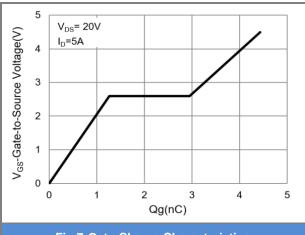


Fig.7 Gate-Charge Characteristics

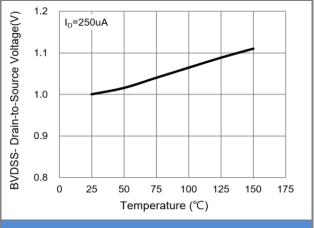


Fig.8 Breakdown Voltage Variation vs. Temperature

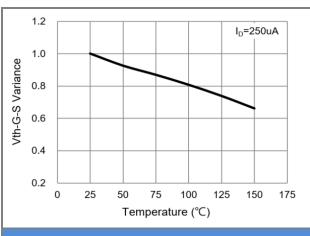


Fig.9 Threshold Voltage Variation with Temperature

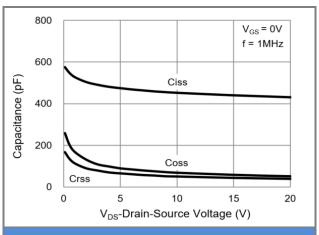


Fig.10 Capacitance vs. Drain-Source Voltage

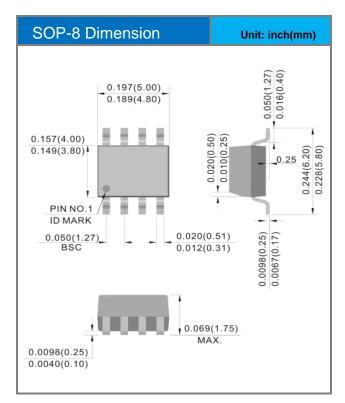


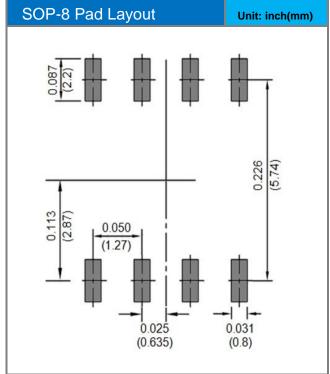


### **Part No Packing Code Version**

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9428_R2_00001	SOP-8	2.5K pcs / 13" reel	L9428	Halogen free

### **Packaging Information & Mounting Pad Layout**









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