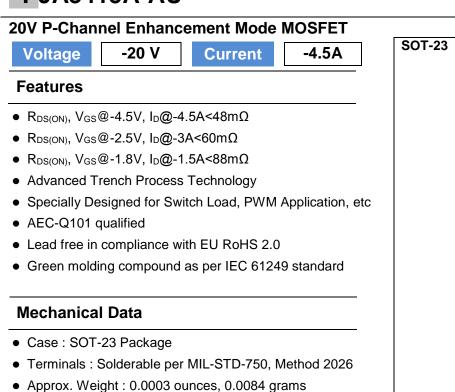
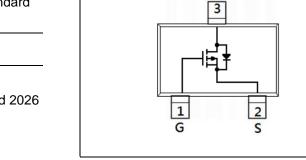
PAN	JIT
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D

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-20	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 12		
Continuous Drain Current ^(Note 4)		ID	-4.5	A	
Pulsed Drain Current ^(Note 1)		I _{DM}	-18		
Power Dissipation	T _a =25°C	PD	1.25	W	
	Derate above 25°C		10	mW/°C	
Operating Junction and Storage Temperature Range		Tյ,Tsтg	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 3,4)		Reja	100	°C/W	



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V, I _D =-250uA	-20	-	-	- V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-0.74	-1.3	
Drain-Source On-State Resistance	RDS(on)	V _{GS} =-4.5V, I _D =-4.5A	-	40	48	mΩ
		V _{GS} =-2.5V, I _D =-3A	-	50	60	
		V _{GS} =-1.8V, I _D =-1.5A	-	75	88	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-16V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic ^(Note 5)						
Total Gate Charge	Qg	V _{DS} =-10V, I _D =-4.5A, V _{GS} =-4.5V ^(Note 1,2)	-	10	-	
Gate-Source Charge	Q_{gs}		-	1.7	-	nC
Gate-Drain Charge	Q_{gd}		-	2.4	-	
Input Capacitance	Ciss		-	980	-	
Output Capacitance	Coss	V _{DS} =-10V, V _{GS} =0V, f=1MHZ	-	100	-	pF
Reverse Transfer Capacitance	Crss		-	81	-	
Turn-On Delay Time	td _(on)		-	9.8	-	
Turn-On Rise Time	tr	V_{DD} =-10V, I _D =-4.5A, V _{GS} =-4.5V, R _G =6 $\Omega^{(Note 1,2)}$	-	54	-	
Turn-Off Delay Time	td _(off)		-	44	-	ns
Turn-Off Fall Time	tf		-	31	-]
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-1.5	A
Diode Forward Voltage	V _{SD}	Is=-1A, V _{GS} =0V	-	-0.78	-1.2	V

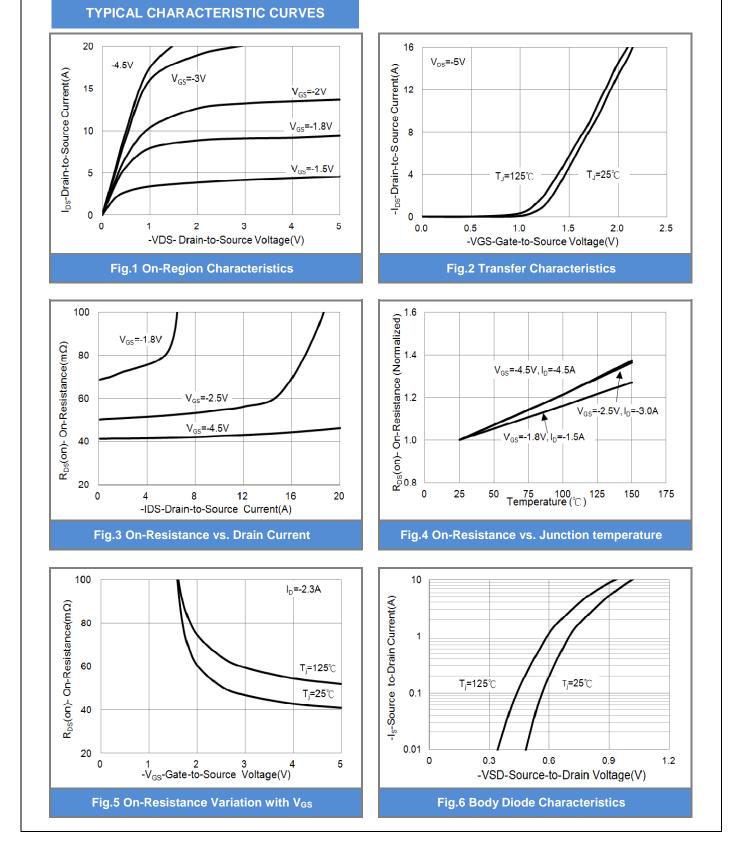
NOTES :

- 1. Pulse width<300us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

September 21,2020

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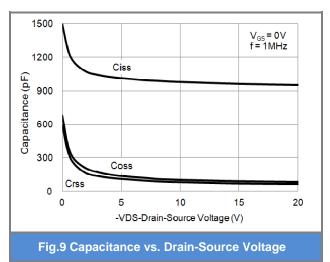
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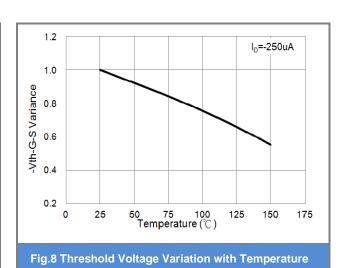




PJA3415A-AU

PJA3415A-AU TYPICAL CHARACTERISTIC CURVES 5 V_{DS}=-10V I_D=-4.5A -Gate-Source Voltage-V_{GS}(Volts) 4 3 2 1 0 3 9 0 6 12 Qg(nC) Fig.7 Gate-Charge Characteristics







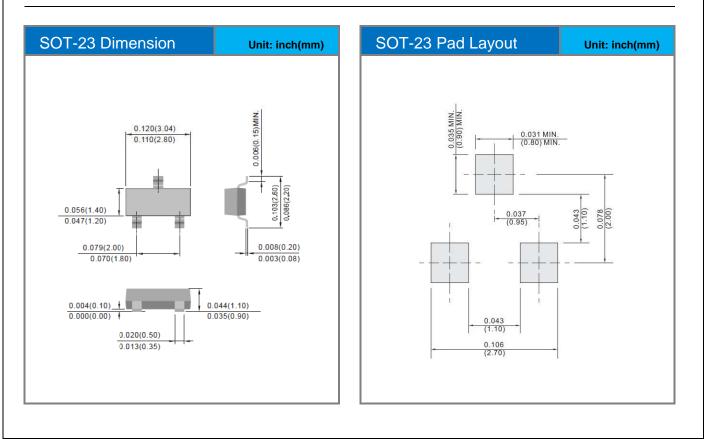




Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJA3415A-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A5A	Halogen free

Packaging Information & Mounting Pad Layout





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