



20V N-Channel Enhancement Mode MOSFET - ESD Protected

Voltage 20 V Current 800 mA

Features

- R_{DS(ON)}, V_{GS}@4.5V,I_{DS}@500mA=0.4Ω
- R_{DS(ON)}, V_{GS}@2.5V,I_{DS}@300mA=0.7Ω
- $R_{DS(ON)}$, $V_{GS}@1.8V$, $I_{DS}@100mA=1.2\Omega(typ)$
- Advanced Trench Process Technology
- Specially Designed for Load Switch or PWM application.
- ESD Protected
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

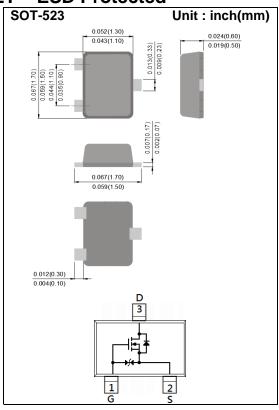
Mechanical Data

Case: SOT-523 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.002 grams

Marking: E06



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _G s	<u>+</u> 12	V
Continuous Drain Current		ΙD	800	mA
Pulsed Drain Current		I _{DM}	3000	mA
Power Dissipation	T _A =25°C	P _D	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance				
- Junction to Ambient ^(Note 3)		R _{θJA}	357	°C/W





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250uA	20	-	-	٧
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} ,I _D =250uA	0.4	0.63	1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V,I _D =500mA	-	0.35	0.4	Ω
		V _{GS} =2.5V,I _D =300mA	-	0.6	0.7	
		V _{GS} =1.8V,I _D =100mA	-	1.2	-	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =16V,V _{GS} =0V	-	0.02	1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 4.5V,V _{DS} =0V	-	<u>+</u> 0.05	<u>+</u> 1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 10V,V _{DS} =0V	-	<u>+</u> 2	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Q_g	V _{DS} =10V, I _D =500mA, V _{GS} =4.5V ^(Note 1,2)	-	0.92	-	nC
Gate-Source Charge	Q_{gs}		-	0.31	-	
Gate-Drain Charge	Q_gd		-	0.08	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V,	-	50	-	pF
Output Capacitance	Coss		-	10	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	8.5	-	
Switching						
Turn-On Delay Time	td _(on)	\/ 40\/ 500 · A	-	4	-	ns
Turn-On Rise Time	tr	$\begin{array}{c} V_{\text{DD}}{=}10\text{V, I}_{\text{D}}{=}500\text{mA,} \\ V_{\text{GS}}{=}4.5\text{V,} \\ R_{\text{G}}{=}6\Omega^{(\text{Note 1,2})} \end{array}$	-	20	-	
Turn-Off Delay Time	td _(off)		-	12	-	
Turn-Off Fall Time	tf		-	25	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	500	mA
Diode Forward Voltage	V _{SD}	Is=500mA, V _{GS} =0V	-	0.91	1.3	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper





TYPICAL CHARACTERISTIC CURVES

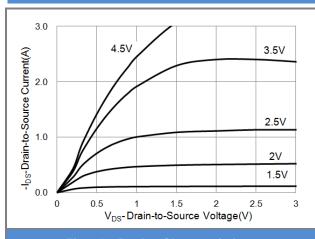


Fig.1 On-Region Characteristics

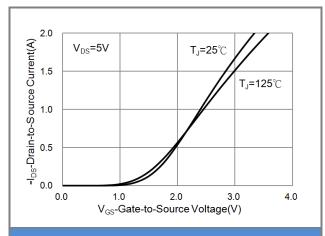


Fig.2 Transfer Characteristics

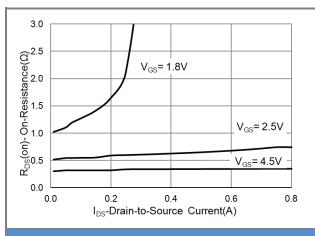


Fig.3 On-Resistance vs. Drain Current

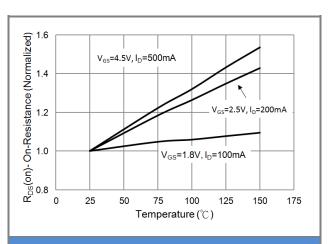


Fig.4 On-Resistance vs. Junction temperature

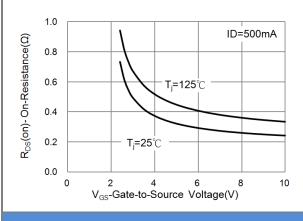
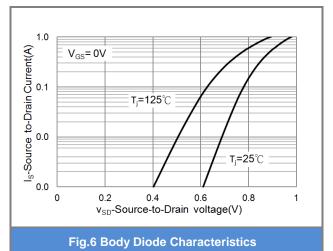


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

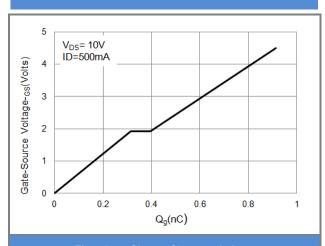


Fig.7 Gate-Charge Characteristics

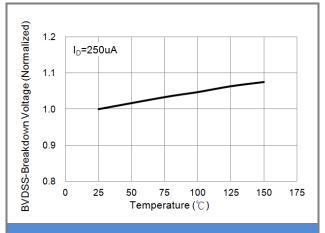


Fig.8 Breakdown Voltage Variation vs. Temperature

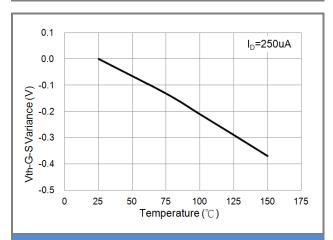


Fig.9 Threshold Voltage Variation with Temperature.

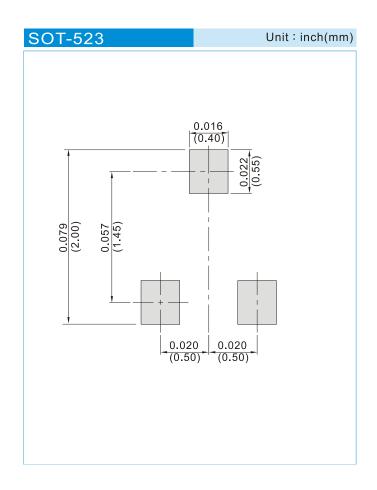




PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJE8406_R1_00001	SOT-523	4K pcs / 7" reel	E06	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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