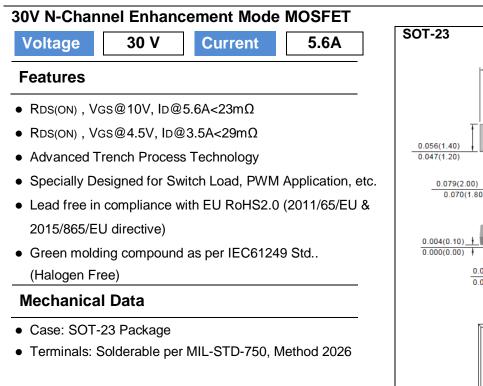
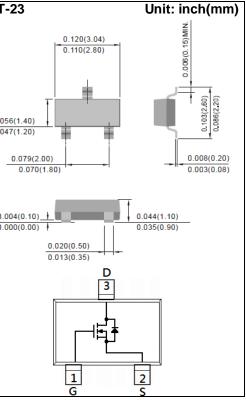
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PJA3404A





Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage	V _{GS}	<u>+</u> 20	V	
Continuous Drain Current		I _D	5.6	А
Pulsed Drain Current		I _{DM}	22	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	100	°C/W

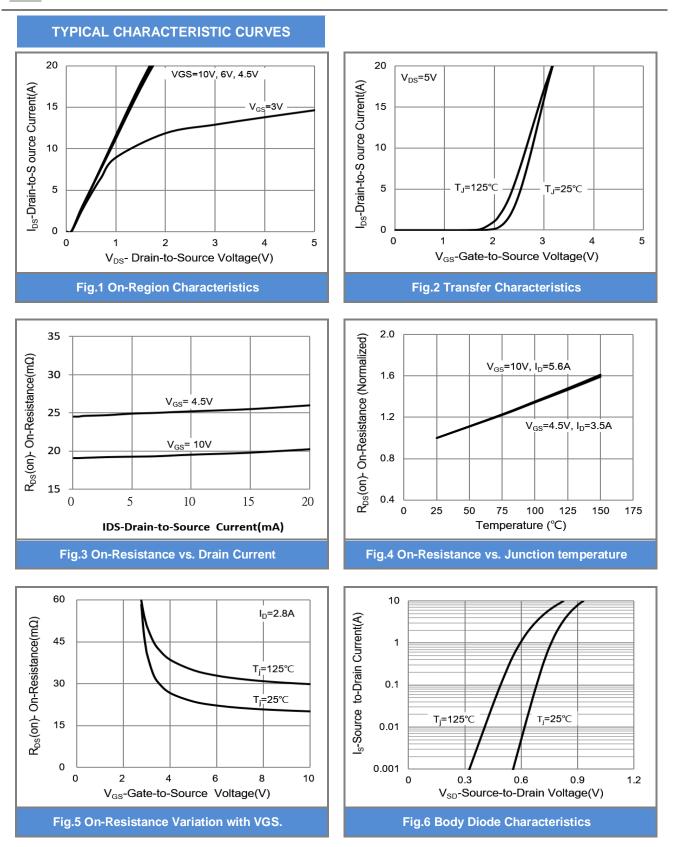


Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} =0V, I _D =250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	1.0	1.57	2.1	V
Drain-Source On-State Resistance	-	V_{GS} =10V, I_{D} =5.6A	-	20	23	mΩ
	$R_{DS(on)}$	V _{GS} =4.5V, I _D =3.5A	-	24	29	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Q_{g}		-	12.8	-	
Gate-Source Charge	Q_{gs}	V_{DS} =15V, I _D =5.6A, V_{GS} =10V ^(Note 1,2)	-	1.6	-	nC
Gate-Drain Charge	Q_gd		-	2.5	-	
Input Capacitance	Ciss	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	602	-	_
Output Capacitance	Coss		-	90	-	pF
Reverse Transfer Capacitance	Crss		-	67	-	
Turn-On Delay Time	td _(on)	V_{DD} =15V, I _D =5.6A, V_{GS} =10V, R_{G} =3 Ω ^(Note 1,2)	-	4.7	-	_
Turn-On Rise Time	tr		-	34	-	
Turn-Off Delay Time	td _(off)		-	15	-	ns
Turn-Off Fall Time	tf		-	17	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					1.5	^
Diode Forward Current	I _S		-	-	1.5	A
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	0.76	1.2	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing



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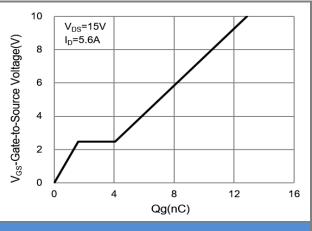
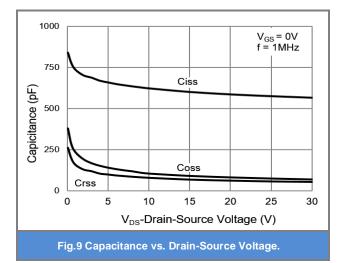
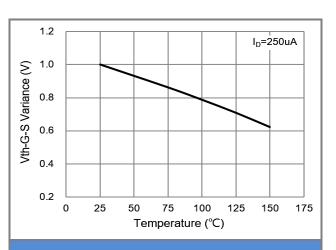


Fig.7 Gate-Charge Characteristics







PJA3404A



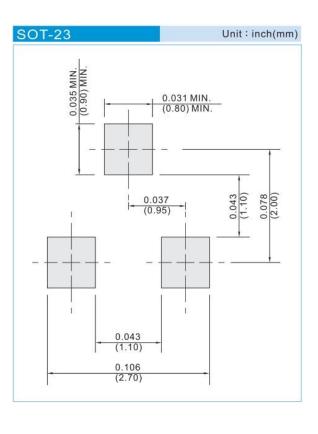




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3404A_R1_00001	SOT-23	3K pcs / 7" reel	A4A	Halogen free
PJA3404A_R2_00001	SOT-23	12K pcs / 13" reel	A4A	Halogen free

MOUNTING PAD LAYOUT





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