ΡΛΝ	JIT
	SEMI
	CONDUCTOR

#### 30V N-Channel Enhancement Mode MOSFET

Current

60 A

#### Features

Voltage

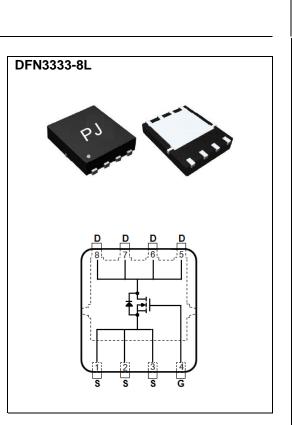
• R<sub>DS(ON)</sub> , V<sub>GS</sub>@10V, I<sub>D</sub>@10A<6mΩ

30 V

- R<sub>DS(ON)</sub>, V<sub>GS</sub>@4.5V, I<sub>D</sub>@8A<9mΩ
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : DFN3333-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.001 ounces, 0.03 grams



#### **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25<sup>o</sup>C unless otherwise noted)

PARAMETE	R	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	30		
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20		
	Tc=25°C		60		
Continuous Drain Current	Tc=100°C	I <sub>D</sub>	38	А	
Pulsed Drain Current <sup>(Note 1)</sup>	Tc=25°C	I <sub>DM</sub>	240		
Power Dissipation	Tc=25°C	6	31	14/	
	Tc=100°C	PD	12.4	W	
	T <sub>A</sub> =25°C		15	А	
Continuous Drain Current	T <sub>A</sub> =70°C	ID	12	А	
Power Dissipation	T <sub>A</sub> =25°C	6	2.0	14/	
Power Dissipation	T <sub>A</sub> =70°C	PD	1.3	W	
Operating Junction and Storage Temperature Range		TJ,T <sub>STG</sub>	-55~150	°C	
	Junction to Case	$R_{ extsf{ heta}JC}$	4.0	00444	
Typical Thermal Resistance <sup>(Note 4,5)</sup>	Junction to Ambient	R <sub>θJA</sub>	62.5	•C/W	

• Limited only By Maximum Junction Temperature



#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250uA	1.0	1.6	2.5	V
Drain-Source On-State Resistance	5	V <sub>GS</sub> =10V,I <sub>D</sub> =10A	-	5	6	
	RDS(on)	V <sub>GS</sub> =4.5V,I <sub>D</sub> =8A	-	6.6	9	mΩ
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1.0	uA
Gate-Source Leakage Current	lgss	V <sub>GS</sub> = <u>+</u> 20V,V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic <sup>(Note 6)</sup>						
Total Gate Charge	Qg		-	12	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=15V, I_{D}=20A,$	-	3.8	-	
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =4.5V <sup>(Note 1,2)</sup>	-	4.3	-	
Input Capacitance	Ciss		-	1323	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	219	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	136	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DS</sub> =15V,RL=1Ω,	-	5.0	-	
Turn-On Rise Time	tr	V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω	-	42	-	-
Turn-Off Delay Time	td <sub>(off)</sub>	(Note 2,3)	-	36	-	ns
Turn-Off Fall Time	tr		-	5.5	-	
Drain-Source Diode	·	·	<u> </u>	·		
Maximum Continuous Drain-Source	Is		-	-	60	А
Diode Forward Current Diode Forward Voltage	V <sub>SD</sub>	Is=1A,V <sub>GS</sub> =0V	-	0.83	1	V

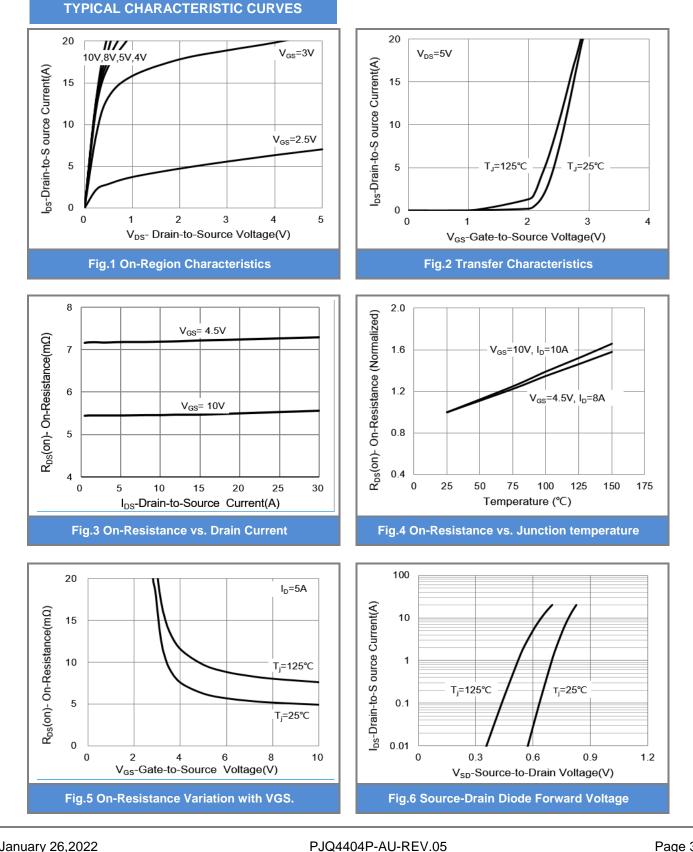
NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics
- Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4. The maximum current rating is package limited
- 5.  $R_{\Theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper
- 6. Guaranteed by design, not subject to production testing.

January 26,2022

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### Page 3



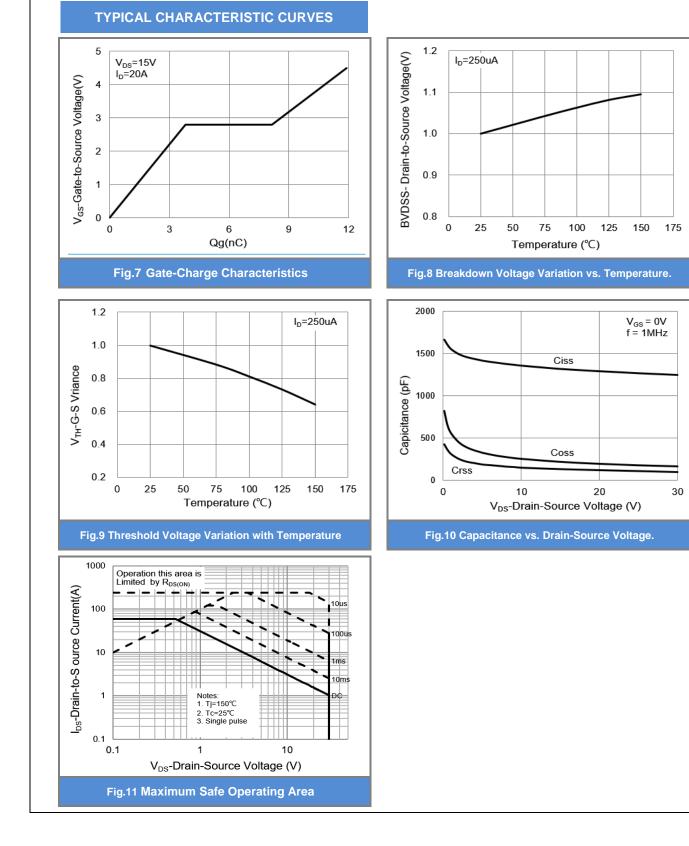


PJQ4404P-AU

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Page 4

PJC	244	04P	-AU					
Т	YPICA			CURVES				
Z <sub>TH-JC</sub> Normalized Transient Thermal Impedance	0.1	D=0.5 0.2 0.1 0.06 0.05	Single Pulse				$T_{J,PK} = T_{C} + P_{DM} * Z_{TF}$ $R_{TH-JC} = 4.0^{\circ}C/W$ $T_{C} = 25^{\circ}C$ $P_{CM}$	
Z <sub>TH-</sub>	0.0	0001	0.0001	0.001 t , P	0.01 ulse Width	0.1	1	
			Fig.12 Normaliz	ed Transient T	hermal Imped	dance vs. Pu	Ilse Width	



10

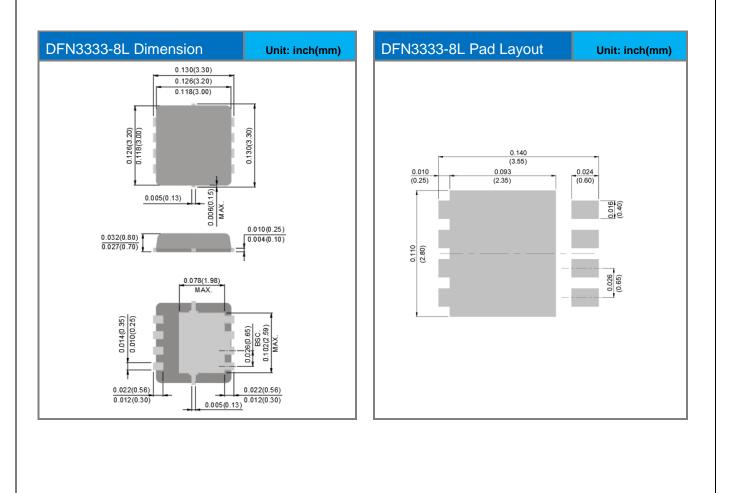




#### Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ4404P-AU_R2_000A1	DFN3333-8L	5K pcs / 13" reel	4404	Halogen free RoHS compliant

## **Packaging Information & Mounting Pad Layout**





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