



30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

-4.6 A

Features

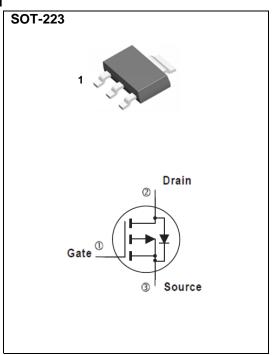
- $R_{DS(ON)}$, V_{GS} @-10V, I_D @-3A<50m Ω
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_{D}@-2A<80m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: SOT-223 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• SOT-223 Approx. Weight: 0.043 ounces, 0.123grams



$\textbf{Maximum Ratings and Thermal Characteristics} \; (T_A = 25 ^{\circ} \text{C unless otherwise noted})$

| PARAMETER | | SYMBOL | LIMIT | UNITS | |
|---|----------------------|-----------------|-------------|-------|--|
| Drain-Source Voltage | | V _{DS} | -30 | _ v | |
| Gate-Source Voltage | | V_{GS} | <u>+</u> 20 | | |
| Continuous Drain Current | T _A =25°C | | -4.6 | | |
| | T _A =70°C | I _D | -3.7 | Α | |
| Pulsed Drain Current (Note 1) | | I _{DM} | -20 | | |
| Power Dissipation | T _A =25°C | P _D | 1.6 | \A/ | |
| | T _A =70°C | | 1.0 | W | |
| Operating Junction and Storage Temperature Range | | T_J, T_{STG} | -55~150 | °C | |
| Typical Thermal Resistance - Junction to Ambient (Note 5) | | $R_{\theta JA}$ | 78.1 | °C/W | |

• Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25 °C unless otherwise noted)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNITS |
|----------------------------------|---------------------|---|------|-------|--------------|-------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =-250uA | -30 | - | - | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}$, $I_{D}=-250uA$ | -1 | -1.6 | -2.5 | |
| Drain-Source On-State Resistance | R _{DS(on)} | V_{GS} =-10V, I_D =-3A | - | 40 | 50 | mΩ |
| | | V _{GS} =-4.5V, I _D =-2A | - | 60 | 80 | |
| Zero Gate Voltage Drain Current | I_{DSS} | V _{DS} =-30V, V _{GS} =0V | - | - | -1 | uA |
| Gate-Source Leakage Current | I_{GSS} | V _{GS} = <u>+</u> 20V, V _{DS} =0V | - | - | <u>+</u> 100 | nA |
| Dynamic (Note 6) | | | | | | |
| Total Gate Charge | Q_{g} | V _{DS} =-15V, I _D =-3A, V _{GS} =-4.5V ^(Note 1,2) | - | 4.8 | - | nC |
| Gate-Source Charge | Q_gs | | - | 1.7 | - | |
| Gate-Drain Charge | Q_{gd} | | - | 1.7 | - | |
| Input Capacitance | Ciss | V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ | - | 516 | - | pF |
| Output Capacitance | Coss | | - | 83 | - | |
| Reverse Transfer Capacitance | Crss | I=1.0IVINZ | - | 61 | - | |
| Turn-On Delay Time | td _(on) | \/ 45\/ \ | - | 5.6 | - | ns |
| Turn-On Rise Time | t _r | V_{DS} =-15V, I_{D} =-1A, V_{GEN} =-10V, R_{G} =6 Ω (Note 1.2) | - | 8.5 | - | |
| Turn-Off Delay Time | td _(off) | | - | 27 | - | |
| Turn-Off Fall Time | t _f | | - | 18 | - | |
| Drain-Source Diode | | | | | | |
| Maximum Continuous Drain-Source | | | - | - | -4.6 | А |
| Diode Forward Current | I _S | | | | | |
| Diode Forward Voltage | V_{SD} | I _S =-1A, V _{GS} =0V | - | -0.76 | -1 | V |

NOTES:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 5. R@JA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.



80

60

40

20

0

0

 $R_{DS}(on)$ - On-Resistance(m Ω)



PJW5P03

TYPICAL CHARACTERISTIC CURVES

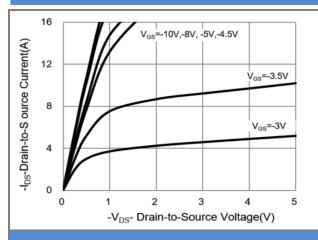
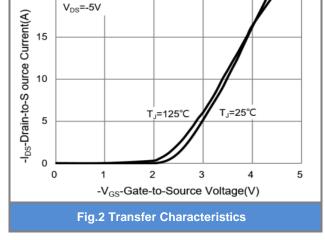


Fig.1 Output Characteristics

V_{GS}=-4.5V

V_{GS}=-10V



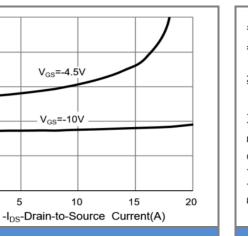


Fig.3 On-Resistance vs. Drain Current

10

5

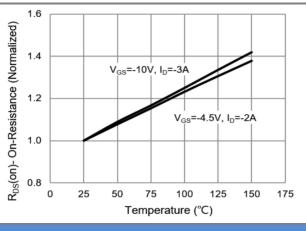
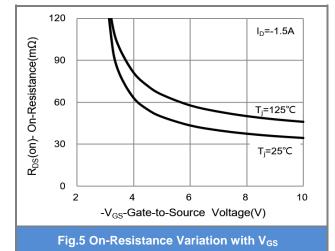


Fig.4 On-Resistance vs. Junction temperature



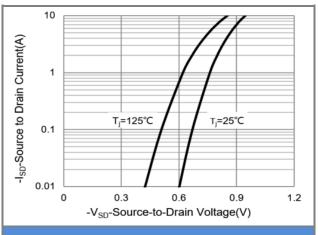


Fig.6 Source-Drain Diode Forward Voltage





TYPICAL CHARACTERISTIC CURVES

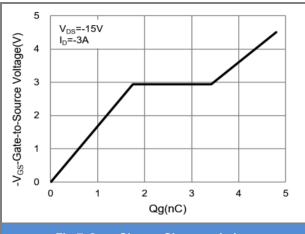


Fig.7 Gate-Charge Characteristics

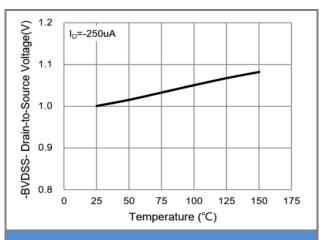


Fig.8 Breakdown Voltage Variation vs. Temperature

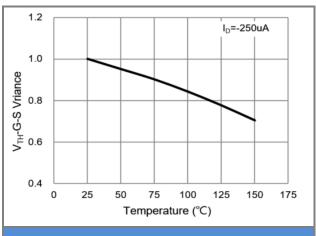


Fig.9 Threshold Voltage Variation with Temperature

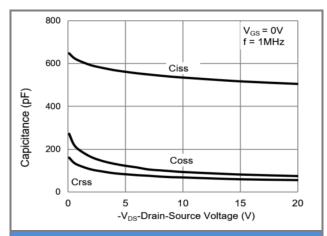
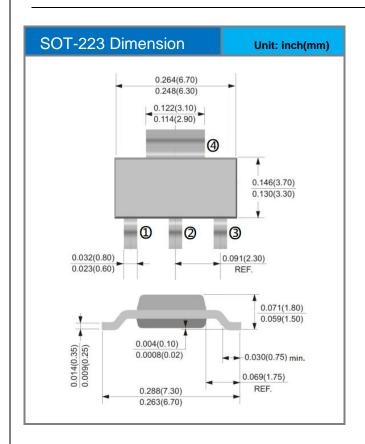


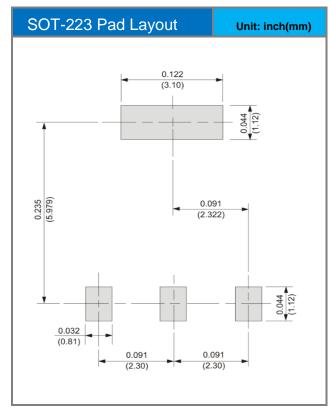
Fig.10 Capacitance vs. Drain-Source Voltage





Packaging Information & Mounting Pad Layout









Part No Packing Code Version

| Part No Packing Code | Package Type | Packing Type | Marking | Version | |
|----------------------|--------------|---------------------|---------|--------------|--|
| PJW5P03_R2_00001 | SOT-223 | 2,500pcs / 13" reel | W5P03 | Halogen free | |





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