



# PJQ2461

## 60V P-Channel Enhancement Mode MOSFET

**Voltage** -60 V **Current** -2.4 A

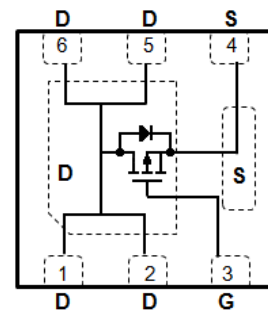
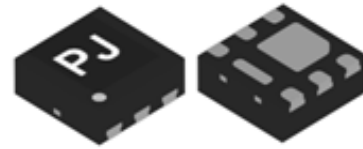
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@-10V$ ,  $I_D@-2A < 190m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@-4.5V$ ,  $I_D@-1.5A < 240m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

DFN2020B-6L



## Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	+20	
Continuous Drain Current (Note 4)	$I_D$	$T_A=25^\circ\text{C}$	-2.4
		$T_A=70^\circ\text{C}$	-1.9
Pulsed Drain Current (Note 1)	$I_{DM}$	-9.6	A
Power Dissipation	$P_D$	$T_A=25^\circ\text{C}$	2
		$T_A=70^\circ\text{C}$	1.3
Single Pulse Avalanche Energy (Note 6)	$E_{AS}$	32	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	$^\circ\text{C}$
Typical Thermal Resistance	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
- Junction to Ambient (Note 4,5)			



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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.88	-2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2A	-	140	190	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.5A	-	190	240	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>Dynamic</b> (Note 7)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-30V, I <sub>D</sub> =-2A, V <sub>GS</sub> =-10V (Note 1,2)	-	8.3	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.8	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.6	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, f=1MHZ	-	430	-	pF
Output Capacitance	C <sub>oss</sub>		-	33	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	29	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-30V, I <sub>D</sub> =-1A, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω (Note 1,2)	-	5.1	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	20	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	36	-	
Turn-Off Fall Time	t <sub>f</sub>		-	11	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	-1.5	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.78	-1	V

**NOTES :**

1. Pulse width ≤ 300us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> = 25°C.
4. The maximum current rating is package limited.
5. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. The test condition is L=1mH, I<sub>AS</sub>=-8A, V<sub>DD</sub>=-25V, V<sub>GS</sub>=-10V
7. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES

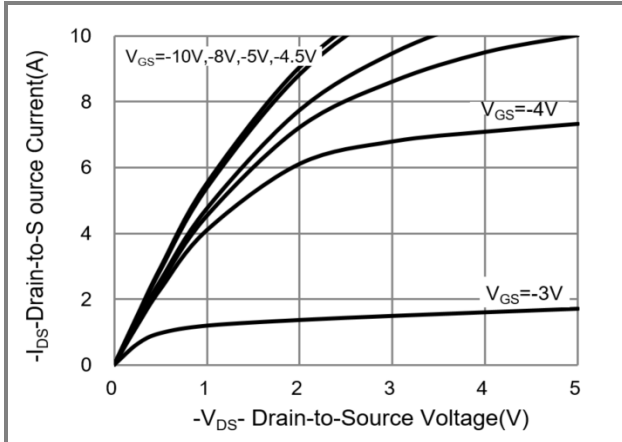


Fig.1 On-Region Characteristics

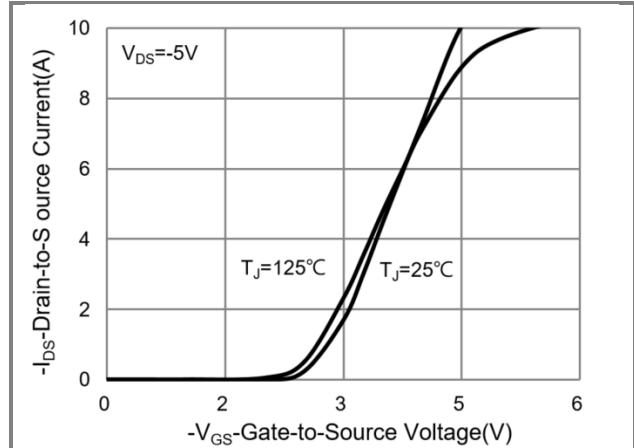


Fig.2 Transfer Characteristics

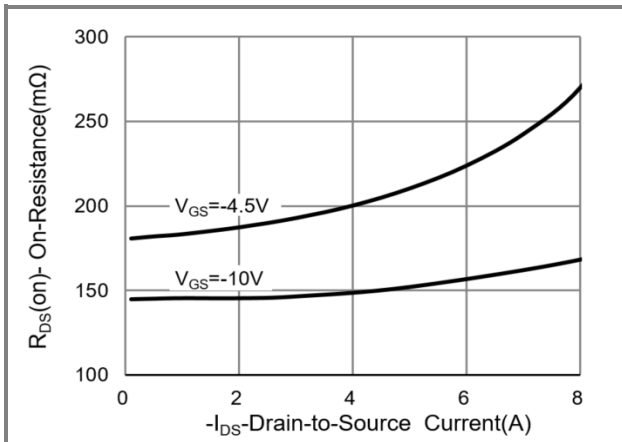


Fig.3 On-Resistance vs. Drain Current

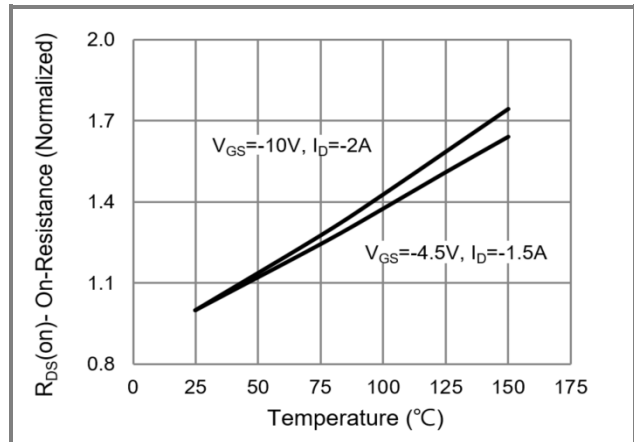


Fig.4 On-Resistance vs. Junction temperature

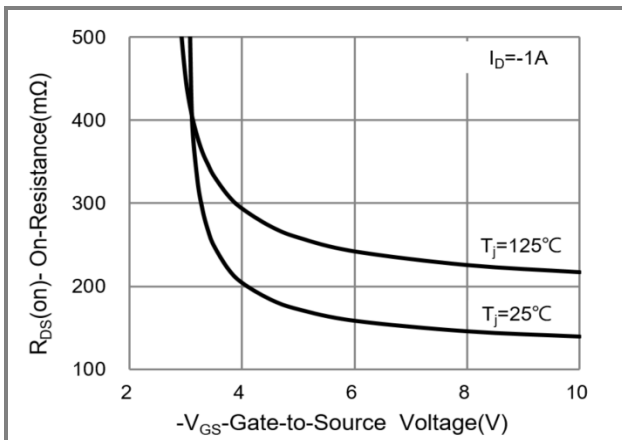


Fig.5 On-Resistance Variation with  $V_{G_S}$

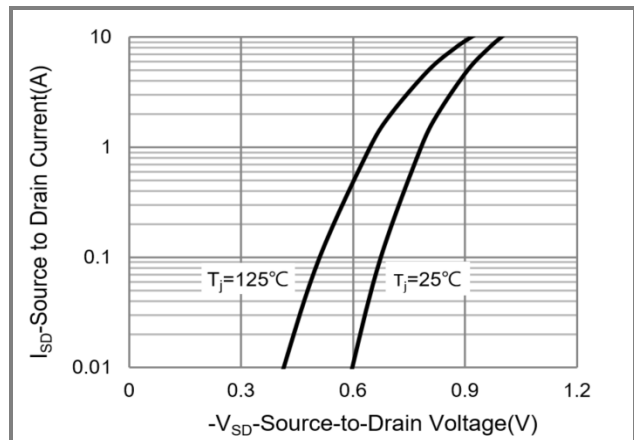


Fig.6 Body Diode Characteristics



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## TYPICAL CHARACTERISTIC CURVES

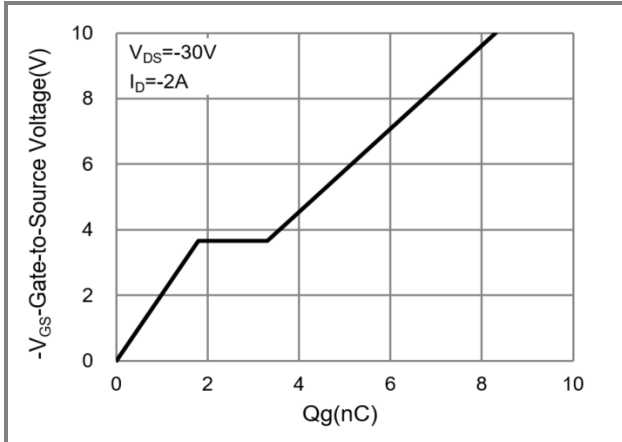


Fig.7 Gate-Charge Characteristics

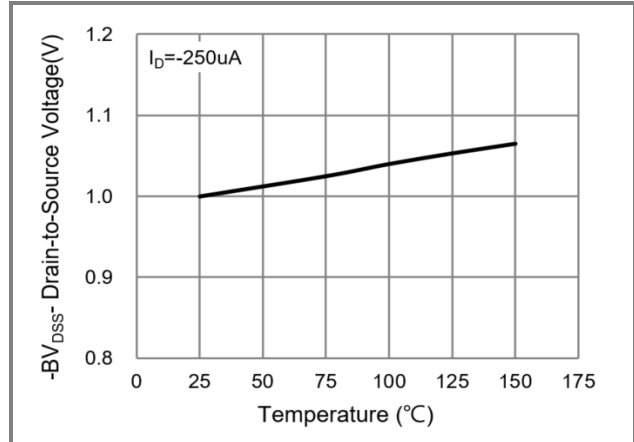


Fig.8 Breakdown Voltage Variation vs. Temperature

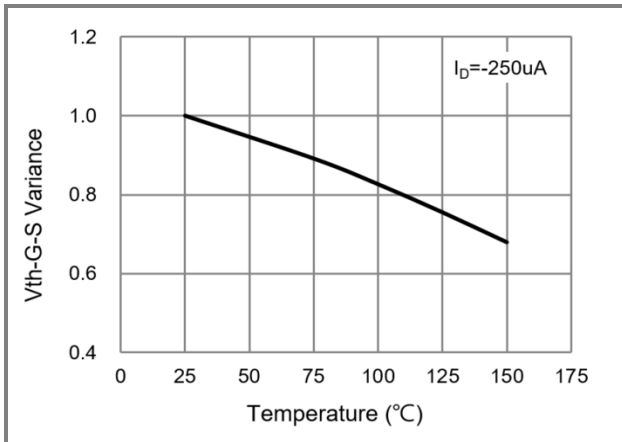


Fig.9 Threshold Voltage Variation with Temperature

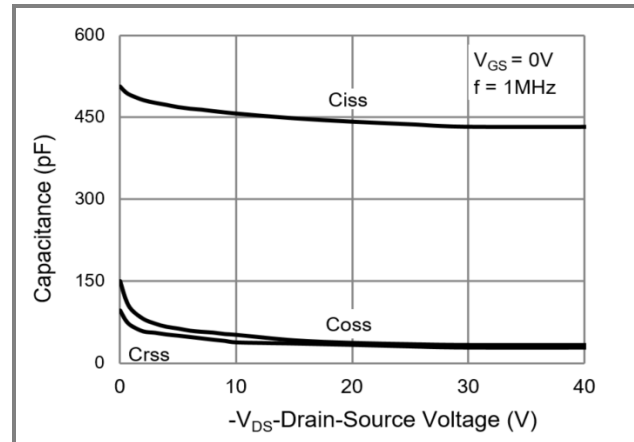


Fig.10 Capacitance vs. Drain-Source Voltage

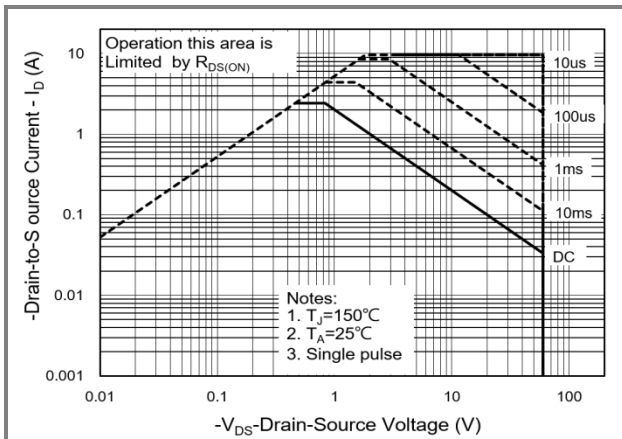


Fig.11 Maximum Safe Operating Area.

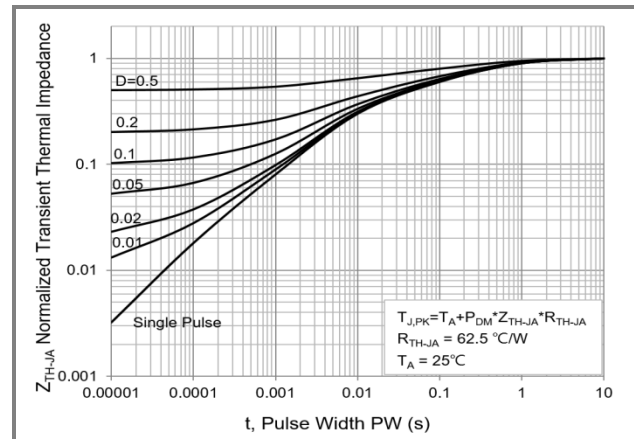


Fig.12 Normalized Transient Thermal Impedance

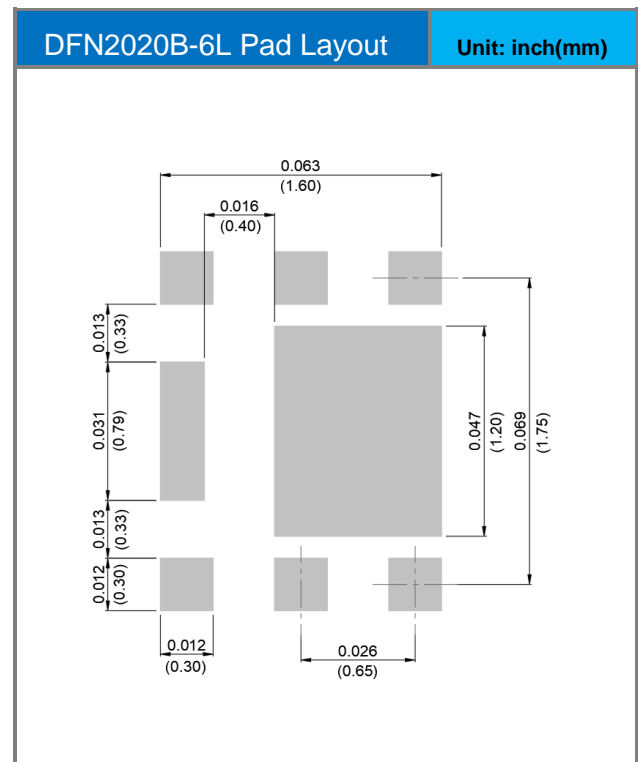
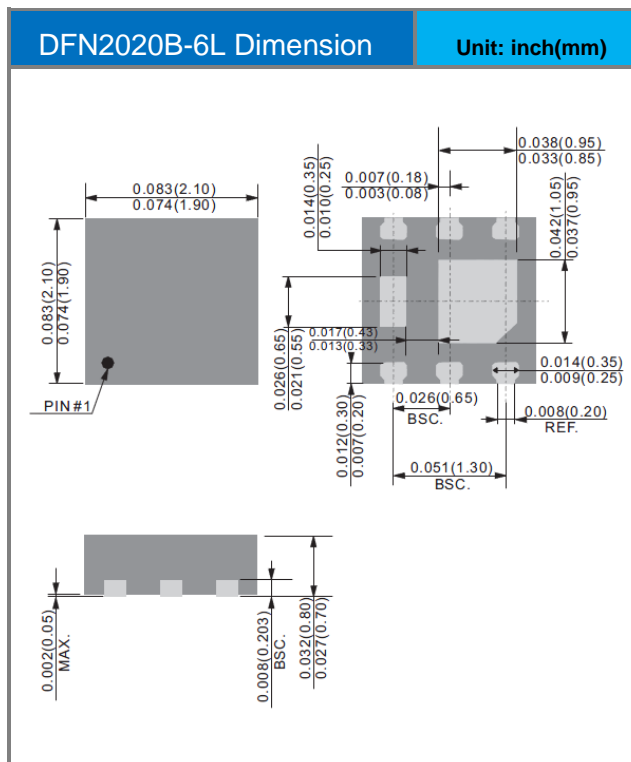


# PJQ2461

## Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2461_R1_00001	DFN2020B-6L	3K pcs / 7" reel	461	Halogen free RoHS compliant

## Packaging Information & Mounting Pad Layout





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