 Advanced Trench Process Technology Specially Designed for Switch Load, PWM Application, etc. AEC-Q101 qualified Lead free in compliance with EU RoHS 2.0 Green molding compound as per IEC 61249 standard 	0.119(3.00) 0.110(2.80) 0.075(1.90) BSC 0.075(1.90) 0.020(0.50) 0.012(0.30)
Mechanical Data	657(1.45) MAX.
 Case : SOT-23 6L Package Terminals : Solderable per MIL-STD-750, Method 2026 Approx. Weight : 0.0005 ounces, 0.014 grams 	

4.1 /-3.1A

Mecha

- Case
- Termi

20V Complementary Enhancement Mode MOSFET

Current

Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	N-Ch LIMIT	P-Ch LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	20	-20	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 12	<u>+</u> 12		
Continuous Drain Current (Note 4)		I _D	4.1	-3.1		
Pulsed Drain Current (Note 1)		I _{DM}	16.4	-12.4	A	
Power Dissipation	T _a =25°C		1.25 10		W	
	Derate above 25°C				mW/°C	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150		°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 3,4)		R _{θJA}	100		100	



Voltage

Features

PJS6601-AU

20 / -20V

Unit: inch(mm)

0.01(0.25) BSC

0.009(0.22)

GAUGE PLANE SEATING PLANE

059(1.50) 19(3.00)

0.051(1.30)

0.006(0.15) MAX.

S1

5

2

S2

1 G1 D2

4

3

G2

SOT-23 6L





N-Channel Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	0.4	0.66	1.2	V
		V _{GS} =4.5V, I _D =4.1A	-	41	56	mΩ
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =2.5V, I _D =2.8A	-	50	68	
		V _{GS} =1.8V, I _D =1.5A	-	66	95	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Qg		-	4.6	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =10V, I _D =4.1A, V _{GS} =4.5V ^(Note 1,2)	-	0.8	-	
Gate-Drain Charge	Q _{gd}	$-V_{GS}=4.5V$ (Note 1,2)	-	1	-	
Input Capacitance	Ciss		-	350	-	pF
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V, f=1MHZ	-	40	-	
Reverse Transfer Capacitance	Crss		-	29	-	
Turn-On Delay Time	td _(on)		-	4	-	ns
Turn-On Rise Time	tr	$V_{DD}=10V, I_{D}=4.1A,$	-	47	-	
Turn-Off Delay Time	td _(off)	V_{GS} =4.5V, R _G =6 Ω ^(Note 1,2)	-	18	-	
Turn-Off Fall Time	tf	L ^C =077	-	10	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	1.5	А
Diode Forward Current	I _S					
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V	-	0.75	1.2	V



P-Channel Electrical Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.71	-1.2	V
Drain-Source On-State Resistance		V _{GS} =-4.5V, I _D =-3.1A	-	97	115	
	R _{DS(on)}	V _{GS} =-2.5V, I _D =-2.0A	-	119	140	mΩ
		V _{GS} =-1.8V, I _D =-1.1A	-	157	190	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Qg	V 40V L 244	-	5.4	-	
Gate-Source Charge	Q _{gs}	V_{DS} =-10V, I_{D} =-3.1A, V_{GS} =-4.5V ^(Note 1,2)	-	0.7	-	nC
Gate-Drain Charge	Q_gd	V _{GS} =-4.5V	-	1.3	-	
Input Capacitance	Ciss		-	416	-	
Output Capacitance	Coss	V _{DS} =-10V, V _{GS} =0V, f=1MHZ	-	43	-	pF
Reverse Transfer Capacitance	Crss	I=IMHZ	-	32	-	
Turn-On Delay Time	td _(on)		-	4	-	
Turn-On Rise Time	tr	V_{DD} =-10V, I_{D} =-3.1A, V_{GS} =-4.5V, R_{G} =6 Ω ^(Note 1,2)	-	27	-	
Turn-Off Delay Time	td _(off)		-	78	-	ns
Turn-Off Fall Time	tf	K _G =012	-	45	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S		-	-	-1.5	А
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.8	-1.2	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. RoJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

10

0

2

4

Fig.5 On-Resistance Variation with V_{GS}

VGS-Gate-to-Source Voltage(V)

6

8

10

0.01

0

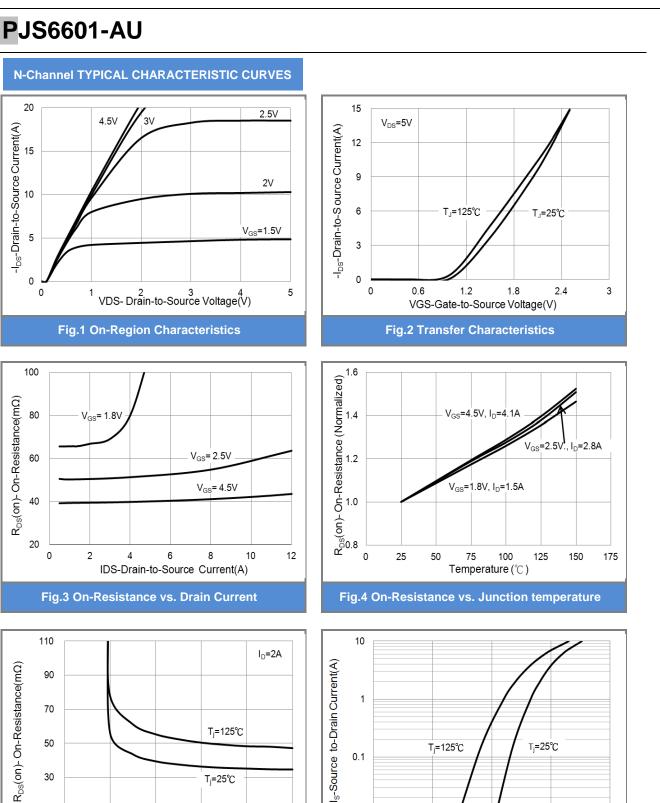
0.3

Fig.6 Body Diode Characteristics

0.9

0.6

VSD-Source-to-Drain Voltage(V)









1.2

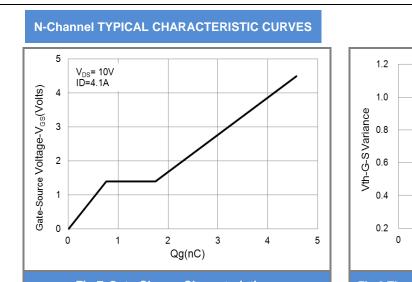


Fig.7 Gate-Charge Characteristics

Ciss

Coss

5

10

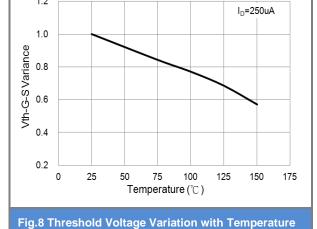
VDS-Drain-Source Voltage (V)

Fig.9 Capacitance vs. Drain-Source Voltage

15

20

V_{GS}=0V f=1MHz





600

400

0 Crss 0

Capacitance (pF)

PJS6601-AU



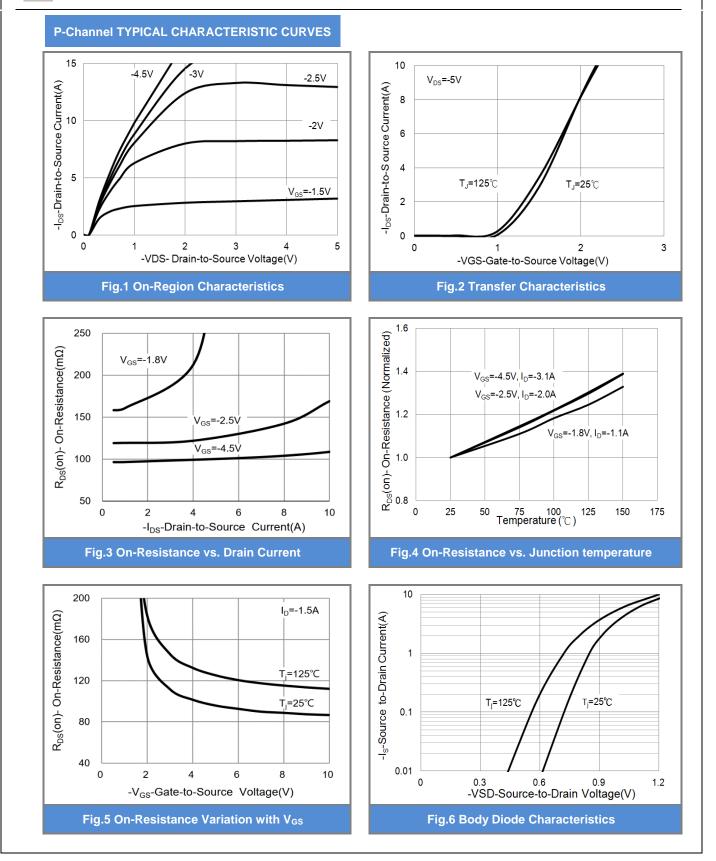








Fig.7 Gate-Charge Characteristics

3

Qg(nC)

4

5

6

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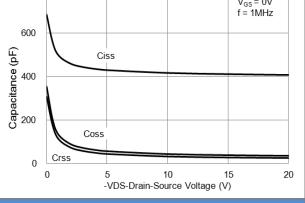


Fig.9 Threshold Voltage Variation with Temperature

P-Channel TYPICAL CHARACTERISTIC CURVES

PANJ SEMI CONDUCTOR

5

Gate-Source Voltage-V_{GS}(Volts) \sim $1 \sim \sim \sim \sim e^{-5}$

1

0

0

PJS6601-AU

V_{DS}= -10V I_D=-3.1A

1

2

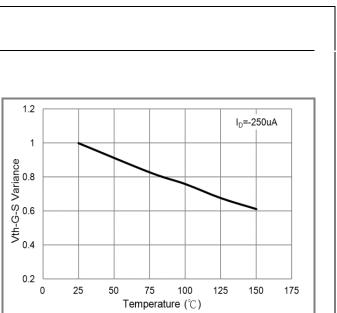


Fig.8 Threshold Voltage Variation with Temperature



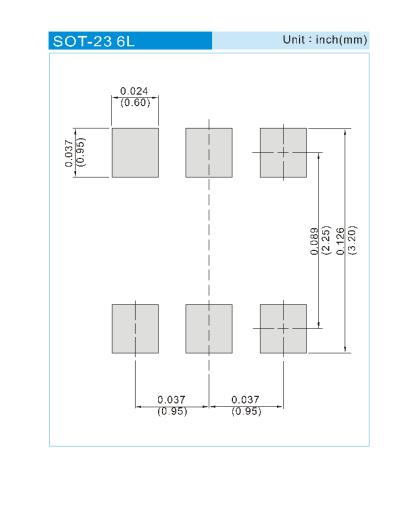




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJS6601-AU_S1_000A1	SOT-23 6L	3K pcs / 7" reel	SC1	Halogen free

Mounting Pad Layout





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