

150V N-Channel MOSFET

Voltage	150 V	Rdson	7.5 mΩ
Current	125 A	Qg	97 nC

Feature:

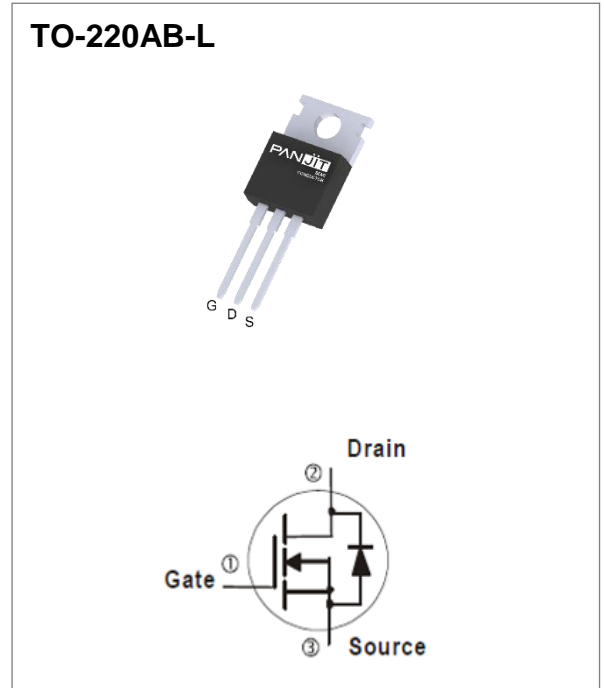
- $R_{DS(ON) Max, V_{GS}@10V}$: 7.5mΩ
- $R_{DS(ON) Max, V_{GS}@7V}$: 9mΩ
- High Speed Switching and Low $R_{DS(ON)}$
- 100% Avalanche Tested
- 100% Rg Tested
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: TO-220AB-L package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0739 ounces, 2.0948 grams

Application

- BMS, BLDC, SMPS SR.



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current ^(Note 3)	$T_C=25^\circ\text{C}$	I_D	125	A
	$T_C=100^\circ\text{C}$		88.5	
Pulsed Drain Current		I_{DM}	350	A
Single Pulse Avalanche Current ^(Note 5)		I_{AS}	36	A
Single Pulse Avalanche Energy ^(Note 5)		E_{AS}	655	mJ
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	258.6	W
	$T_C=100^\circ\text{C}$		129	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~175	°C

Thermal Characteristics

PARAMETER		SYMBOL	MAXIMUM	UNITS
Thermal Resistance	Junction-to-Case	$R_{\theta JC}$	0.58	°C/W
	Junction-to-Ambient ^(Note 3)	$R_{\theta JA}$	62.5	°C/W

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	150	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3.2	4	
Drain-Source On-State Resistance (Note 1)	$R_{DS(on)}$	$V_{GS}=10V, I_D=50A$	-	6.3	7.5	m Ω
	$R_{DS(on)}$	$V_{GS}=7V, I_D=25A$	-	6.8	9	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=120V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	$V_{DS}=75V, I_D=50A,$ $V_{GS}=7V$	-	72	-	nC
	Q_g	$V_{DS}=75V, I_D=50A,$ $V_{GS}=10V$	-	97	-	
Gate-Source Charge	Q_{gs}	$V_{DS}=75V, I_D=50A,$ $V_{GS}=10V$	-	31	-	
Gate-Drain Charge	Q_{gd}	$V_{DS}=75V, I_D=50A,$ $V_{GS}=10V$	-	23	-	
Input Capacitance	C_{iss}	$V_{DS}=75V, V_{GS}=0V,$ $F=1MHz$	-	6511	-	pF
Output Capacitance	C_{oss}		-	862	-	
Reverse Transfer Capacitance	C_{rss}		-	83	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=75V, I_D=50A,$ $V_{GS}=10V, R_G=2\Omega$ (Note 2)	-	53	-	ns
Turn-On Rise Time	t_r		-	111	-	
Turn-Off Delay Time	$t_{d(off)}$		-	99	-	
Turn-Off Fall Time	t_f		-	113	-	
Gate Resistance	R_g	$f=1.0MHz$	-	2.7	-	Ω
Drain-Source Diode						
Diode Forward Voltage	V_{SD}	$I_S=50A, V_{GS}=0V$	-	0.9	1.3	V
Reverse Recovery Charge	Q_{rr}	$I_S=50A$	-	541	-	nC
Reverse Recovery Time	T_{rr}	$di/dt=100A/\mu s$	-	117	-	ns

NOTES :

- Pulse width<300us, Duty cycle<2%
- Essentially independent of operating temperature typical characteristics.
- The maximum current rating is silicon limited.
- $R\theta JA$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- The test condition is L=1mH, IAS=36.2A, VDD=50V, VGS=10V, RG=25ohm, Starting $T_J=25^\circ\text{C}$
- Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTIC CURVES

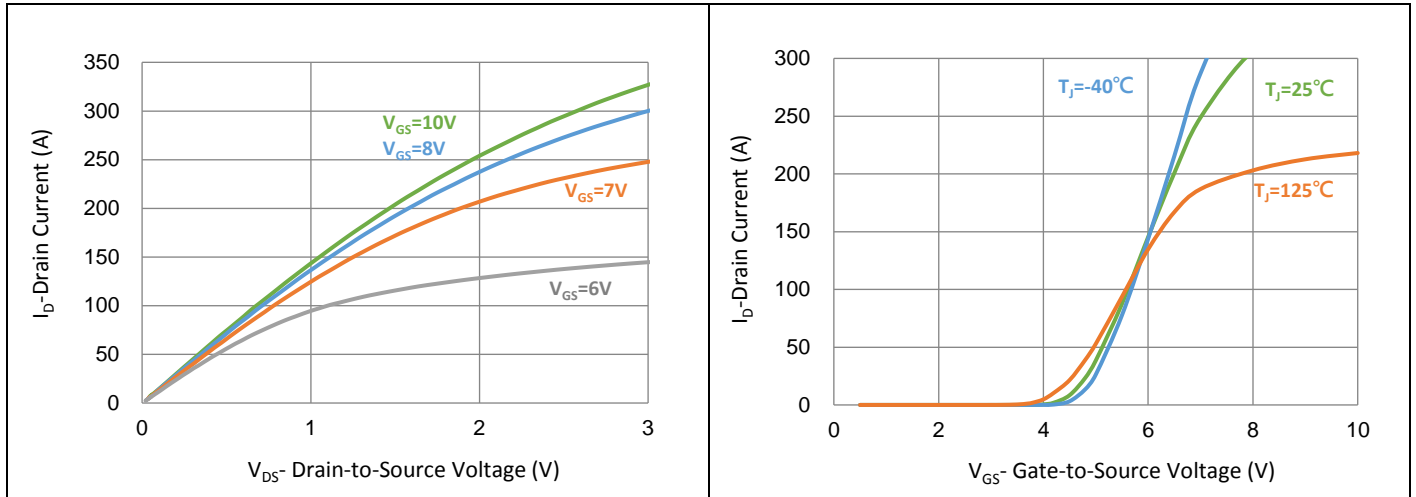


Fig.1 Output Characteristics

Fig.2 Transfer Characteristics

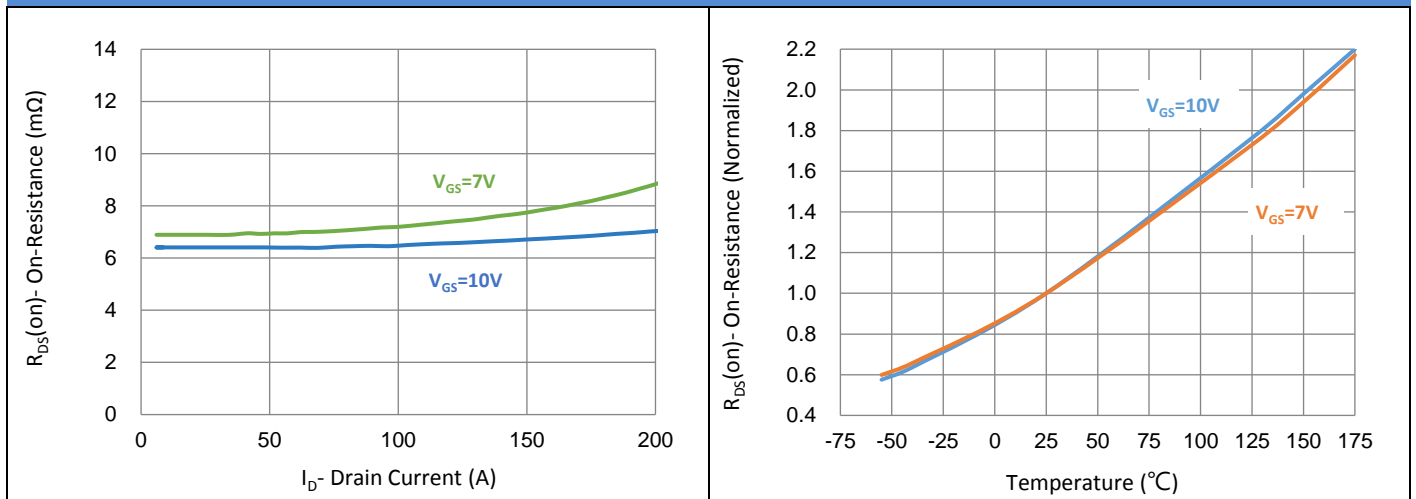


Fig.3 On-Resistance vs. Drain Current

Fig.4 On-Resistance vs. Junction Temperature

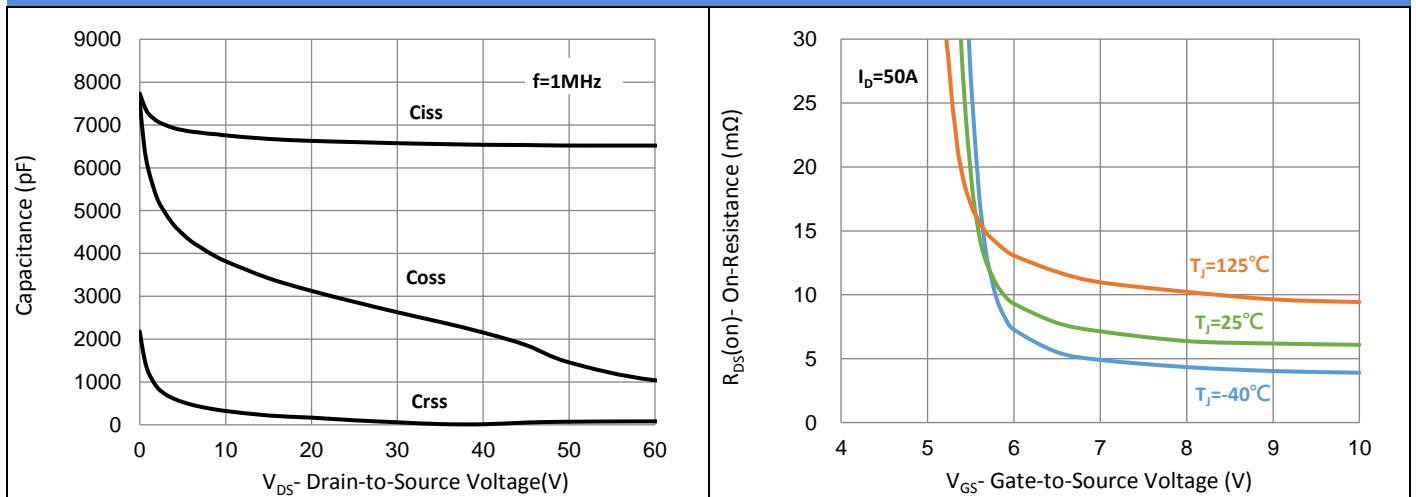


Fig.5 Capacitance vs. Drain-Source Voltage

Fig.6 On-Resistance vs. Gate-Source Voltage

TYPICAL CHARACTERISTIC CURVES

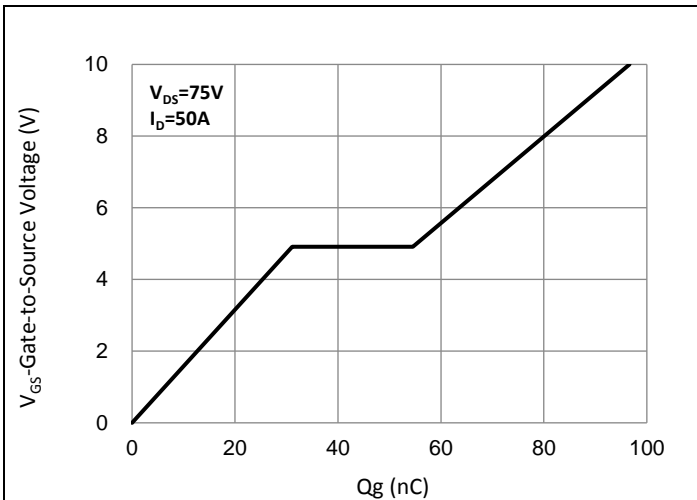


Fig.7 Gate-Charge Characteristics

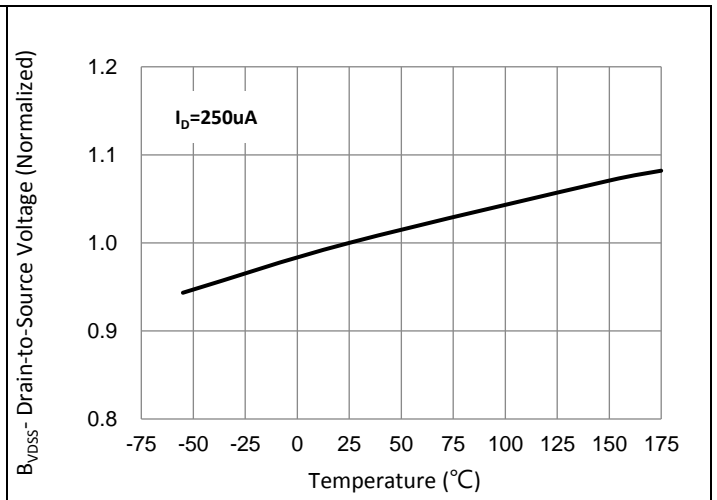


Fig.8 Breakdown Voltage Variation vs. Temperature

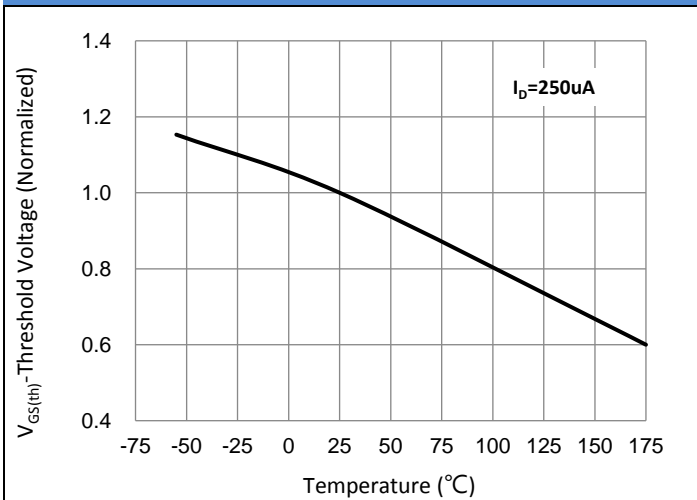


Fig.9 Threshold Voltage Variation with Temperature

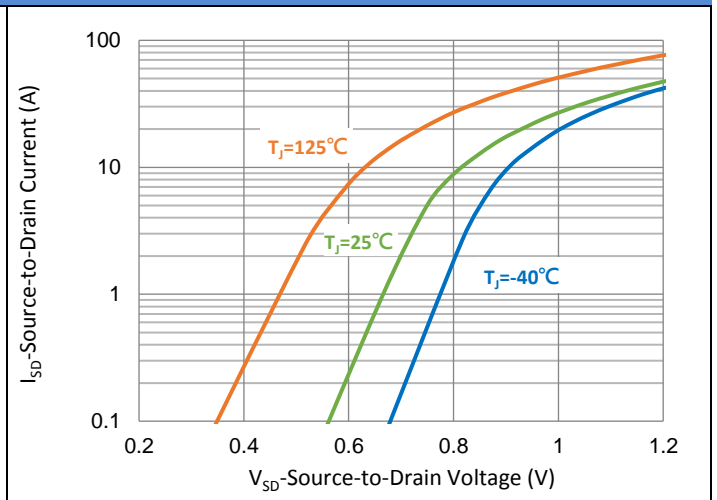


Fig.10 Source-Drain Diode Forward Voltage

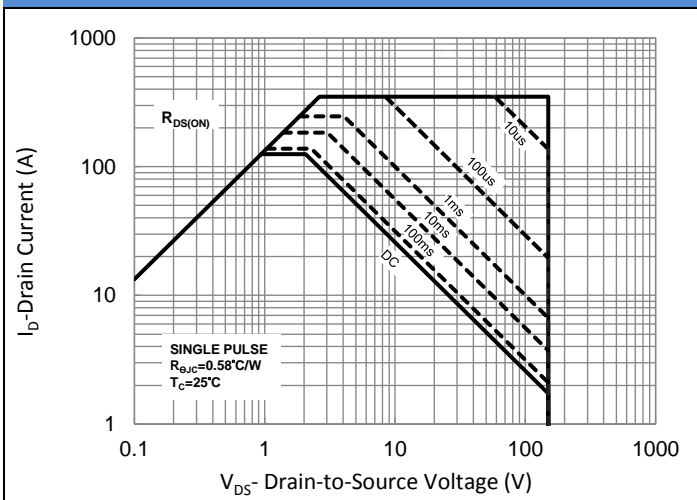


Fig.11 Maximum Safe Operating Area

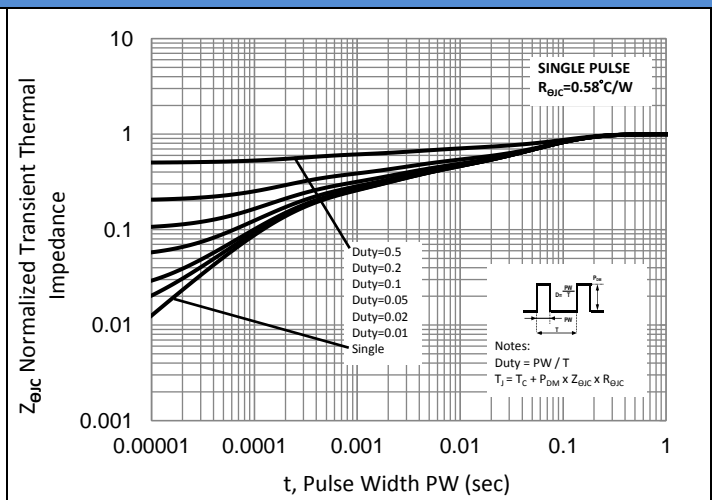


Fig.12 Normalized Transient Thermal Impedance

TYPICAL CHARACTERISTIC CURVES

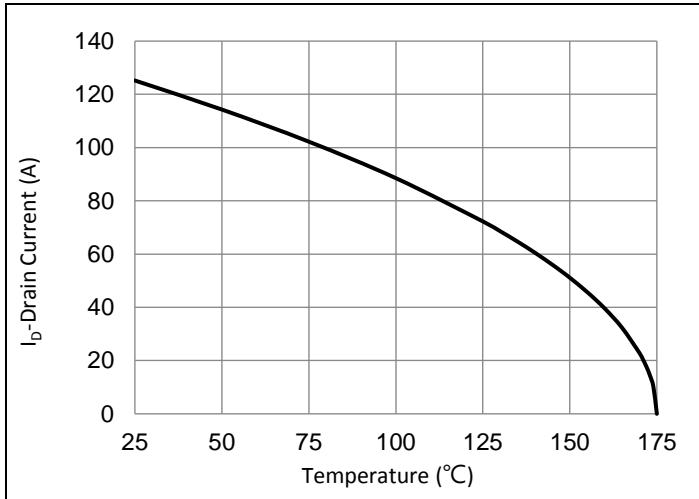
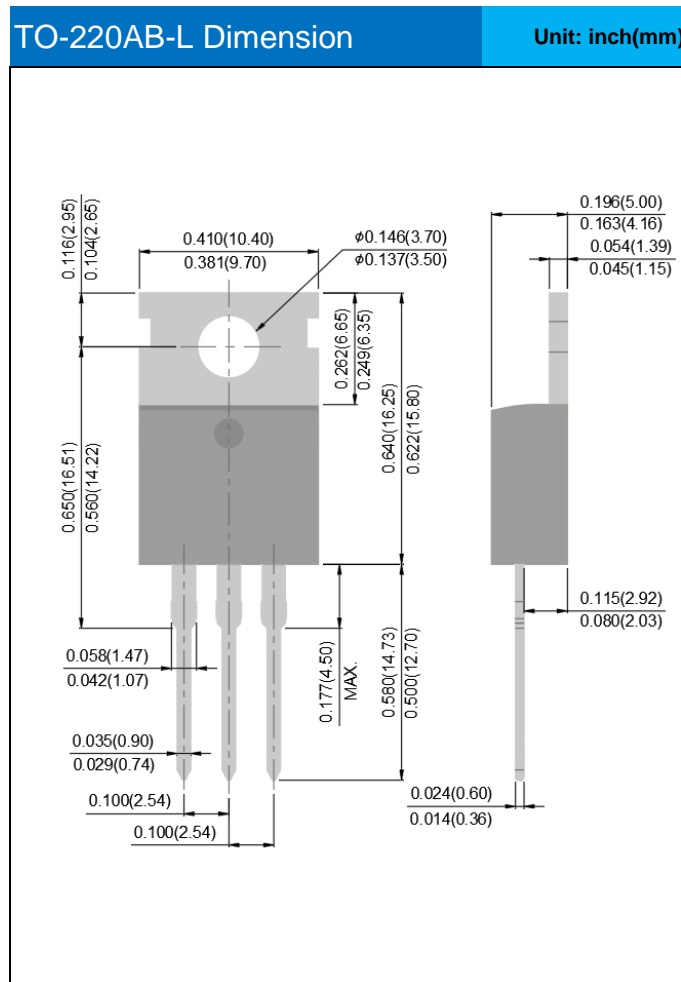


Fig.13 Drain Current vs. Case Temperature

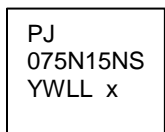
Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PSMP075N15NS1	TO-220AB-L	50pcs / Tube	075N15NS

Packaging Information



Marking Diagram



- Y** = Year Code
- W** = Week Code (A~Z)
- LL** = Lot Code (00~99)
- x** = Production Line Code

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