



20V P-Channel Enhancement Mode MOSFET - ESD Protected

Voltage

-20 V

Current

-1.5A

Features

- RDS(ON), VGS@-4.5V, ID@-1.5A<325mΩ
- RDS(ON) , VGS@-2.5V, ID@-1.2A<420mΩ
- RDS(ON) , VGS@-2.5V, ID@-0.5A<600mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected 2KV HBM
- Lead free in comply with EU RoHS 2011/65/EU directives.
- Green molding compound as per IEC61249 Std. (Halogen Free)

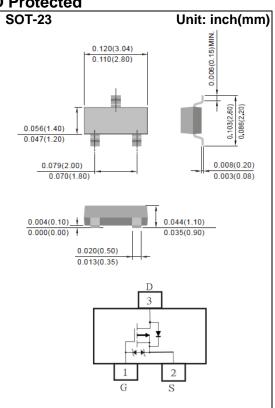
Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0003 ounces, 0.0084 grams

Marking: A31



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		I _D	-1.5	Α
Pulsed Drain Current (Note 4)		I _{DM}	-4	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance				
- Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-0.5	-0.64	-1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-1.5A	-	240	325	mΩ
		V _{GS} =-2.5V, I _D =-1.2A	-	295	420	
		V _{GS} =-1.8V, I _D =-0.5A	-	405	600	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.02	-1	uA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\underline{+}8V, V_{DS}=0V$	-	<u>+</u> 3.5	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Q_g	V _{DS} =-10V, I _D =-1.5A, V _{GS} =-4.5V ^(Note 1,2)	-	1.7	-	nC
Gate-Source Charge	Q_gs		-	0.35	-	
Gate-Drain Charge	Q_gd		-	0.43	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V,	-	165	-	pF
Output Capacitance	Coss		-	25	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	14.7	-	
Switching						
Turn-On Delay Time	td _(on)	\/ 40\/ L 45A	-	11	-	
Turn-On Rise Time	tr	V_{DD} =-10V, I_{D} =-1.5A, V_{GS} =-4.5V, R_{G} =6 Ω (Note 1,2)	-	38	-	ns
Turn-Off Delay Time	td _(off)		-	130	-	
Turn-Off Fall Time	tf	R _G =012	-	75	-	
Drain-Source Diode						
Maximum Continuous Drain-Source				_	-1.6	А
Diode Forward Current	I _S		-			
Diode Forward Voltage	$V_{\mathtt{SD}}$	I _S =-1.6A, V _{GS} =0V	-	-1.03	-1.2	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.





TYPICAL CHARACTERISTIC CURVES

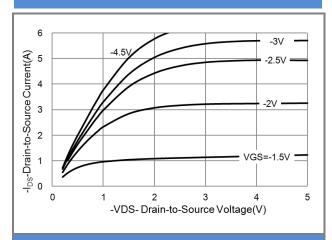


Fig.1 On-Region Characteristics

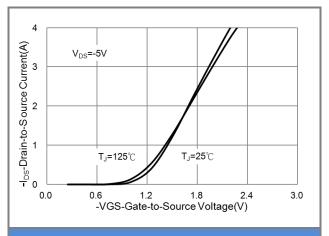


Fig.2 Transfer Characteristics

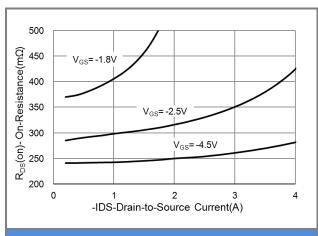


Fig.3 On-Resistance vs. Drain Current

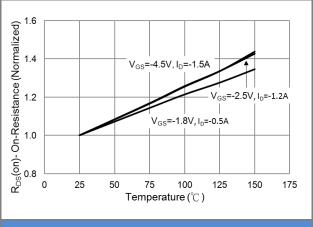


Fig.4 On-Resistance vs. Junction temperature

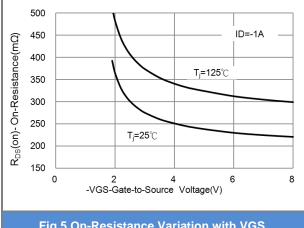


Fig.5 On-Resistance Variation with VGS.

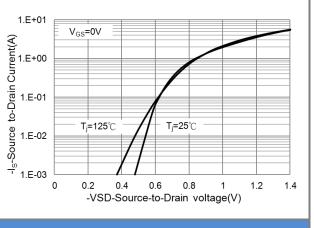


Fig.6 Body Dlode CharacterIslcs





TYPICAL CHARACTERISTIC CURVES

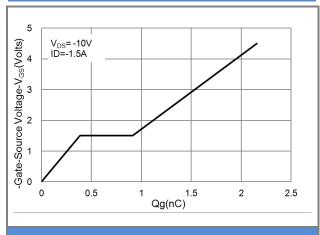
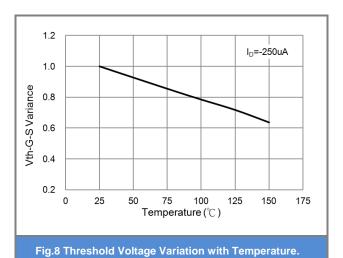


Fig.7 Gate-Charge Characteristics



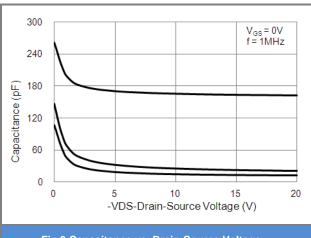


Fig.9 Capacitance vs. Drain-Source Voltage.

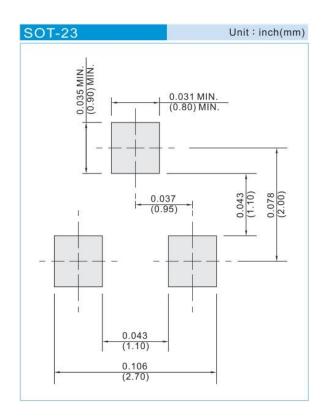




PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJA3431_R1_00001	SOT-23	3K pcs / 7" reel	A31	Halogen free
PJA3431_R2_00001	SOT-23	12K pcs / 13" reel	A31	Halogen free

MOUNTING PAD LAYOUT







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