



30V N-Channel Enhancement Mode MOSFET

Voltage 30 V Current 5.6A

Features

- RDS(ON), VGS@10V, ID@5.6A<30mΩ
- RDS(ON), VGS@4.5V, ID@3.5A<45mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Acquire quality system certificate: TS16949
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

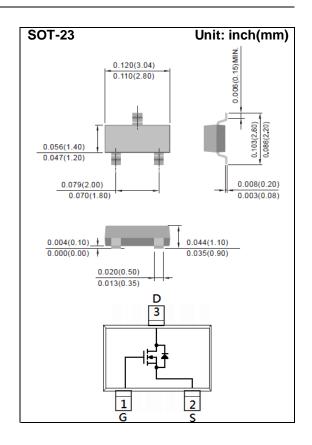
Mechanical Data

• Case: SOT-23 Package

• Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0003 ounces, 0.0084 grams

Marking: A04



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V
Continuous Drain Current		I _D	5.6	Α
Pulsed Drain Current		I _{DM}	22	Α
Power Dissipation	T _a =25°C	<u></u>	1.25	W
	Derate above 25°C	P_{D}	10	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1.0	1.33	2.1	V		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =5.6A	-	27	30	mΩ		
		V_{GS} =4.5V, I_{D} =3.5A	-	39	45			
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	0.01	1	uA		
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA		
Dynamic								
Total Gate Charge	Q_g	V _{DS} =15V, I _D =5.6A, V _{GS} =10V ^(Note 1,2)	-	7.8	-	nC		
Gate-Source Charge	Q_gs		-	1.2	-			
Gate-Drain Charge	Q_gd		-	1.5	-			
Input Capacitance	Ciss	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	343	-	pF		
Output Capacitance	Coss		-	48	-			
Reverse Transfer Capacitance	Crss		-	34	-			
Switching								
Turn-On Delay Time	td _(on)	\/ 45\/ 5.0A	-	3	-			
Turn-On Rise Time	tr	V_{DD} =15V, I_{D} =5.6A, V_{GS} =10V, R_{G} =3 Ω (Note 1,2)	-	40	-	ns		
Turn-Off Delay Time	td _(off)		-	38	-			
Turn-Off Fall Time	tf	K _G =312	-	39	-			
Drain-Source Diode								
Maximum Continuous Drain-Source			-	-	1.5	А		
Diode Forward Current	I _S							
Diode Forward Voltage	$V_{\mathtt{SD}}$	I _S =1.0A, V _{GS} =0V	-	0.77	1.2	V		

NOTES:

- 1. Pulse width<a><a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





TYPICAL CHARACTERISTIC CURVES

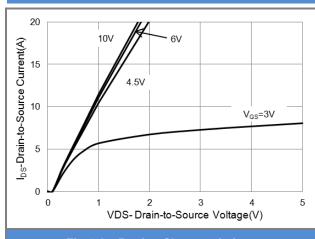


Fig.1 On-Region Characteristics

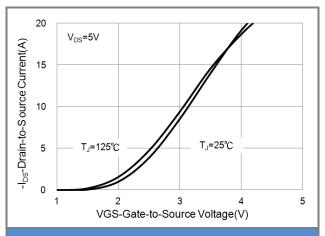


Fig.2 Transfer Characteristics

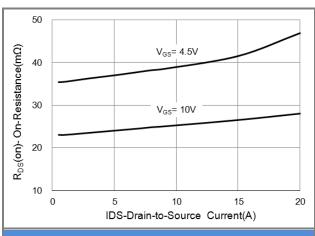


Fig.3 On-Resistance vs. Drain Current

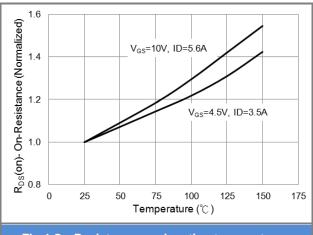


Fig.4 On-Resistance vs. Junction temperature

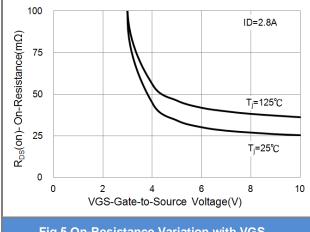
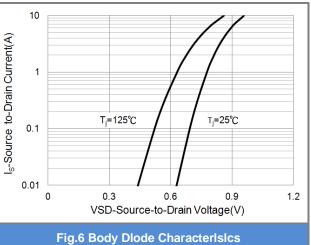


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

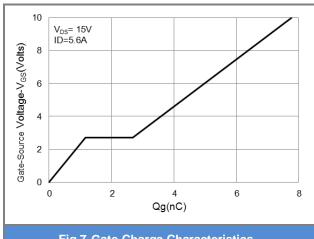


Fig.7 Gate-Charge Characteristics

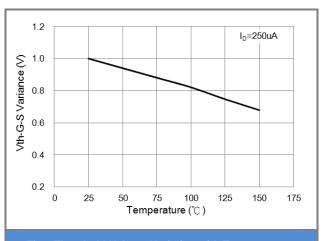


Fig.8 Threshold Voltage Variation with Temperature

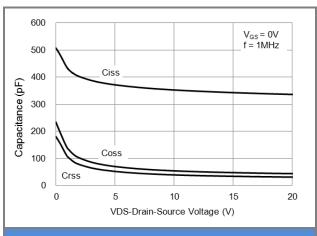


Fig.9 Capacitance vs. Drain-Source Voltage.

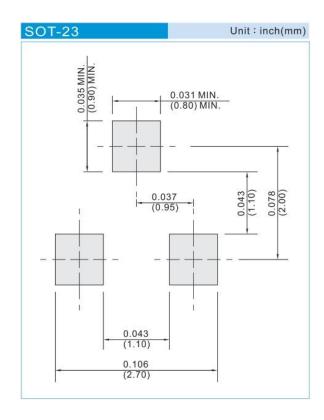




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3404-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A04	Halogen free
PJA3404-AU_R2_000A1	SOT-23	12K pcs / 13" reel	A04	Halogen free

MOUNTING PAD LAYOUT







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