



30V P-Channel Enhancement Mode MOSFET

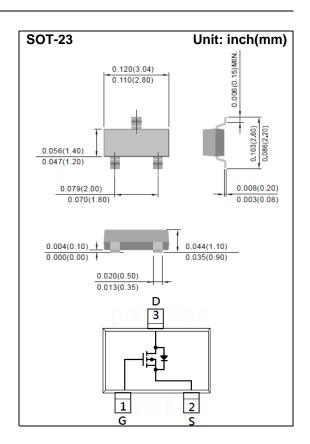
Voltage -30 V Current -3.6A

Features

- RDS(ON), VGS@-10V, ID@-3.6A<73m Ω
- RDS(ON) , VGS@-4.5V, ID@-2.4A<97mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A05



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V
Continuous Drain Current		I _D	-3.6	Α
Pulsed Drain Current		I _{DM}	-14.4	Α
Power Dissipation	T _a =25°C	P_{D}	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_{J}, T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	ı	>
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1.0	-1.37	-2.1	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-3.6A	_	59	73	mΩ
		V _{GS} =-4.5V, I _D =-2.4A	-	76	97	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-3.6A, V _{GS} =-10V ^(Note 1,2)	_	10	-	nC
Gate-Source Charge	Q_gs		_	1.1	-	
Gate-Drain Charge	Q_gd		-	1.7	ı	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	_	417	-	pF
Output Capacitance	Coss		_	50	-	
Reverse Transfer Capacitance	Crss		-	36	-	
Switching						
Turn-On Delay Time	td _(on)	\/ 45\/ L 000	-	3.2	i	ns
Turn-On Rise Time	tr	V_{DD} =-15V, I_{D} =-3.6A, V_{GS} =-10V, R_{G} =6 Ω (Note 1,2)	-	33	ı	
Turn-Off Delay Time	td _(off)		-	119	ı	
Turn-Off Fall Time	tf		-	68	ı	
Drain-Source Diode						
Maximum Continuous Drain-Source				-	-1.5	А
Diode Forward Current	I _S		-			
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.77	-1.2	٧

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Reja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





TYPICAL CHARACTERISTIC CURVES

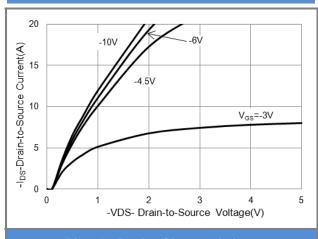


Fig.1 On-Region Characteristics

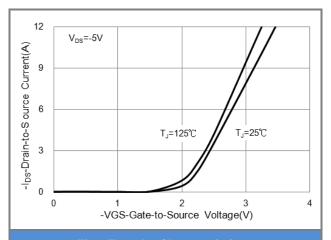


Fig.2 Transfer Characteristics

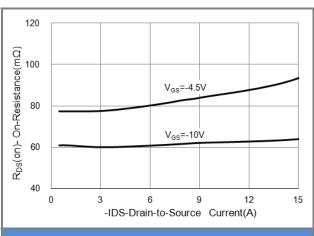


Fig.3 On-Resistance vs. Drain Current

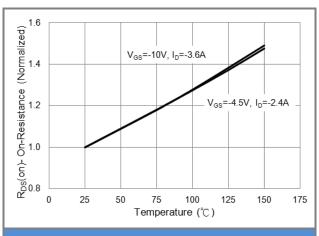
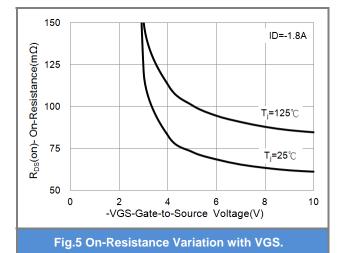


Fig.4 On-Resistance vs. Junction temperature



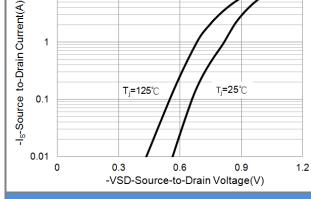


Fig.6 Body Dlode CharacterIslcs

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TYPICAL CHARACTERISTIC CURVES

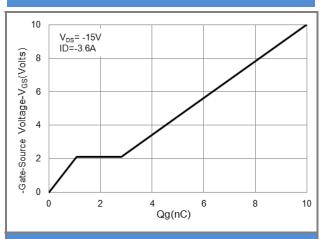


Fig.7 Gate-Charge Characteristics

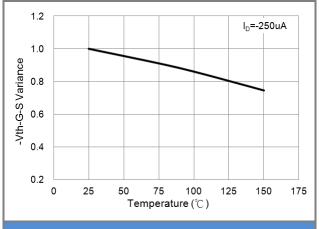


Fig.8 Threshold Voltage Variation with Temperature.

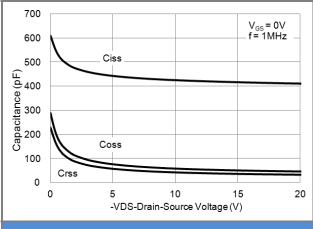


Fig.9 Capacitance vs. Drain-Source Voltage.

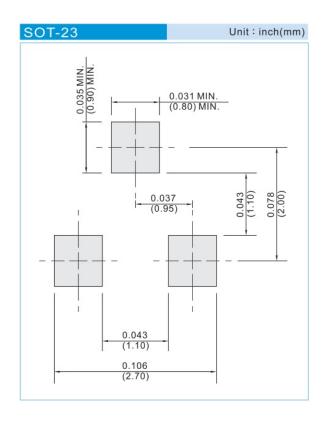




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJA3405_R1_00001	SOT-23	3K pcs / 7" reel	A05	Halogen free
PJA3405 _R2_00001	SOT-23	12K pcs / 13" reel	A05	Halogen free

MOUNTING PAD LAYOUT







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