

- R_{DS(ON)}, V_{GS}@-4.5V, I_D@-4.3A<52mΩ
- R_{DS(ON)}, V_{GS}@-2.5V, I_D@-3.0A<60mΩ
- R_{DS(ON)}, V_{GS}@-1.8V, I_D@-1.5A<80mΩ •
- Advanced Trench Process Technology •
- Specially Designed for Switch Load, PWM Application, etc •
- ESD Protected 2KV HBM •
- AEC-Q101 gualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard •

Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams

0.006(0.15)MIN 0.103(2.60) 0.086(2.20) 0.056(1.40) 0.047(1.20) 0.079(2.00) 0.008(0.20) 0.070(1.80) 0.003(0.08) 0.004(0.10) 0.044(1.10) 0.000(0.00) 0.035(0.90) 0.020(0.50) 0.013(0.35) D 3 1 2

G

0.120(3.04)

0.110(2.80)

SOT-23

-4.3A

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-20	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 8		
Continuous Drain Current		I _D	-4.3	A	
Pulsed Drain Current		I _{DM}	-17.2		
Power Dissipation	T _a =25°C	P _D	1.25	W	
	Derate above 25°C		10	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance		R _{eJA}		°C/W	
- Junction to Ambient (Note 3)			100		

Unit : inch(mm)







Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V, I _D =-250uA	-20	-	-	- v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-0.4	-0.72	-1.0	V	
Drain-Source On-State Resistance	$R_{DS(on)}$	V _{GS} =-4.5V, I _D =-4.3A	-	44	52	mΩ	
		V _{GS} =-2.5V, I _D =-3.0A	-	53	60		
		V _{GS} =-1.8V, I _D =-1.5A	-	70	80		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-	-1	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	-	<u>+</u> 10		
Dynamic (Note 5)			-	•			
Total Gate Charge	Qg		-	24	-	nC	
Gate-Source Charge	Q_gs	V _{DS} =-10V, I _D =-4.3A, V _{GS} =-4.5V ^(Note 1,2)	-	1.5	-		
Gate-Drain Charge	Q_{gd}	V _{GS} =-4.5V	-	2.5	-		
Input Capacitance	Ciss		-	907	-	pF	
Output Capacitance	Coss	V_{DS} =-10V, V_{GS} =0V,	-	90	-		
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	70	-		
Turn-On Delay Time	td _(on)		-	45	-	- ns	
Turn-On Rise Time	tr	V_{DD} =-10V, I_{D} =-4.3A, V_{GS} =-4.5V, R_{G} =6 Ω ^(Note 1,2)	-	79	-		
Turn-Off Delay Time	td _(off)		-	193	-		
Turn-Off Fall Time	tf	NG=032	-	826	-		
Drain-Source Diode				•			
Maximum Continuous Drain-Source	I _S		-	-	-1.5	A	
Diode Forward Current	Ŭ		<u> </u>		-		
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.76	-1.2	V	

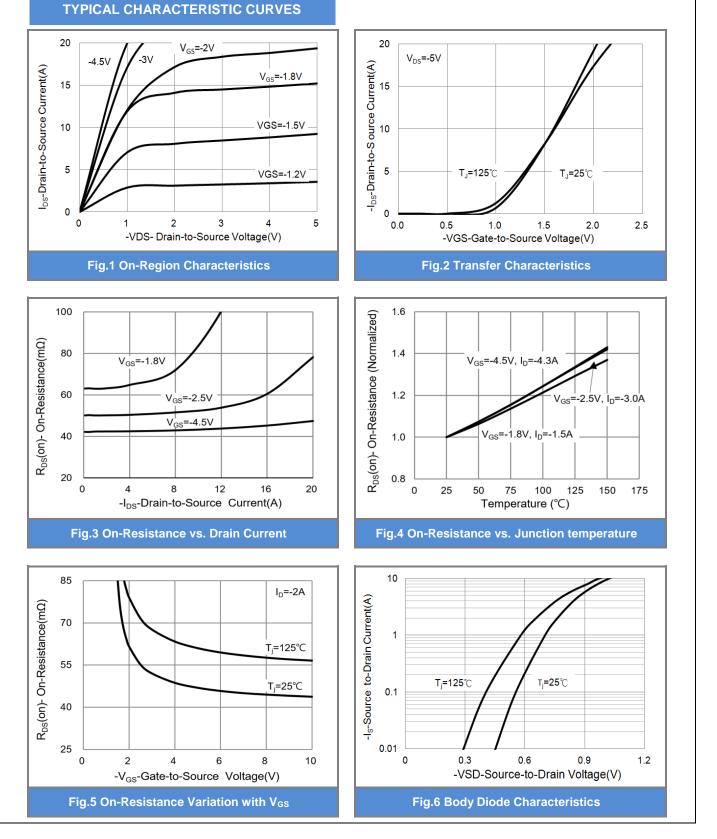
NOTES :

1. Pulse width</br>

2. Essentially independent of operating temperature typical characteristics.

- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing.

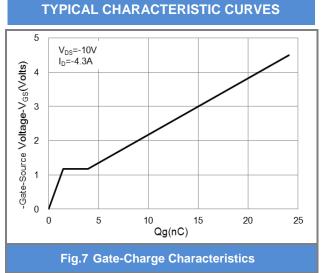
October 27,2017-REV.02



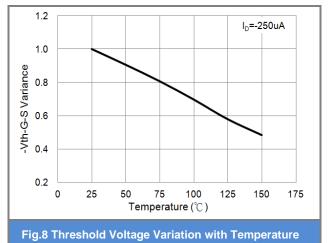
PJA3415AE-AU







V_{GS}= 0V f = 1MHz





1500

1200

900

600

300

0 Crss

Capacitance (pF)

Ciss

Coss

5

10

-VDS-Drain-Source Voltage (V)

Fig.9 Capacitance vs. Drain-Source Voltage

15

20



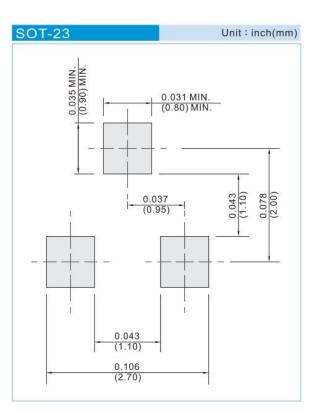




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3415AE-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A5AE	Halogen free

Mounting Pad Layout





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