



60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

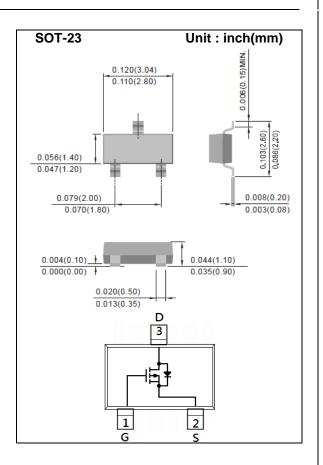
-300mA

Features

- R_{DS(ON)}, V_{GS}@-10V, I_D@-500mA<4Ω
- R_{DS(ON)}, V_{GS}@-4.5V, I_D@-200mA<6Ω
- R_{DS(ON)}, V_{GS}@-2.5V, I_D@-50mA<13Ω
- Advanced Trench Process Technology
- Specially Designed for Relay driver, Speed line drive, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-60	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20		
Continuous Drain Current		I _D	-300	mA	
Pulsed Drain Current		I _{DM}	-1000		
Power Dissipation	T _A =25°C	P_{D}	500	mW	
	Derate above 25°C		4	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 3)		$R_{ hetaJA}$	250	°C/W	





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static (Note 1)						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1.0	-1.5	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V,I _D =-500mA	-	2.4	4	Ω
		V_{GS} =-4.5V, I_{D} =-200mA	-	2.65	6	
		V_{GS} =-2.5V, I_D =-50mA	-	4.5	13	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-48V,V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Q_g	V _{DS} =-25V, I _D =-100mA, V _{GS} =-4.5V	-	1.1	-	nC
Gate-Source Charge	Q_gs		-	0.3	-	
Gate-Drain Charge	Q_gd		-	0.2	-	
Input Capacitance	Ciss	V _{DS} =-25V, V _{GS} =0V, f=1.0MHZ	-	51	-	pF
Output Capacitance	Coss		-	15	-	
Reverse Transfer Capacitance	Crss	I=1.0IVINZ	-	2.2	-	
Turn-On Delay Time	td _(on)	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	4.8	-	
Turn-On Rise Time	tr	V_{DD} =-25V, I_{D} =-100mA, V_{GS} =-10V, R_{G} =6 Ω (Note 1.2)	-	19	-	ns
Turn-Off Delay Time	td _(off)		-	52	-	
Turn-Off Fall Time	tf	K _G =012	-	32	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					-300	mA
Diode Forward Current	I _S		-	-		
Diode Forward Voltage	V_{SD}	I _S =-500mA, V _{GS} =0V	-	-0.95	-1.3	V

NOTES:

- 1. Pulse width < 300us, Duty cycle < 2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

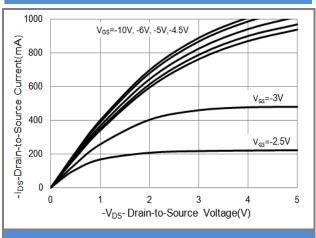


Fig.1 On-Region Characteristics

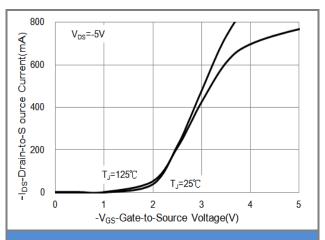


Fig.2 Transfer Characteristics

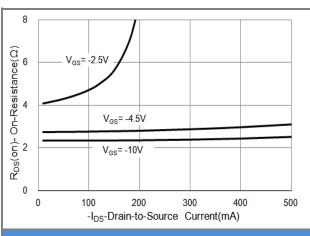


Fig.3 On-Resistance vs. Drain Current

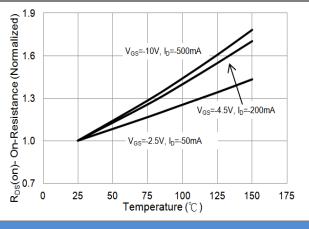


Fig.4 On-Resistance vs. Junction temperature

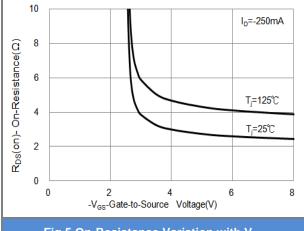


Fig.5 On-Resistance Variation with V_{GS}

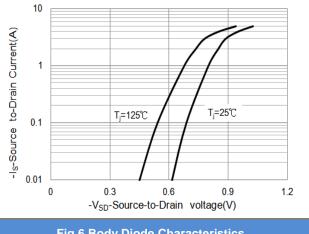


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

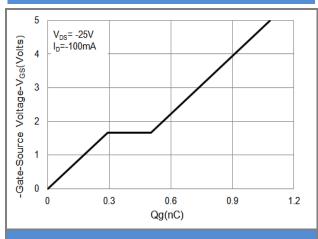


Fig.7 Gate-Charge Characteristics

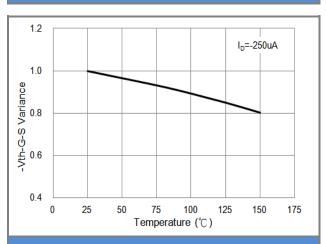


Fig.9 Threshold Voltage Variation with Temperature

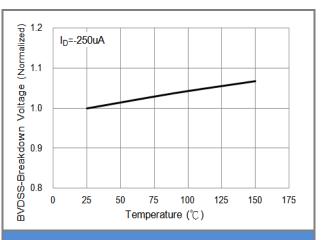


Fig.8 Breakdown Voltage Variation vs. Temperature

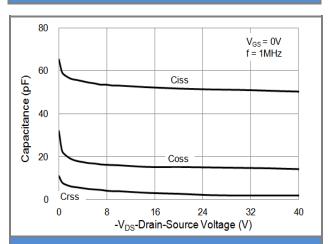


Fig.10 Capacitance vs. Drain-Source Voltage

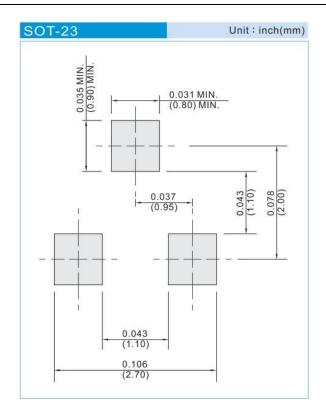




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3439-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A39	Halogen free

Mounting Pad Layout







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