



PJA3471

100V P-Channel Enhancement Mode MOSFET

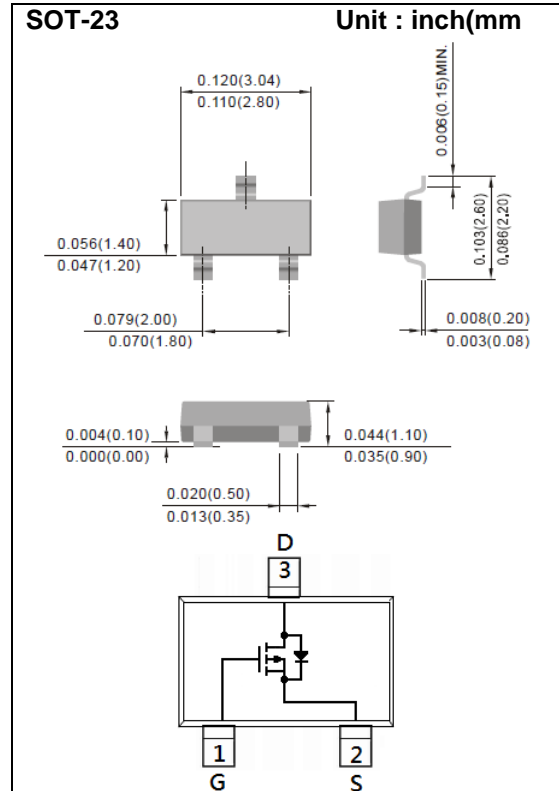
Voltage -100 V **Current** -0.9 A

Features

- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-0.9A < 650m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_D@-0.45A < 700m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage	V_{DS}	-100	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current (Note 4)	I_D	$T_A=25^\circ C$	-0.9	A
		$T_A=70^\circ C$	-0.75	
Pulsed Drain Current (Note 1)	I_{DM}	-3.6		
Power Dissipation	P_D	$T_A=25^\circ C$	1.25	W
		$T_A=70^\circ C$	0.8	
Single Pulse Avalanche Energy (Note 6)	E_{AS}	0.2	mJ	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ C$	
Typical Thermal resistance	$R_{\theta JA}$	100	$^\circ C/W$	
- Junction to Ambient (Note 4,5)				



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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-100	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-1	-2	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-0.9A	-	500	650	mΩ
		V _{GS} =-4.5V, I _D =-0.45A	-	560	700	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-80V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Dynamic (Note 7)						
Total Gate Charge	Q _g	V _{DS} =-50V, I _D =-1A, V _{GS} =-10V (Note 2,3)	-	8	-	nC
Gate-Source Charge	Q _{gs}		-	1.8	-	
Gate-Drain Charge	Q _{gd}		-	1.4	-	
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, f=1MHZ	-	448	-	pF
Output Capacitance	C _{oss}		-	28	-	
Reverse Transfer Capacitance	C _{rss}		-	21	-	
Turn-On Delay Time	t _{d(on)}	V _{DS} =-50V, I _D =1A, V _{GS} =-10V, R _G =6.2Ω (Note 2,3)	-	3.7	-	ns
Turn-On Rise Time	t _r		-	25	-	
Turn-Off Delay Time	t _{d(off)}		-	21	-	
Turn-Off Fall Time	t _f		-	22	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _s	---	-	-	-1.5	A
Diode Forward Voltage	V _{SD}	I _s =-1A, V _{GS} =0V	-	-0.82	-1.2	V

NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J = 25°C.
5. The test condition is L=0.1mH, I_{AS}=-2A, V_{DD}=-25V, V_{GS}=-10V
6. R_{ΘJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
7. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

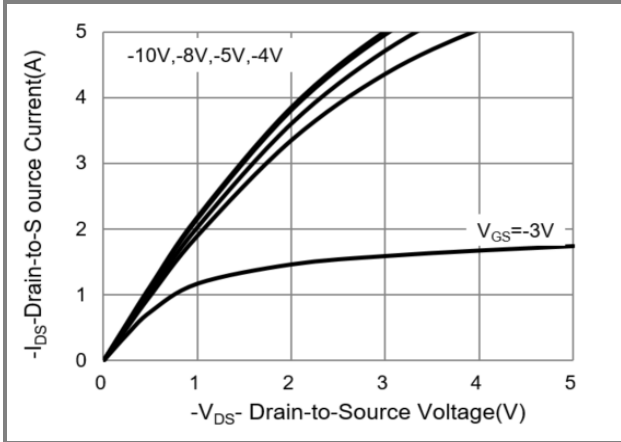


Fig.1 On-Region Characteristics

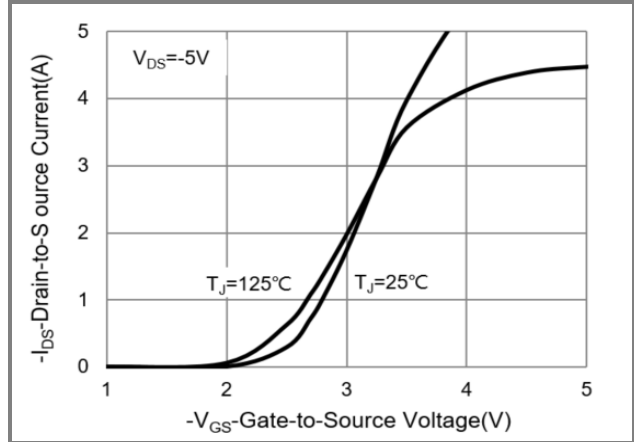


Fig.2 Transfer Characteristics

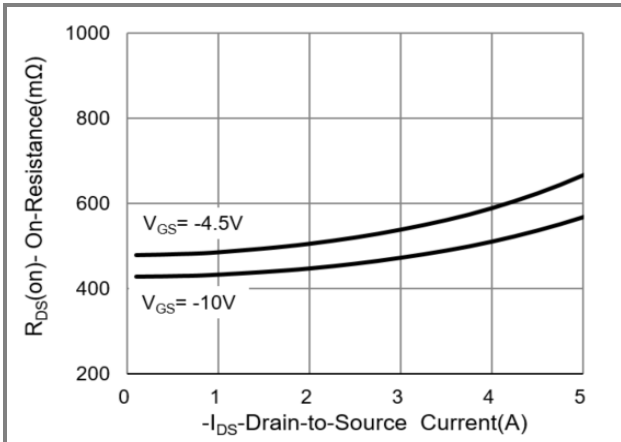


Fig.3 On-Resistance vs. Drain Current

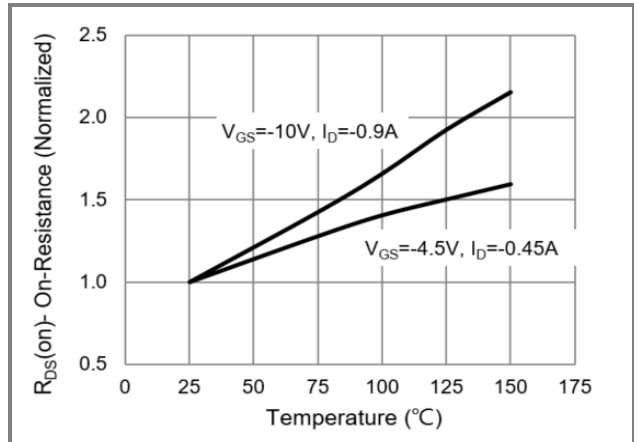


Fig.4 On-Resistance vs. Junction temperature

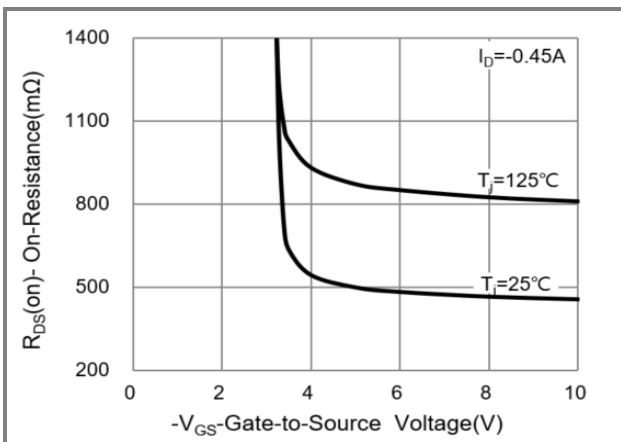


Fig.5 On-Resistance Variation with V_{GS}

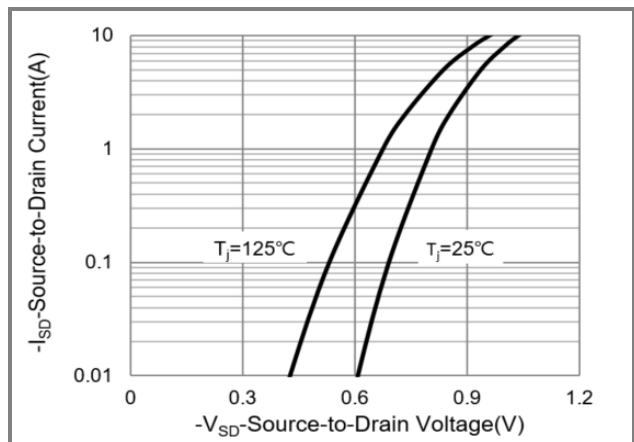


Fig.6 Body Diode Characteristics



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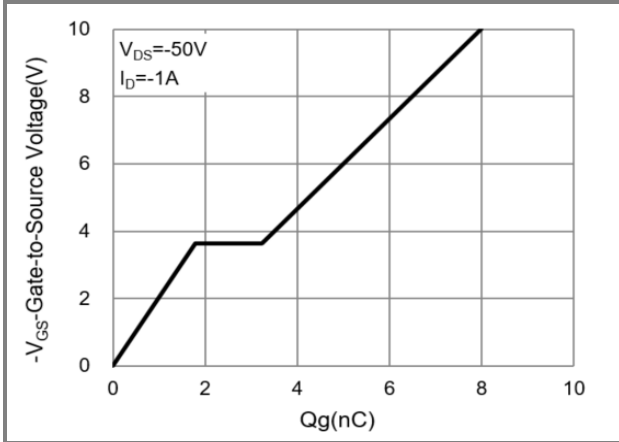


Fig.7 Gate-Charge Characteristics

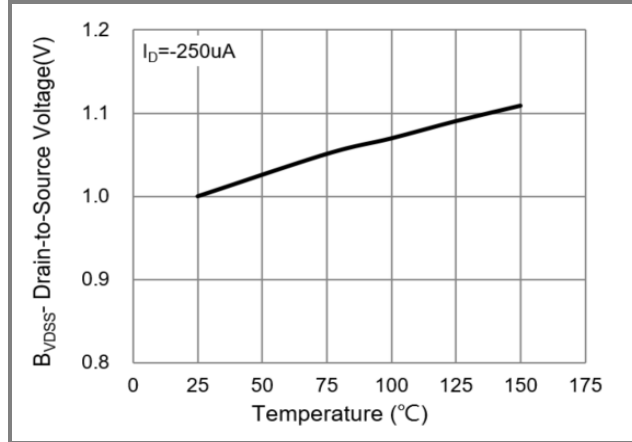


Fig.8 Breakdown Voltage Variation vs. Temperature

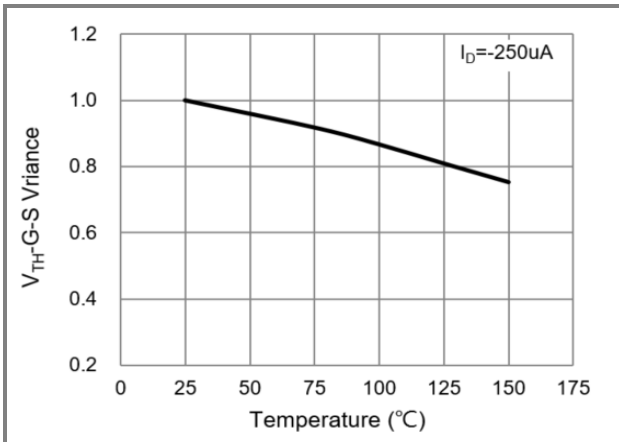


Fig.9 Threshold Voltage Variation with Temperature

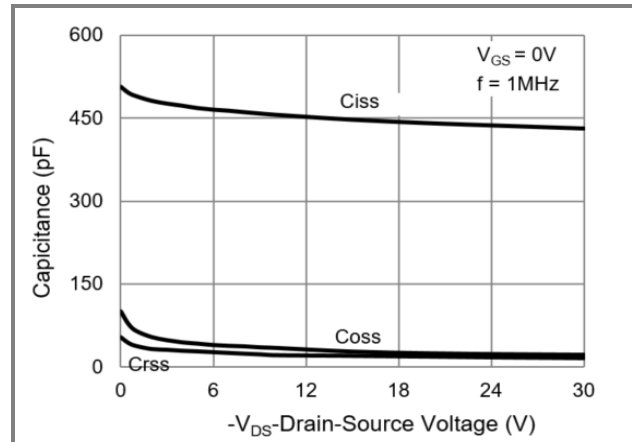


Fig.10 Capacitance vs. Drain-Source Voltage

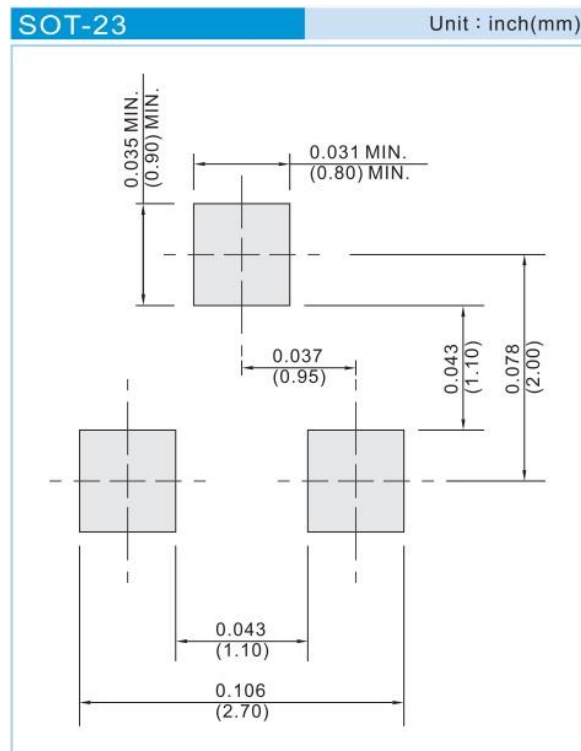


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Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3471_R1_00001	SOT-23	3K pcs / 7" reel	A71	Halogen free

Mounting Pad Layout





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