المعتثان
SEMI
CONDUCTOR

TO-252AA

(2)

3

Gate _

Drain

Source

60V P-Channel Enhancement Mode MOSFET



-60 V Current

Features

- R_{DS(ON)}, V_{GS}@-10V,I_D@-7.5A<68mΩ
- $R_{DS(ON)}, V_{GS}@-4.5V, I_D@-4.0A < 85m\Omega$ •
- High switching speed •
- Improved dv/dt capability •
- Low Gate Charge •
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0104 ounces, 0.297grams

Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

-15 A

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-60	V
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V
Continuous Drain Current	T _C =25°C	I _D	-15	
	T _C =100°C		-9.5	А
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	-60	
Power Dissipation	T _C =25°C	PD	25	
	T _C =100°C		10	W
Continuous Drain Current	T _A =25°C	I _D	-4.0	А
	T _A =70°C		-3.2	А
Power Dissipation	T _A =25°C		2.0	
Power Dissipation	T _A =70°C	PD	1.3	W
Single Pulse Avalanche Energy (Note 6)		E _{AS}	31	mJ
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance	Junction to Case	$R_{ extsf{ heta}JC}$	5.0	°0000
(Note 4,5)	Junction to Ambient	R _{θJA}	62.5	°C/W

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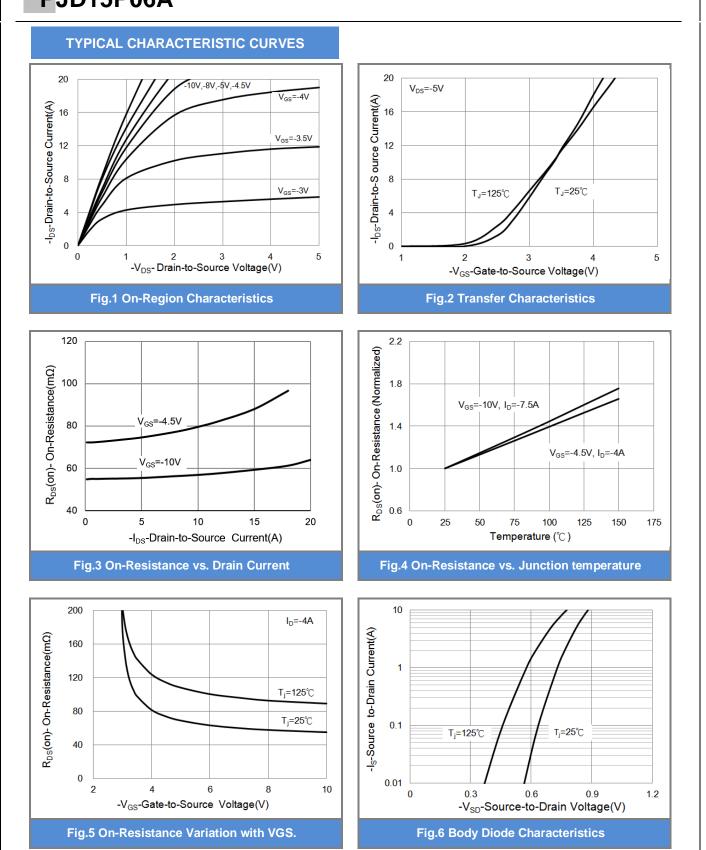
Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-60	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-1.0	-1.63	-2.5	V	
Drain-Source On-State Resistance	$R_{DS(on)}$	V _{GS} =-10V,I _D =-7.5A	-	55	68		
		V _{GS} =-4.5V,I _D =-4.0A	-	73	85	mΩ	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-60V, V_{GS} =0V	-	-	-1.0	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA	
Dynamic (Note 7)		·					
Total Gate Charge	Qg		-	17	-	nC	
Gate-Source Charge	Q_{gs}	V _{DS} =-30V, I _D =-7.5A, V _{GS} =-10V ^(Note 3)	-	2.8	-		
Gate-Drain Charge	Q _{gd}		-	3.6	-		
Input Capacitance	Ciss		-	879	-	pF	
Output Capacitance	Coss	V_{DS} =-30V, V_{GS} =0V,	-	70	-		
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	47	-		
Turn-On Delay Time	td _(on)		-	8.4	-	ns	
Turn-On Rise Time	t _r	V_{DD} =-30V, I_{D} =-1A,	-	30	-		
Turn-Off Delay Time	td _(off)	V _{GS} =-10V, R _G =6Ω	-	52	-		
Turn-Off Fall Time	t _f		-	16	-		
Drain-Source Diode							
Maximum Continuous Drain-Source					45	•	
Diode Forward Current	I _S		-	-	-15	A	
Reverse Recovery Time	V _{SD}	I _S =-1A,V _{GS} =0V	-	-0.73	-1.0	V	

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited
- 5. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
- 6. L=0.1mH, I_{AS} =-25A, V_{GS} =-10V, V_{DS} =-25V, R_{G} =25 ohm
- 7. Guaranteed by design, not subject to production testing.

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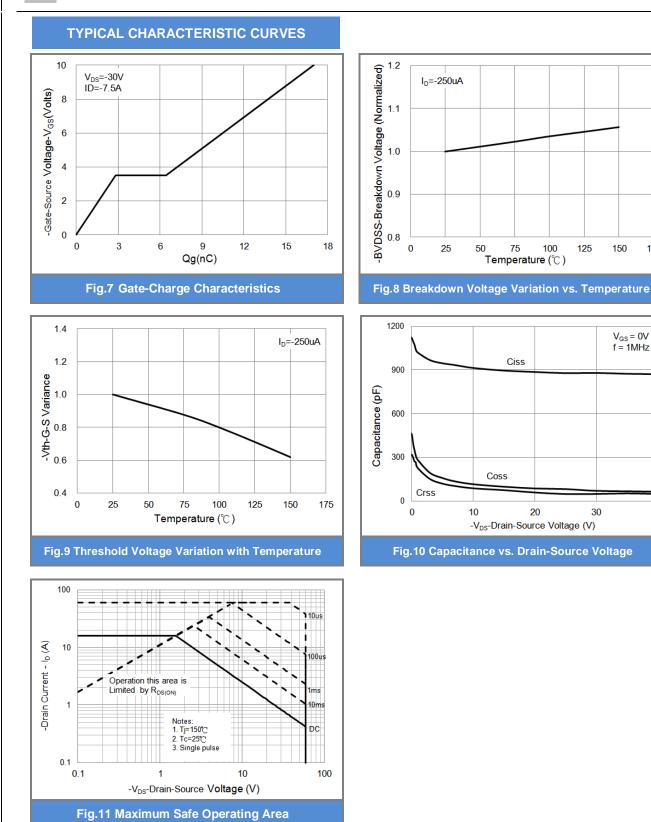




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July 26,2016-REV.01

Page 4





PJD15P06A



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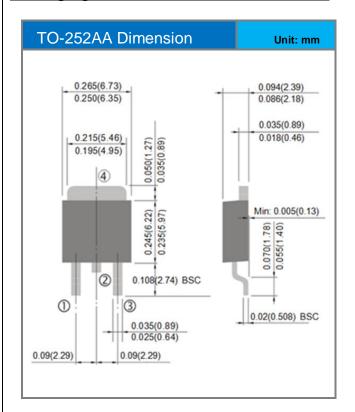
PJD15P06	6A					
TYPICAL CHA	ARACTERISTI	C CURVES				
0.01 0.000001	le Pulse			R _{TH}	$P_{K} = Tc + P_{DM} * Z_{TH-Jc} * R$ $H_{JC} = 5^{\circ}C / W$ $= 25^{\circ}C$ $= 25^{\circ}C$ $D = \frac{PW}{T}$	TH-JC
	0.0001	0.001	0.01	0.1	1	10

PAN





Packaging Information



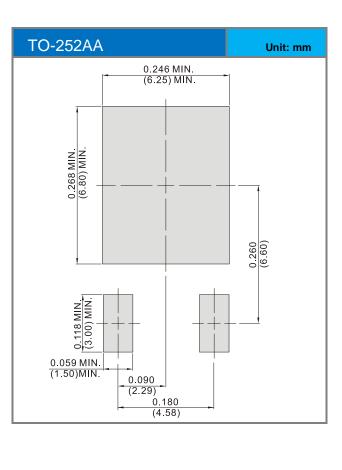




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Packing Type Marking	
PJD15P06A_L2_00001	TO-252AA	3,000pcs / 13" reel	D15P06A	Halogen free

MOUNTING PAD LAYOUT





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