



100V N-Channel Enhancement Mode MOSFET

Voltage

100 V

Current

25 A

Features

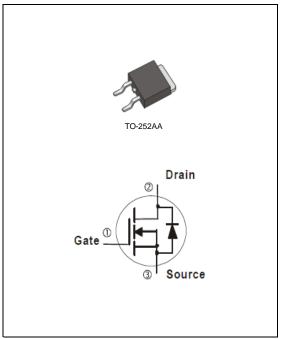
- $R_{DS(ON)}$, $V_{GS}@10V$, I_D $\overline{@15A<50m\Omega}$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@10A<55m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.. (Halogen Free)



• Case: TO-252AA Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0104 ounces, 0.297grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMET	ER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _C =25°C	l _D	25	A	
Continuous Drain Current	T _C =100°C		15		
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	100		
Davies Dissipation	T _C =25°C	60	107		
Power Dissipation	T _C =100°C	Pb	24	W	
Ocationary Ducie Ocasa	T _A =25°C	I _D	4.4	Α	
Continuous Drain Current	T _A =70°C		3.5	Α	
Power Dissipation	T _A =25°C	6	2.0	١٨/	
Power Dissipation	T _A =70°C	Po	1.3	W	
Single Pulse Avalanche Energy (Note 6)		E _{AS}	31	mJ	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C	
Typical Thermal Resistance	Junction to Case	$R_{ heta JC}$	2.1	°C/W	
(Note 4,5)	Junction to Ambient	$R_{ heta JA}$	62.5		

Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250uA	100	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1.0	1.5	2.5	V	
Drain-Source On-State Resistance	D	V _{GS} =10V,I _D =15A	-	37	50	mO	
Dialii-Source Oii-State Resistance	R _{DS(on)}	V _{GS} =4.5V,I _D =10A	-	38	55 mΩ	11177	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =80V, V_{GS} =0V	-	-	1.0	uA	
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA	
Dynamic (Note 7)							
Total Gate Charge	Q_{g}	\/ 00\/ 4EA	-	61	-	nC	
Gate-Source Charge	Q _{gs}	V_{DS} =80V, I_{D} =15A, V_{GS} =10V (Note 1,2)	-	8.8	-		
Gate-Drain Charge	Q_{gd}		-	11	-		
Input Capacitance	Ciss	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	3601	-	pF	
Output Capacitance	Coss		-	133	-		
Reverse Transfer Capacitance	Crss		-	65	-		
Turn-On Delay Time	td _(on)	V_{DD} =50V, I_{D} =15A, V_{GS} =10V, R_{G} =3.3 Ω (Note 1,2)	-	16	-	ns	
Turn-On Rise Time	t _r		-	50	-		
Turn-Off Delay Time	td _(off)		-	64	-		
Turn-Off Fall Time	t _f	K _G =3.312	-	18	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	I.				25	Α	
Diode Forward Current	I _S		-	-	20	^	
Diode Forward Voltage	V_{SD}	I _S =1A,V _{GS} =0V	-	0.7	1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited.
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH, I_{AS} =25A, V_{DD} =25V, V_{GS} =10V
- 7. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

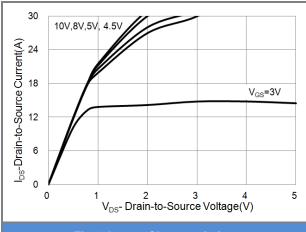


Fig.1 Output Characteristics

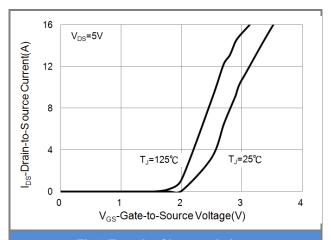


Fig.2 Transfer Characteristics

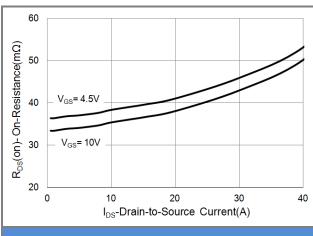


Fig.3 On-Resistance vs. Drain Current

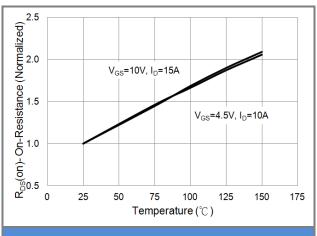
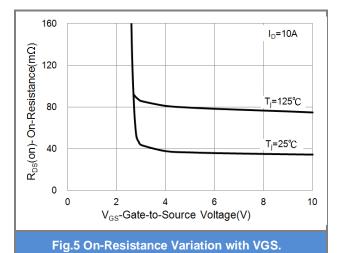


Fig.4 On-Resistance vs. Junction temperature



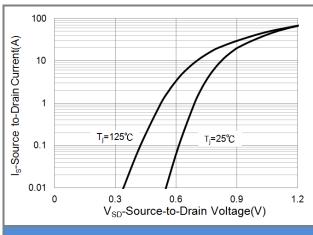


Fig.6 Source-Drain Diode Forward Voltage





TYPICAL CHARACTERISTIC CURVES

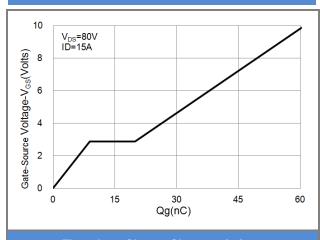


Fig.7 Gate-Charge Characteristics

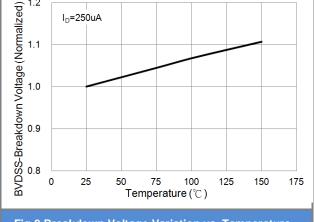


Fig.8 Breakdown Voltage Variation vs. Temperature

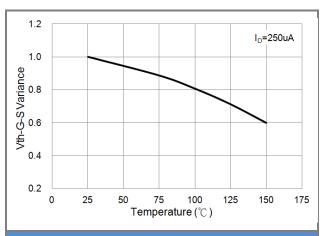


Fig.9 Threshold Voltage Variation with Temperature

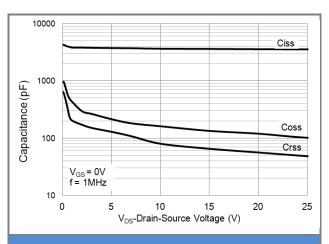
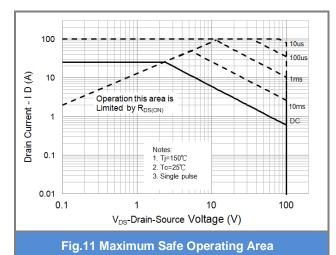


Fig.10 Capacitance vs. Drain-Source Voltage







TYPICAL CHARACTERISTIC CURVES

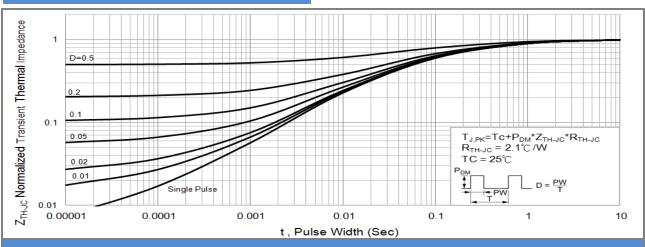
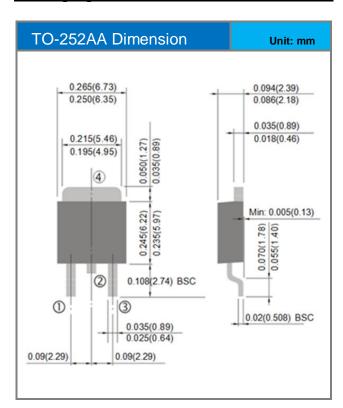


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width





Packaging Information



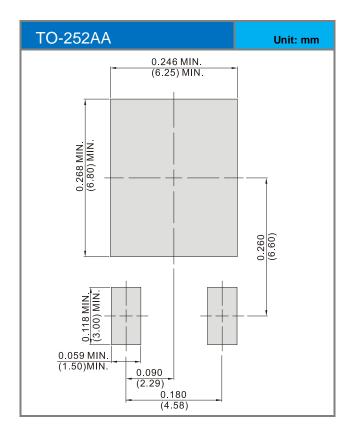




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJD25N10A_L2_00001	TO-252AA	3,000pcs / 13" reel	D25N10A	Halogen free	

MOUNTING PAD LAYOUT







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