



30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

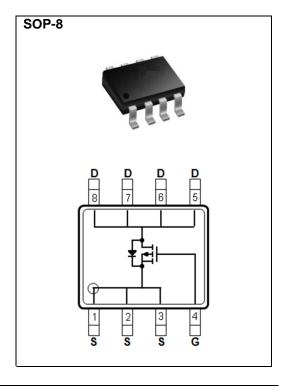
-10A

Features

- R_{DS(ON)}, V_{GS}@-10V, I_D@-10A<15.5mΩ
- $R_{DS(ON)}$, V_{GS} @-4.5V, I_D @-6A<23m Ω
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive).
- Green molding compound as per IEC61249 Std.. (Halogen Free)

Mechanical Data

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _A =25°C		-10		
	T _A =70°C	I _D	-8	Α	
Pulsed Drain Current (Note 1)		I _{DM}	-40		
Power Dissipation	T _A =25°C		1.7	W	
	T _A =70°C	P _D	1.1		
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C	
Typical Thermal Resistance					
- Junction to Ambient (Note 5)		$R_{\theta JA}$	73.5	°C/W	





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-30	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1.0	-1.6	-2.5			
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V,I _D =-10A -	12	15.5	0			
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V,I _D =-6A	-	19	23	mΩ		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1.0	uA		
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA		
Dynamic (Note 6)								
Total Gate Charge	Q_{g}	V _{DS} =-15V, I _D =-8A, V _{GS} =-4.5V ^(Note 1,2)	-	14	-	nC		
Gate-Source Charge	Q_gs		-	4.6	-			
Gate-Drain Charge	Q_gd		-	5.4	-			
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	1556	-	pF		
Output Capacitance	Coss		-	243	-			
Reverse Transfer Capacitance	Crss		-	175	-			
Turn-On Delay Time	td _(on)	V_{DD} =-15V, I_{D} =-1A, V_{GS} =-10V, R_{G} =6 Ω (Note 1,2)	-	7.3	-			
Turn-On Rise Time	tr		-	13	-			
Turn-Off Delay Time	td _(off)		-	88	-			
Turn-Off Fall Time	tf		-	48	-			
Drain-Source Diode								
Maximum Continuous Drain-Source			-	-	-10	А		
Diode Forward Current	I _S							
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.7	-1.0	V		

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

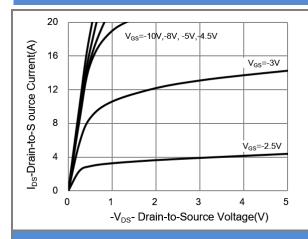


Fig.1 On-Region Characteristics

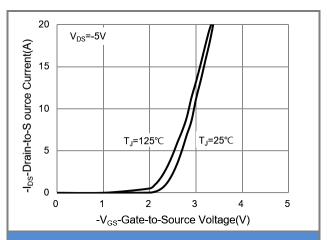


Fig.2 Transfer Characteristics

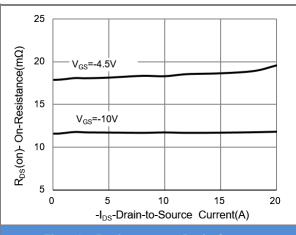


Fig.3 On-Resistance vs. Drain Current

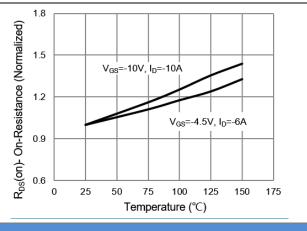
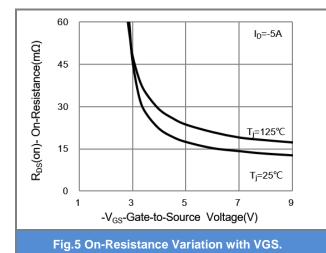


Fig.4 On-Resistance vs. Junction temperature

10



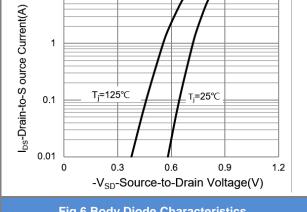


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

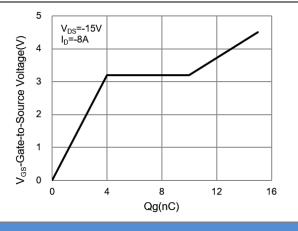
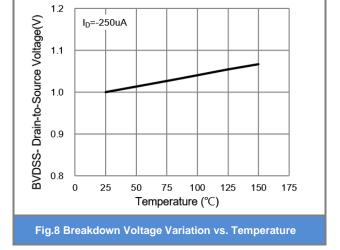


Fig.7 Gate-Charge Characteristics



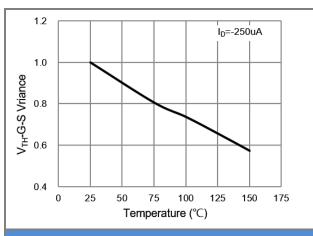


Fig.9 Threshold Voltage Variation with Temperature.

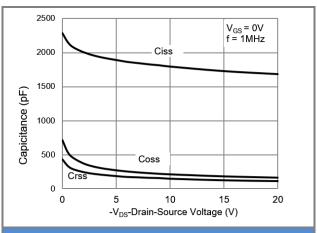


Fig.10 Capacitance vs. Drain-Source Voltage.

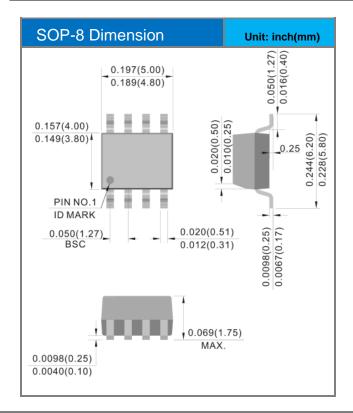


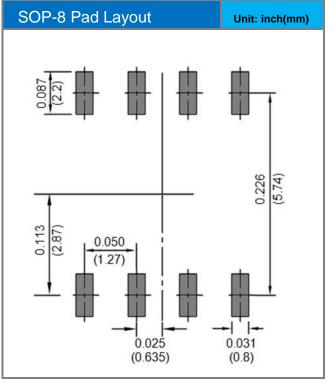


Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9413_R2_00001	SOP-8	2.5K pcs / 13" reel	L9413	Halogen free

Packaging Information & Mounting Pad Layout









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