



30V Dual N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

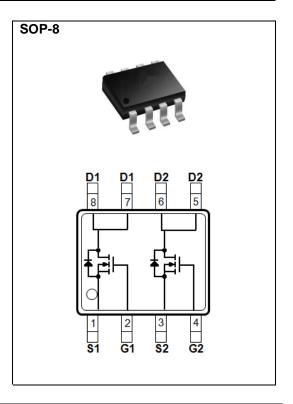
8 A

Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@8A<19m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_{D}@5A<29m\Omega$
- · High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std.. (Halogen Free)

Mechanical Data

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V_{DS}	30	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _A =25°C		8.0		
	T _A =70°C	I _D	6.0	Α	
Pulsed Drain Current (Note 1)		I _{DM}	32		
Power Dissipation	T _A =25°C		1.25	W	
	T _A =70°C	P _D	0.8		
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 5)		$R_{\theta JA}$	100	°C/W	





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V, I_D =250uA	30	-	-	V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1.2	1.7	2.5			
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V,I _D =8A	-	16	19	mΩ		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V,I _D =5A	-	23	29			
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	-	1.0	uA		
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA		
Dynamic (Note 6)								
Total Gate Charge	Q_g	V _{DS} =15V, I _D =8A, V _{GS} =4.5V ^(Note 2,3)	-	4.3	-	nC		
Gate-Source Charge	Q_gs		-	1.3	-			
Gate-Drain Charge	Q_gd		-	1.6	-			
Input Capacitance	Ciss	V _{DS} =25V, V _{GS} =0V, f=1.0MHZ	-	392	-	pF		
Output Capacitance	Coss		-	76	-			
Reverse Transfer Capacitance	Crss	I=1.0IVINZ	-	54	-			
Turn-On Delay Time	td _(on)	V_{DS} =15V, I_{D} =1A, V_{GS} =10V, R_{G} =6 Ω (Note 2,3)	-	5.9	-			
Turn-On Rise Time	tr		-	11	-			
Turn-Off Delay Time	td _(off)		-	17	-			
Turn-Off Fall Time	tf		-	3.8	-			
Drain-Source Diode								
Maximum Continuous Drain-Source	1			8.0	8 O	А		
Diode Forward Current	I _S		-		0.0			
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	0.73	1.0	V		

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- $2. \quad \hbox{Essentially independent of operating temperature typical characteristics.}$
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

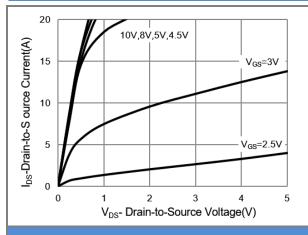


Fig.1 On-Region Characteristics

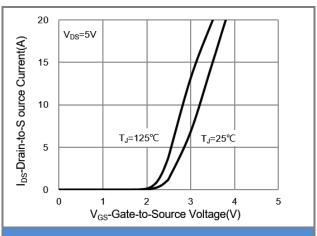


Fig.2 Transfer Characteristics

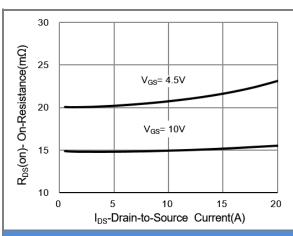


Fig.3 On-Resistance vs. Drain Current

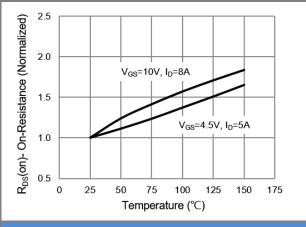
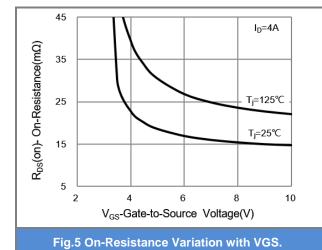
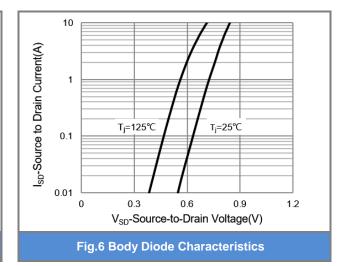


Fig.4 On-Resistance vs. Junction temperature









TYPICAL CHARACTERISTIC CURVES

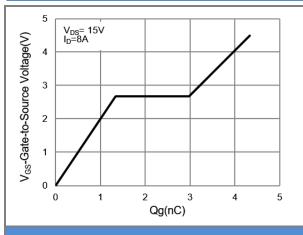


Fig.7 Gate-Charge Characteristics

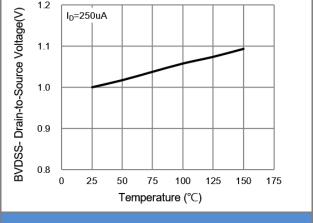


Fig.8 Breakdown Voltage Variation vs. Temperature

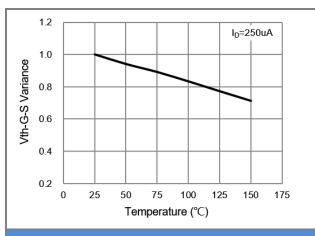


Fig.9 Threshold Voltage Variation with Temperature.

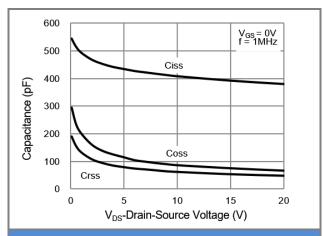


Fig.10 Capacitance vs. Drain-Source Voltage.

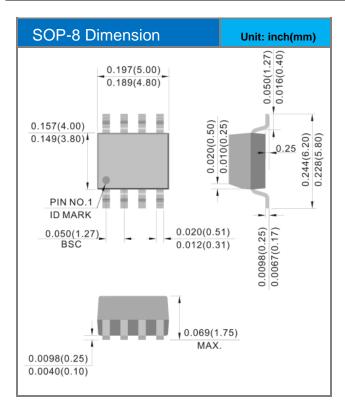


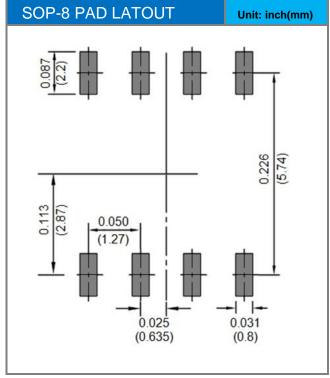


Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9808_R2_00001	SOP-8	2.5K pcs / 13" reel	L9808	Halogen free

Packaging Information & Mounting Pad Layout









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