

Solid-state relays - ST-OV3-120AC/220DC/1 - 2903163

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Plug-in power solid-state relay, with LED and protective circuit in input and output circuits, input: 120 V AC, output: 15 - 220 V DC/max. 1 A, can be plugged into basic terminal blocks

The illustration shows version ST-OV
3- 5 DC/220 DC/1



Key commercial data

Packing unit	1 pc
GTIN	 4 017918 078959
Weight per Piece (excluding packing)	58.58 GRM
Custom tariff number	85364900
Country of origin	Germany

Technical data

Dimensions

Width	20.8 mm
Height	33 mm
Depth	66.5 mm

Ambient conditions

Ambient temperature (operation)	-20 °C ... 60 °C
Ambient temperature (storage/transport)	-20 °C ... 70 °C

Input data

Nominal input voltage U_N	120 V AC ± 20 %
Switching threshold "0" signal, voltage	≤ 10.5 V
Switching threshold "1" signal voltage	≥ 29 V
Typical input current at U_N	5 mA

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Technical data

Input data

Typical response time	4 ms
Typical turn-off time	35 ms
Transmission frequency	10 Hz

Output data

Output voltage range	15 V DC ... 220 V DC
Limiting continuous current	1 A (see derating curve)
Surge current	2 A (t = 1 s)
Peak offstate voltage	220 V DC (Collector-emitter reverse voltage)
Voltage drop at max. limiting continuous current	1.2 V
Output circuit	2-wire, floating
Type of protection	Protection against polarity reversal
	Surge protection
Protective circuit/component	Polarity protection diode

General

Test voltage input/output	2.5 kV AC
Mounting position	any
Standards/regulations	DIN VDE 0110

Classifications

eCl@ss

eCl@ss 4.0	27371102
eCl@ss 4.1	27371102
eCl@ss 5.0	27371001
eCl@ss 5.1	27371001
eCl@ss 6.0	27371001
eCl@ss 7.0	27371001
eCl@ss 8.0	27371001

ETIM

ETIM 2.0	EC001504
ETIM 3.0	EC001504
ETIM 4.0	EC001504
ETIM 5.0	EC001504

UNSPSC

UNSPSC 6.01	30211916
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Classifications

UNSPSC

UNSPSC 7.0901	39121542
UNSPSC 11	39121542
UNSPSC 12.01	39121542
UNSPSC 13.2	39121542

Approvals

Approvals

Approvals

GOST / GOST

Ex Approvals

Approvals submitted

Approval details

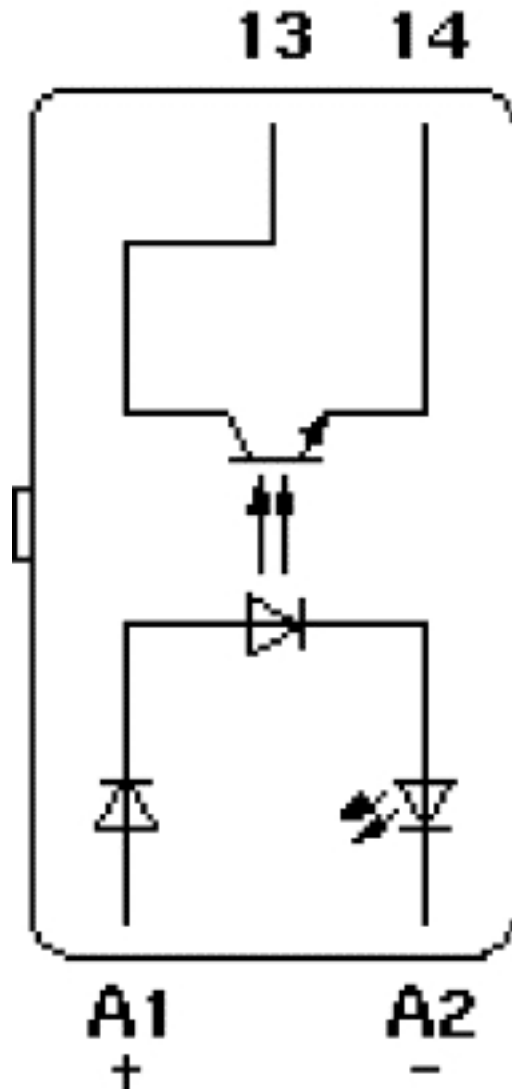
GOST

GOST

Drawings

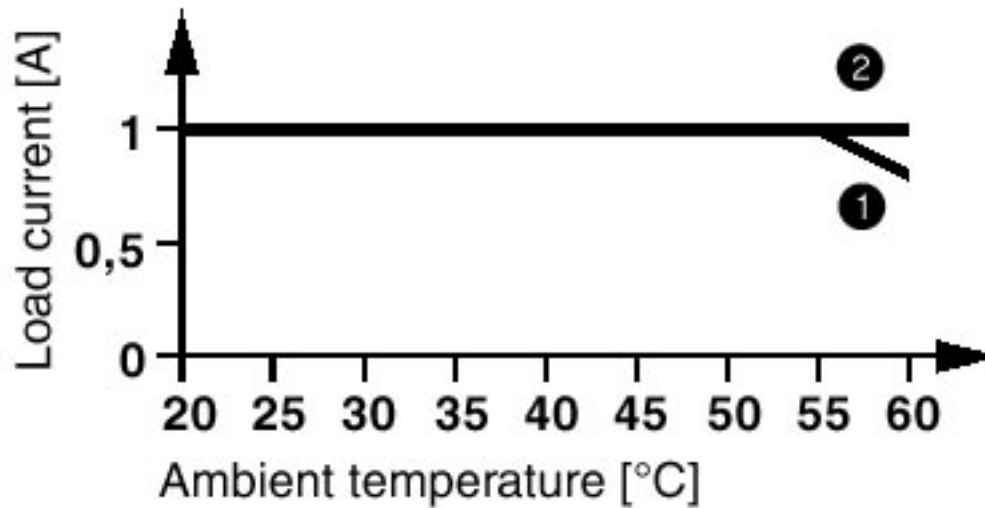
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Circuit diagram



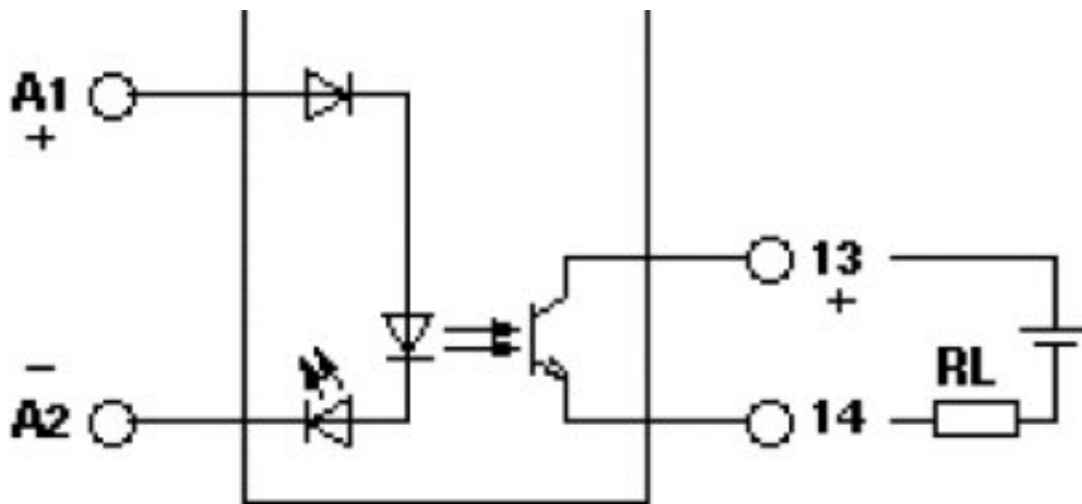
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Diagram



- ① mounted in rows with zero spacing.
- ② power optocouplers mounted in rows, ≥ 20 mm.

Circuit diagram



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