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LSA-PLUS plug with protection for conductor pairs in floating signal circuits. Nominal voltage: 12 V DC

The illustration shows version CTM 1x2- 24 DC

Product Features

- The CTM 10-MAG surge protection magazine can be freely fitted with various protective plugs
- ☑ Can be used in LSA-PLUS disconnect and control strips or CT-TERMIBLOCK





Key commercial data

Packing unit	1 pc
Weight per Piece (excluding packing)	10.03 GRM
Custom tariff number	85363010
Country of origin	Germany

Technical data

Dimensions

Height	21 mm
Width	9.5 mm
Depth	53.5 mm

Ambient conditions

Ambient temperature (operation)	-25 °C 75 °C
Degree of protection	IP20

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Technical data

General

Housing material	PA
Inflammability class according to UL 94	V0
Color	black
Standards for air and creepage distances	DIN VDE 0110-1
	IEC 60664-1
Surge voltage category	II
Pollution degree	2
Mounting type	On CT-TERMIBLOCK and LSA-PLUS disconnect strip
Туре	LSA-PLUS module
Number of positions	2
Direction of action	Line-Line & Line-Earth Ground
Arrester can be tested with CHECKMASTER from software version:	From SW rev. 1.10

Protective circuit

	T-:
IEC test classification	B2
	C1
	C2
	C3
	D1
VDE requirement class	B2
	C1
	C2
	C3
	D1
Nominal voltage U _N	12 V DC
Maximum continuous operating voltage U _C	± 15 V DC
	10 V AC
Maximum continuous voltage UC (wire-wire)	± 15 V DC
	10 V AC
Maximum continuous voltage U _C (wire-ground)	72 V DC
Nominal current I _N	380 mA (25 °C)
Operating effective current I _C at U _C	≤ 5 µA
Residual current I _{PE}	≤ 2 µA
Nominal discharge current I _n (8/20) µs (Core-Core)	5 kA
Nominal discharge current I _n (8/20) µs (Core-Earth)	5 kA
Total surge current (8/20) μs	10 kA
Total surge current (10/350) µs	2.5 kA

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Technical data

Protective circuit

Max. discharge current I _{max} (8/20) μs maximum (Core-Earth) 10 kA (in total) Nominal pulse current lan (10/1000) μs (Core-Core) 100 A Impuise discharge current (10/1000) μs (Core-Earth) 100 A Output voltage limitation at 1 kV/µs (Core-Core) spike 2 45 V Output voltage limitation at 1 kV/µs (Core-Earth) spike 2 700 V Output voltage limitation at 1 kV/µs (Core-Earth) spike 2 700 V Output voltage limitation at 1 kV/µs (Core-Earth) static 2 5 V Output voltage limitation at 1 kV/µs (Core-Earth) static 2 700 V Residual voltage at I _m (conductor-conductor) 2 22 V Residual voltage with lan (10/1000)µs (conductor-conductor) 2 25 V Residual voltage with lan (10/1000)µs (conductor-ground) 2 25 V Residual voltage with lan (10/1000)µs (conductor-ground) 2 25 V Voltage protection level U _T (Core-Core) 4 40 V (C2, 10 kV/5 kA, spike) 2 5 V (C2, 10 kV/5 kA, spike) 2 25 V (C2, 10 kV/5 kA, spike) 4 5 V (C2, 10 kV/5 kA, spike) 4 5 V (C2, 10 kV/5 kA, spike) 4 5 V (C2, 10 kV/5 kA, spike) 4 5 V (C2, 10 kV/5 kA, spike) 4 5 V (C2, 10 kV/5 kA, spike) 4 5 V (C2, 10 kV/5 kA, spike) 5 V (C2, 10 kV/5 kA, spike) 4		
Nominal pulse current I an (10/1000) μs (Core-Earth) 100 A Impulse discharge current (10/350)#μs, peak value I _{lusp} 1 kA Output voltage limitation at 1 kV/μs (Core-Core) spike ≤ 45 V Output voltage limitation at 1 kV/μs (Core-Earth) spike ≤ 700 V Output voltage limitation at 1 kV/μs (Core-Core) static ≤ 25 V Output voltage at I _{li} (conductor-conductor) ≤ 22 V Residual voltage at I _{li} (conductor-conductor) ≤ 25 V Residual voltage with Ian (10/1000)µs (conductor-conductor) ≤ 25 V Residual voltage with Ian (10/1000)µs (conductor-ground) ≤ 25 V Voltage protection level U _P (Core-Core) ≤ 40 V (C2 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, spike) √ 25 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, spike) √ 26 V (C2, 10 kV/5 kA, spike) ≤ 45 V (C2, 10 kV/5 kA, spike) √ 27 V (C2, 10 kV/5 kA, spike) ≤ 45 V (C2, 10 kV/5 kA, spike) √ 27 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, spike) √ 28 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, spike) √ 28 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, spike) √ 27 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 k	Max. discharge current I _{max} (8/20) μs maximum (Core-Earth)	10 kA (in total)
Impulse discharge current (10/350)#js, peak value I _{rro} 1 kA Output voltage limitation at 1 kVlys (Core-Core) spike ≤ 45 V Output voltage limitation at 1 kVlys (Core-Earth) spike ≤ 700 V Output voltage limitation at 1 kVlys (Core-Earth) spike ≤ 700 V Residual voltage imitation at 1 kVlys (Core-Core) static ≤ 700 V Residual voltage at I _{II} , (conductor-conductor) ≤ 22 V Residual voltage with Ian (10/1000)µs (conductor-conductor) ≤ 25 V Residual voltage with Ian (10/1000)µs (conductor-ground) ≤ 25 V Voltage protection level U _{II} (Core-Core) ≤ 40 V (C2, 10 kV/5 kA, spike) < 25 V (C2, 10 kV/5 kA, static)	Nominal pulse current lan (10/1000) μs (Core-Core)	100 A
Output voltage limitation at 1 kV/jus (Core-Core) spike ≤ 45 V Output voltage limitation at 1 kV/jus (Core-Earth) spike ≤ 700 V Output voltage limitation at 1 kV/jus (Core-Core) static ≤ 25 V Output voltage limitation at 1 kV/jus (Core-Earth) static ≤ 700 V Residual voltage at I _m (conductor-conductor) ≤ 22 V Residual voltage at I _m (conductor-ground) ≤ 45 V Residual voltage with lan (10/1000)jus (conductor-ground) ≤ 25 V Voltage protection level U _P (Core-Core) ≤ 40 V (C2, 10 kV/5 kA, spike) Voltage protection level U _P (Core-Earth) ≤ 25 V (C2, 10 kV/5 kA, spike) Voltage protection level U _P (Core-Earth) ≤ 700 V (C3, 7.5 kV/100 A) Voltage protection level U _P (Core-Earth) ≤ 700 V (C3, 7.5 kV/100 A, spike) 2 20 V (C3, 7.5 kV/100 A, spike) ≤ 20 V (C3, 7.5 kV/100 A, spike) 2 20 V (C3, 7.5 kV/100 A, spike) ≤ 20 V (C3, 7.5 kV/100 A, spike) 3 20 V (C3, 7.5 kV/100 A, spike) ≤ 20 V (C3, 7.5 kV/100 A, spike) 4 2 V (C2, 10 kV/5 kA, static) ≤ 20 V (C3, 7.5 kV/100 A, spike) 4 2 V (C2, 10 kV/5 kA, static) ≤ 20 V (C3, 7.5 kV/100 A, spike) 4 2 V (C3, 7.5 kV/100 A, spike) ≤ 20 V (C3, 7.5 kV/100 A, spike) 4 2 V (Nominal pulse current lan (10/1000) μs (Core-Earth)	100 A
Output voltage limitation at 1 kV/µs (Core-Earth) spike ≤ 700 V Output voltage limitation at 1 kV/µs (Core-Core) static ≤ 25 V Output voltage limitation at 1 kV/µs (Core-Earth) static ≤ 700 V Residual voltage at I _n (conductor-conductor) ≤ 22 V Residual voltage with lan (10/1000)µs (conductor-ground) ≤ 25 V Residual voltage with lan (10/1000)µs (conductor-ground) ≤ 25 V Voltage protection level U _P (Core-Core) ≤ 40 V (C2, 10 kV/5 kA, spike) < 25 V (C2, 7.5 kV/100 A)	Impulse discharge current (10/350)#µs, peak value l _{imp}	1 kA
Output voltage limitation at 1 kV/μs (Core-Core) static ≤ 25 V Output voltage limitation at 1 kV/μs (Core-Earth) static ≤ 700 V Residual voltage at I _m (conductor-ground) ≤ 22 V Residual voltage with lan (10/1000)µs (conductor-conductor) ≤ 25 V Residual voltage with lan (10/1000)µs (conductor-ground) ≤ 25 V Residual voltage with lan (10/1000)µs (conductor-ground) ≤ 25 V Voltage protection level U _P (Core-Core) ≤ 40 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, static) ≤ 25 V (C3, 7.5 kV/100 A) Voltage protection level U _P (Core-Earth) < 700 V (C2, 10 kV/5 kA, spike)	Output voltage limitation at 1 kV/µs (Core-Core) spike	≤ 45 V
Output voltage limitation at 1 kV/μs (Core-Earth) static ≤ 700 V Residual voltage at I _{In.} (conductor-conductor) ≤ 22 V Residual voltage at I _{In.} (conductor-ground) ≤ 45 V Residual voltage with lan (10/1000)µs (conductor-ground) ≤ 25 V Residual voltage with lan (10/1000)µs (conductor-ground) ≤ 25 V Voltage protection level U _P (Core-Core) ≤ 40 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, static) ≤ 25 V (C3, 7.5 kV/100 A) Voltage protection level U _P (Core-Earth) ≤ 700 V (C2, 10 kV/5 kA, static) ≤ 25 V (C2, 10 kV/5 kA, static) ≤ 45 V (C2, 10 kV/5 kA, static) ≤ 25 V (C3, 7.5 kV/100 A, spike) ≤ 25 V (C2, 10 kV/5 kA, static) € 700 V (C3, 7.5 kV/100 A, spike) ≤ 20 V (C3, 7.5 kV/100 A, spike) € 20 V (C3, 7.5 kV/100 A, spike) ≤ 1 ns Response time tA (Core-Core) ≤ 1 ns Response time tA (Core-Earth) ≤ 100 ns Input attenuation aE, sym. 0.3 dB (≤ 400 kHz) Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) 1.5 nF (f=1 MHz / V _R = 0 V) Resistance in series 3.3 Ω Surge protection fault message None </td <td>Output voltage limitation at 1 kV/µs (Core-Earth) spike</td> <td>≤ 700 V</td>	Output voltage limitation at 1 kV/µs (Core-Earth) spike	≤ 700 V
Residual voltage at I _m (conductor-conductor) \$ 22 V Residual voltage at I _m (conductor-ground) \$ 45 V Residual voltage with Ian (10/1000)µs (conductor-conductor) \$ 25 V Residual voltage with Ian (10/1000)µs (conductor-ground) \$ 25 V Residual voltage with Ian (10/1000)µs (conductor-ground) \$ 25 V Residual voltage with Ian (10/1000)µs (conductor-ground) \$ 25 V Voltage protection level U _P (Core-Core) \$ 40 V (C2, 10 kV/5 kA, spike) \$ 25 V (C3, 7.5 kV/100 A) Voltage protection level U _P (Core-Earth) \$ 700 V (C2, 10 kV/5 kA, static) \$ 45 V (C3, 7.5 kV/100 A, spike) \$ 20 V (C3, 7.5 kV/100 A, static) \$ 88	Output voltage limitation at 1 kV/µs (Core-Core) static	≤ 25 V
Residual voltage at I _m (conductor-ground) ≤ 45 V Residual voltage with Ian (10/1000)µs (conductor-ground) ≤ 25 V Residual voltage with Ian (10/1000)µs (conductor-ground) ≤ 25 V Voltage protection level U _P (Core-Core) ≤ 40 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, static) ≤ 25 V (C2, 10 kV/5 kA, spike) Voltage protection level U _P (Core-Earth) ≤ 700 V (C2, 10 kV/5 kA, spike) Voltage protection level U _P (Core-Earth) ≤ 700 V (C3, 7.5 kV/100 A, spike) 2 V (C3, 7.5 kV/100 A, spike) ≤ 20 V (C3, 7.5 kV/100 A, spike) 2 V (C3, 7.5 kV/100 A, static) ≤ 20 V (C3, 7.5 kV/100 A, static) Response time tA (Core-Core) ≤ 1 ns Response time tA (Core-Earth) ≤ 100 ns Input attenuation aE, sym. 0.3 dB (≤ 400 kHz) Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) 1.5 nF (r=1 MHz / V _R = 0 V) Resistance in series 3.3 Ω 10 % Surge protection fault message None Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C3 - 100 A <td>Output voltage limitation at 1 kV/µs (Core-Earth) static</td> <td>≤ 700 V</td>	Output voltage limitation at 1 kV/µs (Core-Earth) static	≤ 700 V
Residual voltage with Ian (10/1000)µs (conductor-conductor) $\leq 25 \text{ V}$ Residual voltage with Ian (10/1000)µs (conductor-ground) $\leq 25 \text{ V}$ Voltage protection level Uµ (Core-Core) $\leq 40 \text{ V (C2, 10 kV/5 kA, spike)}$ $\leq 25 \text{ V (C2, 10 kV/5 kA, spike)}$ $\leq 25 \text{ V (C2, 10 kV/5 kA, static)}$ $\leq 25 \text{ V (C3, 7.5 kV/100 A)}$ Voltage protection level Uµ (Core-Earth) $\leq 700 \text{ V (C2, 10 kV/5 kA, spike)}$ $\leq 45 \text{ V (C2, 10 kV/5 kA, spike)}$ $\leq 45 \text{ V (C2, 10 kV/5 kA, spike)}$ $\leq 45 \text{ V (C2, 10 kV/5 kA, spike)}$ $\leq 45 \text{ V (C2, 10 kV/5 kA, spike)}$ $\leq 20 \text{ V (C3, 7.5 kV/100 A, spike)}$ $\leq 20 \text{ V (C3, 7.5 kV/100 A, spike)}$ $\leq 20 \text{ V (C3, 7.5 kV/100 A, spike)}$ Response time tA (Core-Core) $\leq 1 \text{ ns}$ Response time tA (Core-Earth) $\leq 100 \text{ ns}$ Input attenuation aE, sym. $\leq 30 \text{ ds} \leq 400 \text{ kHz}$ Cut-off frequency fg (3 dB), sym. in 100 Ohm system $\leq 1.2 \text{ MHz}$ Capacity (Core-Core) $\leq 1.5 \text{ nF (f=1 MHz / V}_R = 0 \text{ V})$ Resistance in series $\leq 3.3 \Omega \log 8 \approx 10.0 \%$ Surge protection fault message $\leq 1.5 \text{ None}$ Surge current resistance (conductor-conductor) $\leq 24 \text{ kV/12 kA}$ C3 - 100 A Surge current resistance (conductor-ground) $\leq 24 \text{ kV/100 A}$ Surge current resistance (conductor-ground) $\leq 24 \text{ kV/100 A}$ Surge current resistance (conductor-ground) $\leq 24 \text{ kV/100 A}$	Residual voltage at I _n , (conductor-conductor)	≤ 22 V
Residual voltage with Ian (10/1000)µs (conductor-ground) ≤ 25 V Voltage protection level U _P (Core-Core) ≤ 40 V (C2, 10 kV/5 kA, spike) ≤ 25 V (C2, 10 kV/5 kA, static) ≤ 25 V (C3, 7.5 kV/100 A) Voltage protection level U _P (Core-Earth) ≤ 700 V (C2, 10 kV/5 kA, spike) ≤ 45 V (C2, 10 kV/5 kA, spike) ≤ 45 V (C3, 7.5 kV/100 A, static) ≤ 700 V (C3, 7.5 kV/100 A, static) ≤ 700 V (C3, 7.5 kV/100 A, static) Response time tA (Core-Core) ≤ 1 ns Response time tA (Core-Earth) ≤ 100 ns Input attenuation aE, sym. 0.3 dB (≤ 400 kHz) Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) 1.5 nF (f=1 MHz / V _R = 0 V) Resistance in series 3.3 Ω 10 % Surge protection fault message None Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C2 (4 kV / 2 kA) C3 - 100 A B2 - 4 kV/100 A B2 - 4 kV/100 A D1 - 1 kA	Residual voltage at I _n , (conductor-ground)	≤ 45 V
Voltage protection level U _P (Core-Core) ≤ 40 ∨ (C2, 10 kV/5 kA, spike) ≤ 25 ∨ (C2, 10 kV/5 kA, static) ≤ 25 ∨ (C3, 7.5 kV/100 A) Voltage protection level U _P (Core-Earth) ≤ 700 ∨ (C2, 10 kV/5 kA, spike) ≤ 45 ∨ (C2, 10 kV/5 kA, spike) ≤ 45 ∨ (C3, 7.5 kV/100 A, spike) ≤ 700 ∨ (C3, 7.5 kV/100 A, spike) ≤ 20 ∨ (C3, 7.5 kV/100 A, static) Response time tA (Core-Core) ≤ 1 ns Response time tA (Core-Earth) ≤ 100 ns Input attenuation aE, sym. 0.3 dB (≤ 400 kHz) Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) 1.5 nF (f=1 MHz / V _R = 0 V) Resistance in series 3.3 Ω 10 % Surge protection fault message None Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C2 (4 kV / 2 kA) C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA D1 - 1 kA	Residual voltage with Ian (10/1000)µs (conductor-conductor)	≤ 25 V
\$25 \cdot	Residual voltage with Ian (10/1000)µs (conductor-ground)	≤ 25 V
$ \leq 25 \text{ V (C3, 7.5 \text{ kV/100 A})} $ $ \leq 700 \text{ V (C2, 10 \text{ kV/5 kA, spike})} $ $ \leq 45 \text{ V (C2, 10 \text{ kV/5 kA, spike})} $ $ \leq 45 \text{ V (C2, 10 \text{ kV/5 kA, spike})} $ $ \leq 20 \text{ V (C3, 7.5 \text{ kV/100 A, spike})} $ $ \leq 20 V (C3, 7.5 \text{ kV/100 A$	Voltage protection level U _P (Core-Core)	≤ 40 V (C2, 10 kV/5 kA, spike)
$ \begin{array}{llllllllllllllllllllllllllllllllllll$		≤ 25 V (C2, 10 kV/5 kA, static)
		≤ 25 V (C3, 7.5 kV/100 A)
= 700 V (C3, 7.5 kV/100 A, spike) $ = 20 V (C3, 7.5 kV/100 A, static) $ Response time tA (Core-Core) $ = 1 ns $ Response time tA (Core-Earth) $ = 100 ns $ Input attenuation aE, sym. $ = 0.3 dB ($	Voltage protection level U _P (Core-Earth)	≤ 700 V (C2, 10 kV/5 kA, spike)
$ \leq 20 \ V \ (C3, 7.5 \ kV/100 \ A, \ static) $ Response time tA (Core-Core) $ \leq 1 \ ns $ Response time tA (Core-Earth) $ \leq 100 \ ns $ Input attenuation aE, sym. $ 0.3 \ dB \ (\leq 400 \ kHz) $ Cut-off frequency fg (3 dB), sym. in 100 Ohm system $ 1.2 \ MHz $ Capacity (Core-Core) $ 1.5 \ nF \ (f=1 \ MHz \ / \ V_R = 0 \ V) $ Resistance in series $ 3.3 \ \Omega \ 10 \ \% $ 3.3 Ω Surge protection fault message $ None $ Surge current resistance (conductor-conductor) $ C2 \ (4 \ kV/2 \ kA) $ C3 - 100 A $ B2 - 4 \ kV/100 \ A $ Surge current resistance (conductor-ground) $ C2 \ (4 \ kV \ / 2 \ kA) $ C3 - 100 A $ B2 - 4 \ kV/100 \ A $ Surge current resistance (conductor-ground) $ C2 \ (4 \ kV \ / 2 \ kA) $ C3 - 100 A $ C3 - 100 \ A $ D1 - 1 kA		≤ 45 V (C2, 10 kV/5 kA, static)
Response time tA (Core-Core) $≤ 1 \text{ ns}$ Response time tA (Core-Earth) $≤ 100 \text{ ns}$ Input attenuation aE, sym. $0.3 \text{ dB} (≤ 400 \text{ kHz})$ Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) $1.5 \text{ nF} (\text{f=1 MHz} / \text{V}_{\text{R}} = 0 \text{ V})$ Resistance in series $3.3 \Omega 10 \%$ Surge protection fault message None Surge current resistance (conductor-conductor) $C2 (4 \text{ kV} / 2 \text{ kA})$ C3 - 100 A Surge current resistance (conductor-ground) $C2 (4 \text{ kV} / 2 \text{ kA})$ C3 - 100 A $C3 - 100 \text{ A}$		≤ 700 V (C3, 7.5 kV/100 A, spike)
Response time tA (Core-Earth) $\leq 100 \text{ ns}$ Input attenuation aE, sym. $0.3 \text{ dB} (\leq 400 \text{ kHz})$ Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) $1.5 \text{ nF} (\text{f=1 MHz} / \text{V}_{\text{R}} = 0 \text{ V})$ Resistance in series $3.3 \Omega 10 \%$ Surge protection fault message None Surge current resistance (conductor-conductor) $C2 (4 \text{ kV/2 kA})$ $C3 - 100 \text{ A}$ Surge current resistance (conductor-ground) $C2 (4 \text{ kV} / 2 \text{ kA})$ $C3 - 100 \text{ A}$ Surge current resistance (conductor-ground) $C2 (4 \text{ kV} / 2 \text{ kA})$ $C3 - 100 \text{ A}$		≤ 20 V (C3, 7.5 kV/100 A, static)
Input attenuation aE, sym. 0.3 dB (\leq 400 kHz) Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) 1.5 nF (f=1 MHz / V_R = 0 V) Resistance in series 3.3 Ω 10 % 3.3 Ω Surge protection fault message None Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA	Response time tA (Core-Core)	≤ 1 ns
Cut-off frequency fg (3 dB), sym. in 100 Ohm system 1.2 MHz Capacity (Core-Core) 1.5 nF (f=1 MHz / V _R = 0 V) Resistance in series 3.3 Ω 10 % Surge protection fault message None Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C2 (4 kV / 2 kA) C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA D1 - 1 kA	Response time tA (Core-Earth)	≤ 100 ns
Capacity (Core-Core) $1.5 \text{ nF (f=1 MHz / V}_{R} = 0 \text{ V})$ Resistance in series $3.3 \Omega 10 \%$ 3.3Ω Surge protection fault message None $C2 (4 \text{ kV/2 kA})$ $C3 - 100 \text{ A}$ $B2 - 4 \text{ kV/100 A}$ Surge current resistance (conductor-ground) $C2 (4 \text{ kV / 2 kA})$ $C3 - 100 \text{ A}$ $D1 - 1 \text{ kA}$	Input attenuation aE, sym.	0.3 dB (≤ 400 kHz)
Resistance in series 3.3 Ω 10 % Surge protection fault message None Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C2 (4 kV / 2 kA) C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA D1 - 1 kA	Cut-off frequency fg (3 dB), sym. in 100 Ohm system	1.2 MHz
3.3 Ω	Capacity (Core-Core)	1.5 nF (f=1 MHz / V _R = 0 V)
Surge protection fault message Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C3 - 100 A C3 - 100 A B2 - 4 kV/100 A C3 - 100 A D1 - 1 kA	Resistance in series	3.3 Ω 10 %
Surge current resistance (conductor-conductor) C2 (4 kV/2 kA) C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C2 (4 kV / 2 kA) C3 - 100 A B2 - 4 kV/100 A B2 - 4 kV/100 A D1 - 1 kA		3.3 Ω
C3 - 100 A B2 - 4 kV/100 A Surge current resistance (conductor-ground) C3 - 100 A C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA	Surge protection fault message	None
B2 - 4 kV/100 A Surge current resistance (conductor-ground) C2 (4 kV / 2 kA) C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA	Surge current resistance (conductor-conductor)	C2 (4 kV/2 kA)
Surge current resistance (conductor-ground) C2 (4 kV / 2 kA) C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA D1 - 1 kA		C3 - 100 A
C3 - 100 A B2 - 4 kV/100 A D1 - 1 kA		B2 - 4 kV/100 A
B2 - 4 kV/100 A D1 - 1 kA	Surge current resistance (conductor-ground)	C2 (4 kV / 2 kA)
D1 - 1 kA		C3 - 100 A
		B2 - 4 kV/100 A
Alternating current carrying capacity (conductor-ground) 5 A - 1 s		D1 - 1 kA
	Alternating current carrying capacity (conductor-ground)	5 A - 1 s

Connection data

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Technical data

Connection data

Connection method	can be plugged into COMTRAB-TERMIBLOCK and LSA-PLUS disconnect and switching strips
Connection type IN	COMTRAB plug-in system
Connection type OUT	COMTRAB plug-in system
Connection method	LSA-PLUS

Connection, equipotential bonding

Connection method	Spring contact
	1 - 1 - 3

Standards and Regulations

Chandanda/namulations	IEC 61643 21
Standards/regulations	IEC 61643-21

Classifications

eCl@ss

eCl@ss 4.0	27140201
eCl@ss 4.1	27130801
eCl@ss 5.0	27130801
eCl@ss 5.1	27130801
eCl@ss 6.0	27130807
eCl@ss 7.0	27130807
eCl@ss 8.0	27130807

ETIM

ETIM 2.0	EC000943
ETIM 3.0	EC000943
ETIM 4.0	EC000943
ETIM 5.0	EC000943

UNSPSC

UNSPSC 6.01	30212010
UNSPSC 7.0901	39121610
UNSPSC 11	39121610
UNSPSC 12.01	39121610
UNSPSC 13.2	39121620

Approvals

Approvals

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Approvals

Approvals

GOST / UL Listed

Ex Approvals

Approvals submitted

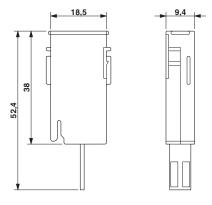
Approval details



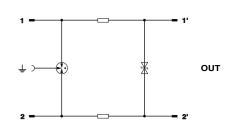
UL Listed

Drawings

Dimensioned drawing



Circuit diagram



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