



PJM05C20DFA

N and P-Channel Complementary Power MOSFET

Features

● N-Channel

$V_{DS}=20V, I_D=5A$
 $R_{DS(on)}<28m\Omega @ V_{GS}=4.5V$
 $R_{DS(on)}<35m\Omega @ V_{GS}=2.5V$

● P-Channel

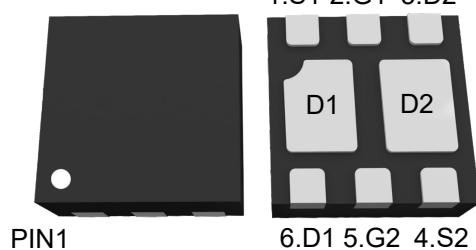
$V_{DS}=-20V, I_D=-5A$
 $R_{DS(on)}<45m\Omega @ V_{GS}=-4.5V$
 $R_{DS(on)}<70m\Omega @ V_{GS}=-2.5V$

- Very fast switching
- Trench MOSFET technology

Applications

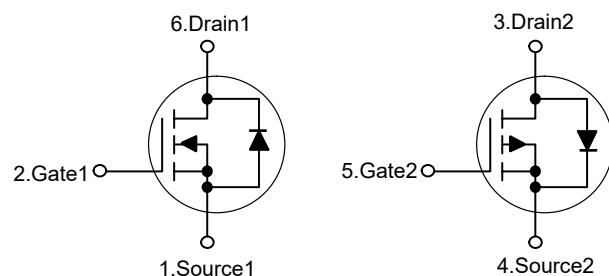
- Switching
- DC/DC Converter

DFN2x2A-6L



Marking Code: 2005

Schematic Diagram



Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	20	-20	V
Gate-Source Voltage	V_{GS}	± 12	± 12	V
Drain Current-Continuous	I_D	+5	-5	A
Drain Current-Pulsed ^{Note1}	I_{DM}	20	-20	A
Maximum Power Dissipation	P_D	1.2		W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	150, -55~150		°C

Thermal Characteristics

Thermal Resistance, Junction-to-Ambient ^{Note2}	$R_{\theta JA}$	104	°C/W
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Electrical Characteristics

($T_a=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	20	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
Gate Threshold Voltage ^{Note3}	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.5	0.7	1.0	V
Drain-Source On-Resistance ^{Note3}	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=4.5\text{A}$	--	--	28	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=4\text{A}$	--	--	35	$\text{m}\Omega$
Forward Transconductance ^{Note3}	g_{FS}	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=5\text{A}$	--	25	--	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=8\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	500	--	pF
Output Capacitance	C_{oss}		--	295	--	pF
Reverse Transfer Capacitance	C_{rss}		--	96	--	pF
Switching Characteristics						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=10\text{V}, I_{\text{D}}=1\text{A}$ $V_{\text{GS}}=4.5\text{V}, R_{\text{GEN}}=6\Omega$	--	11	--	nS
Turn-on Rise Time	t_r		--	30	--	nS
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	35	--	nS
Turn-off Fall Time	t_f		--	10	--	nS
Total Gate Charge	Q_g	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=3\text{A}, V_{\text{GS}}=4.5\text{V}$	--	10	--	nC
Gate-Source Charge	Q_{gs}		--	2.3	--	nC
Gate-Drain Charge	Q_{gd}		--	2.9	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note3}	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=5\text{A}$	--	--	1.2	V
Diode Forward Current ^{Note2}	I_{s}		--	--	5	A



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Electrical Characteristics

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Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$-V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	20	--	--	V
Zero Gate Voltage Drain Current	$-I_{\text{DSS}}$	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
Gate Threshold Voltage ^{Note3}	$-V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	0.5	0.7	0.9	V
Drain-Source On-Resistance ^{Note3}	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4.1\text{A}$	--	--	45	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-3\text{A}$	--	--	70	$\text{m}\Omega$
Forward Transconductance ^{Note3}	g_{FS}	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-4.1\text{A}$	--	6	--	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-4\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	740	--	pF
Output Capacitance	C_{oss}		--	290	--	pF
Reverse Transfer Capacitance	C_{rss}		--	190	--	pF
Switching Characteristics						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-4\text{V}, R_{\text{L}}=1.2\Omega$ $V_{\text{GS}}=-4.5\text{V}, R_{\text{GEN}}=1\Omega$	--	12	--	nS
Turn-on Rise Time	t_{r}		--	35	--	nS
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	30	--	nS
Turn-off Fall Time	t_{f}		--	10	--	nS
Total Gate Charge	Q_{g}	$V_{\text{DS}}=-4\text{V}, I_{\text{D}}=-4.1\text{A},$ $V_{\text{GS}}=-4.5\text{V}$	--	7.8	--	nC
Gate-Source Charge	Q_{gs}		--	1.2	--	nC
Gate-Drain Charge	Q_{gd}		--	1.6	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note3}	$-V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-5\text{A}$	--	--	1.2	V
Diode Forward Current ^{Note2}	$-I_{\text{s}}$		--	--	5	A

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \leq 10$ sec.

3. Pulse Test: Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

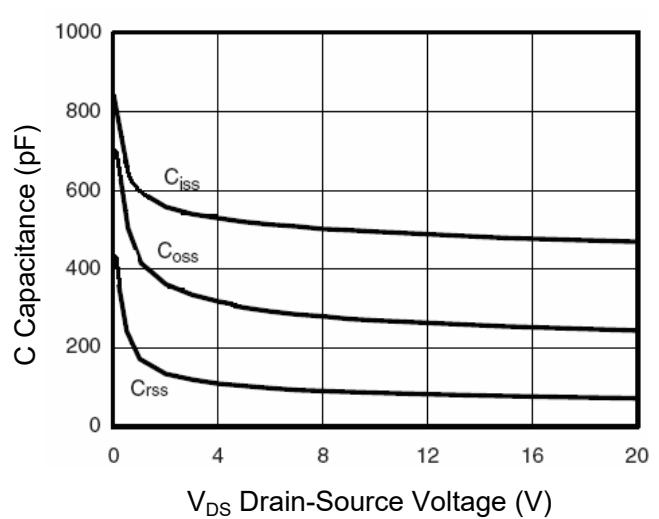
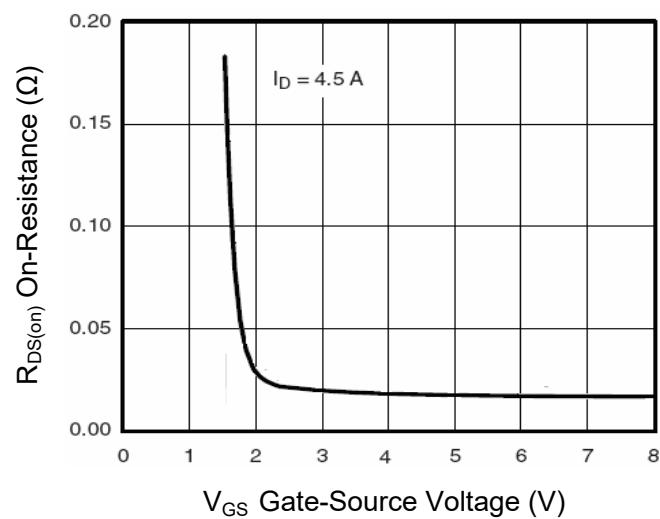
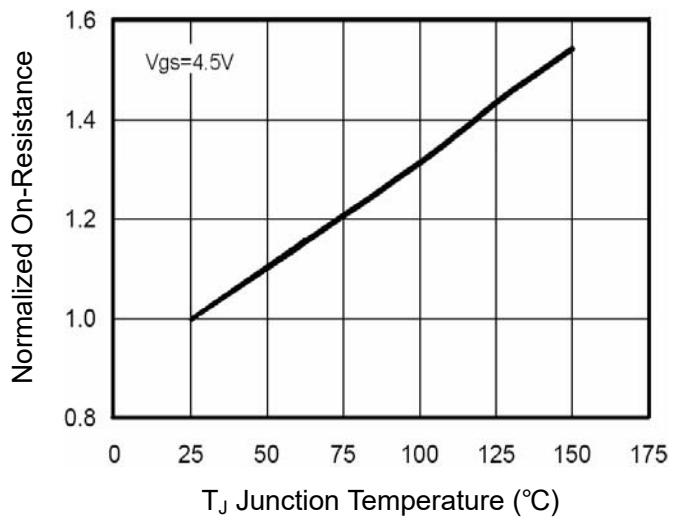
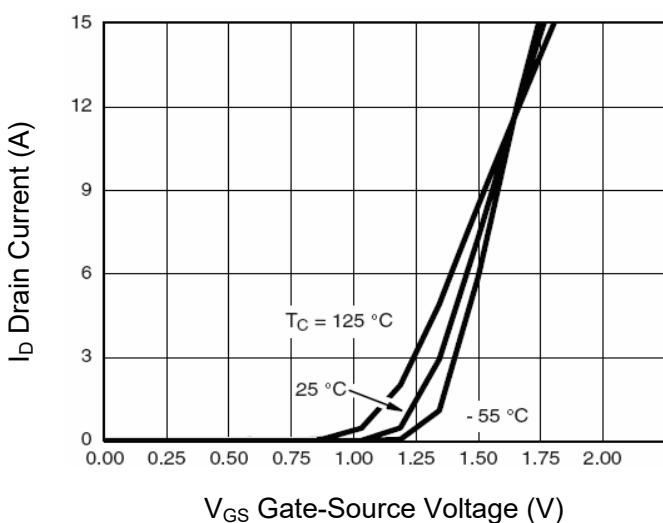
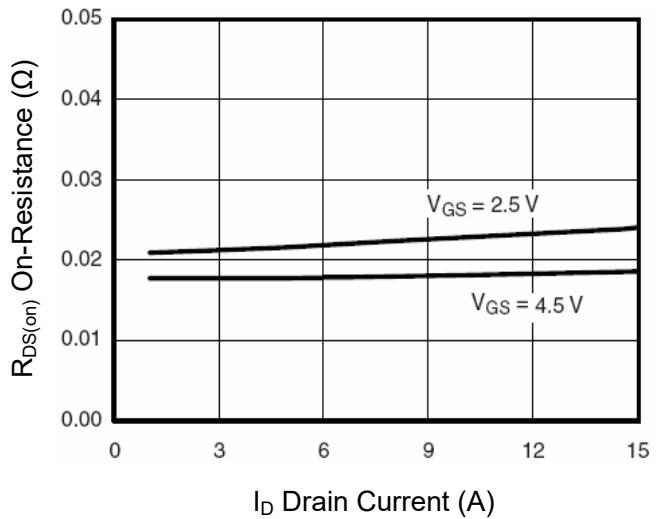
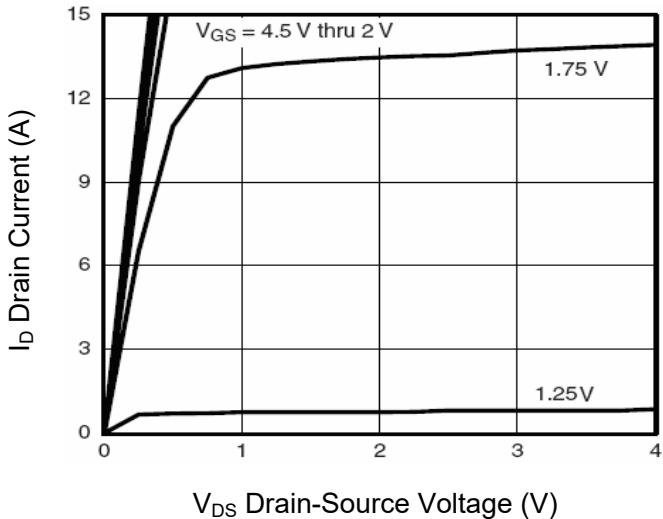


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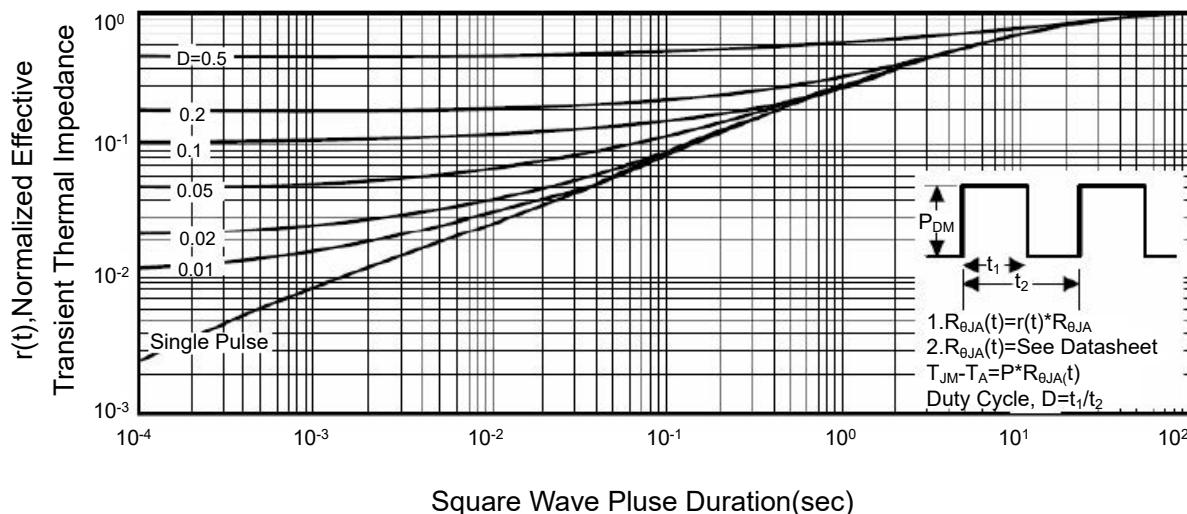
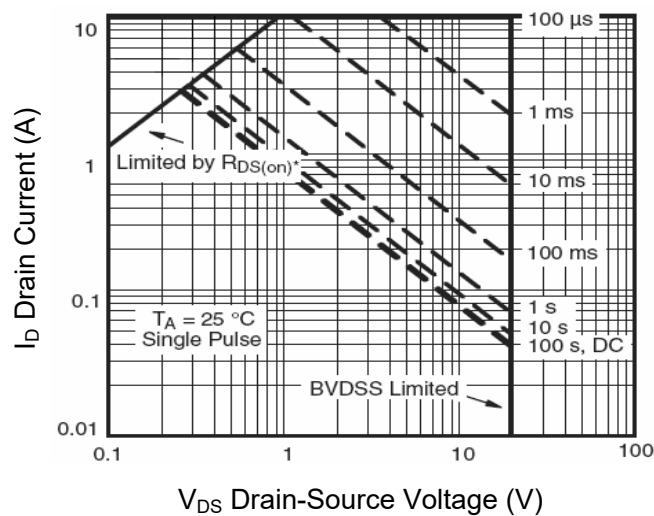
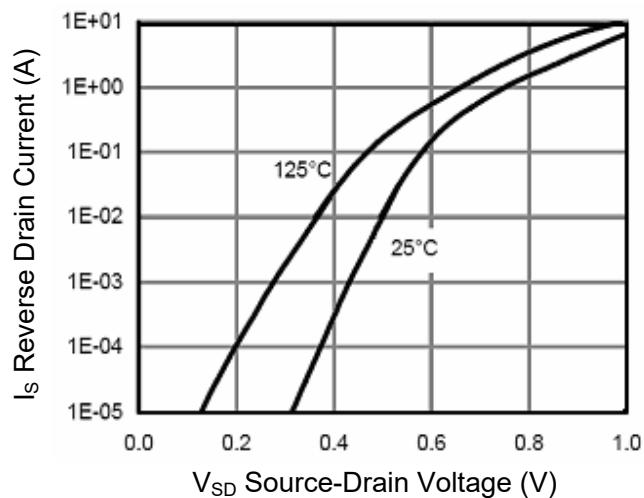
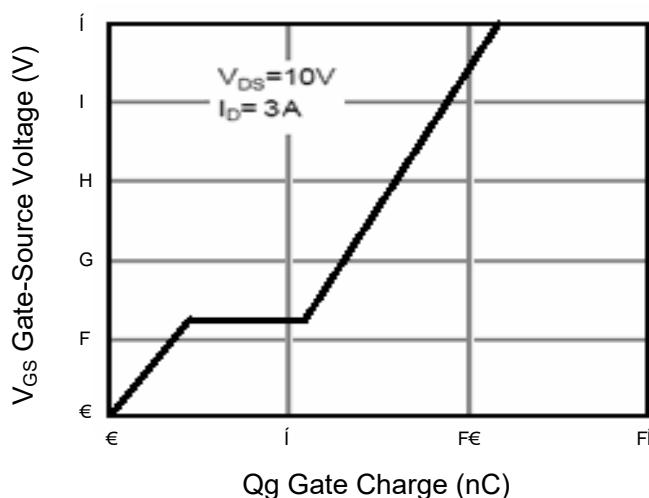
Electrical Characteristics Curves





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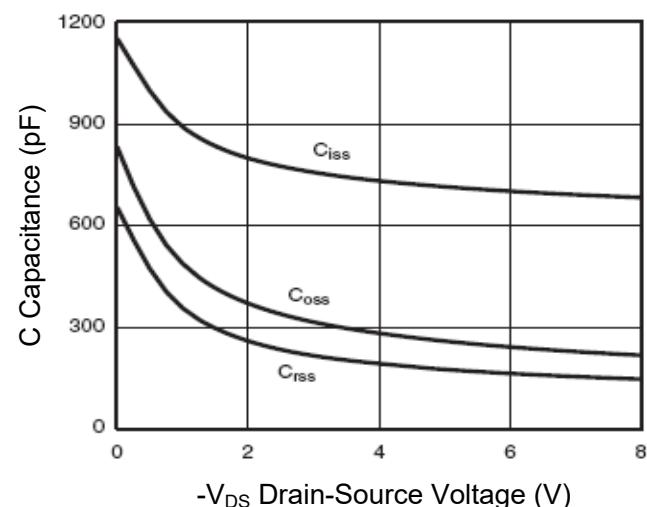
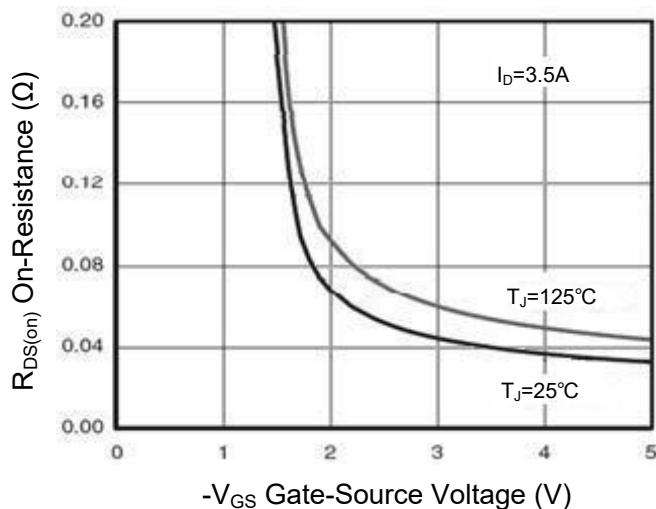
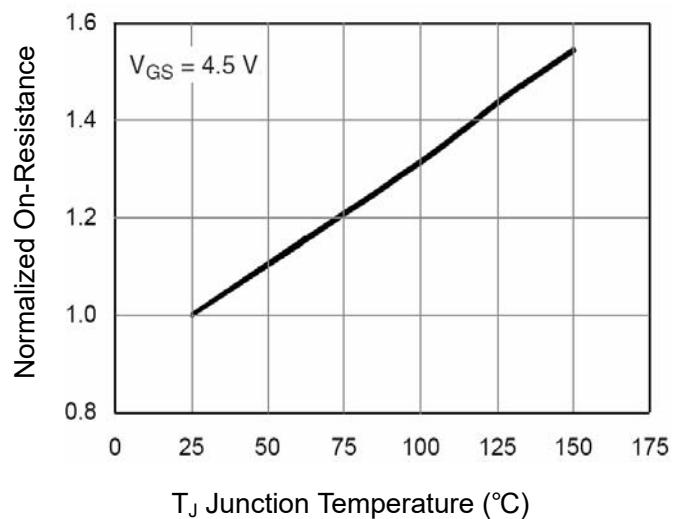
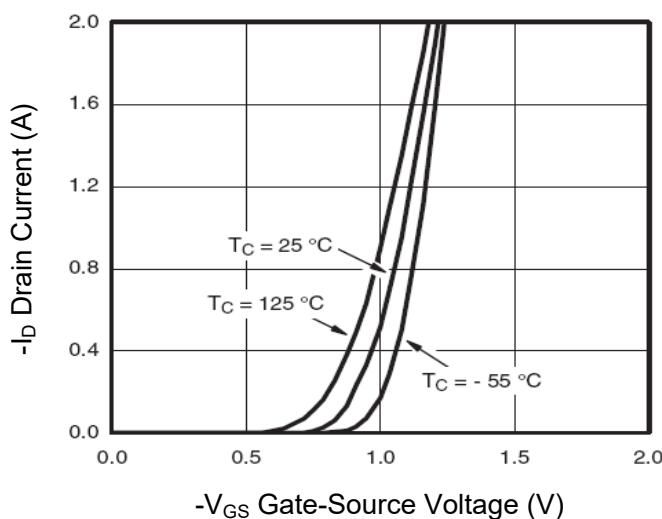
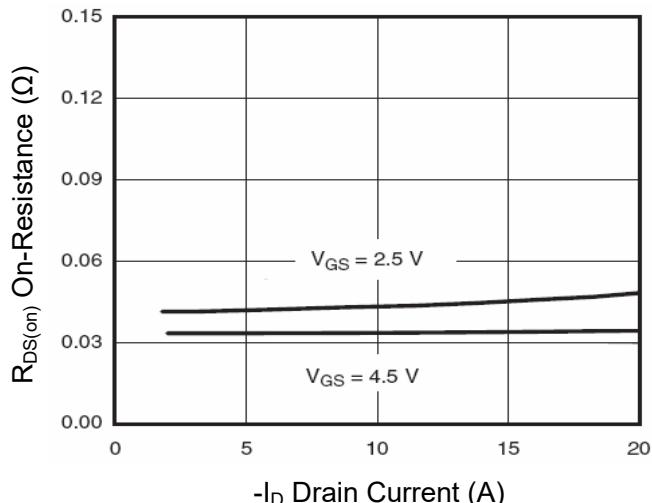
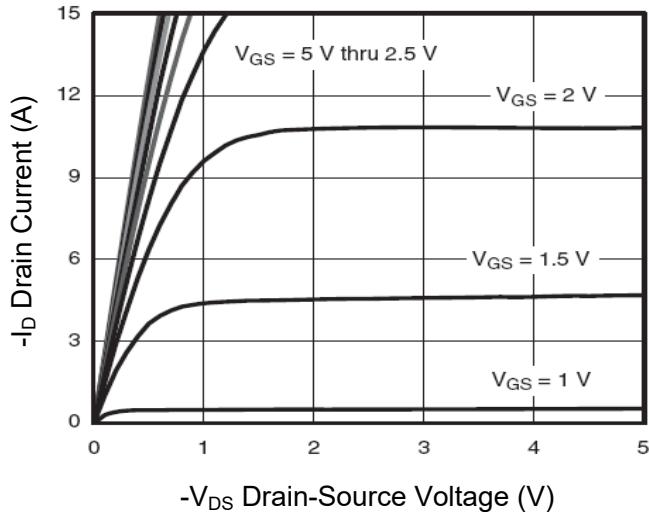


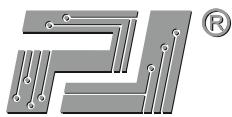
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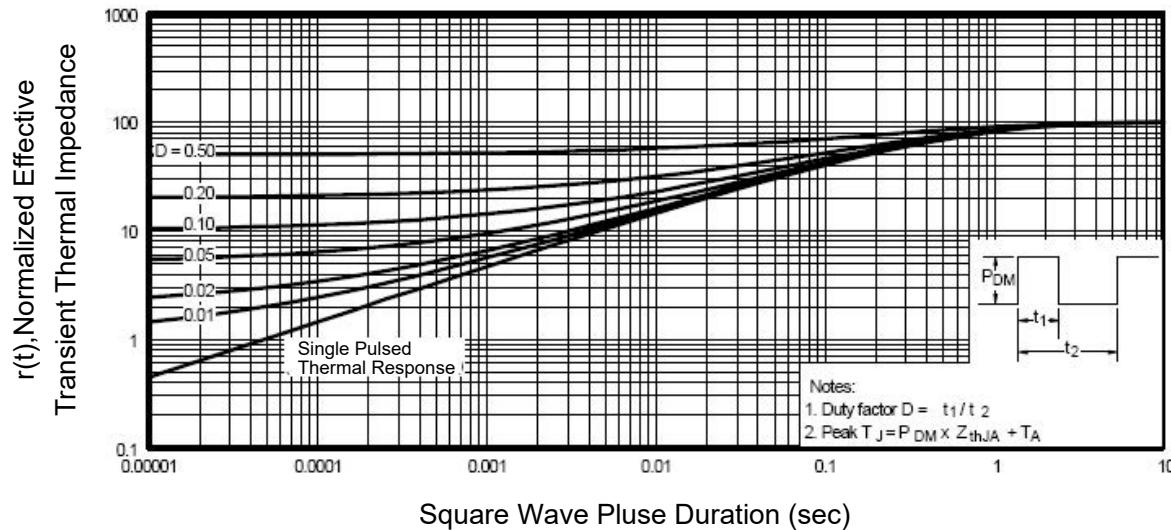
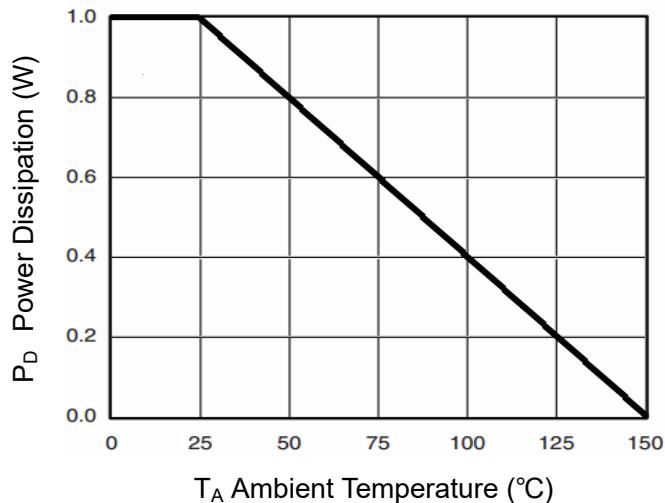
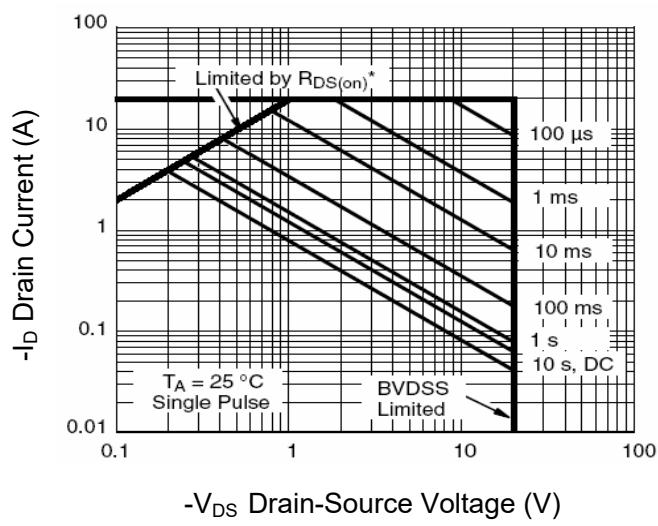
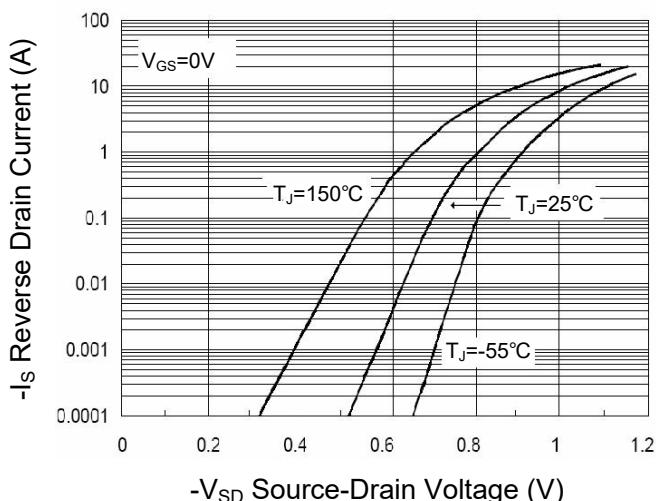
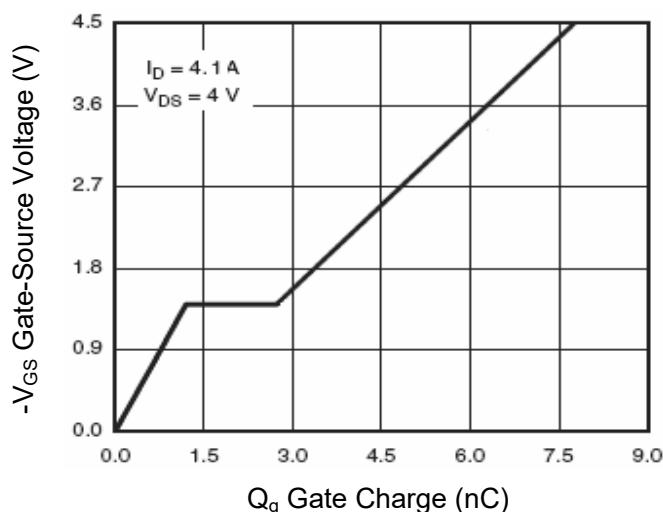
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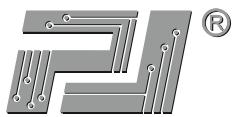




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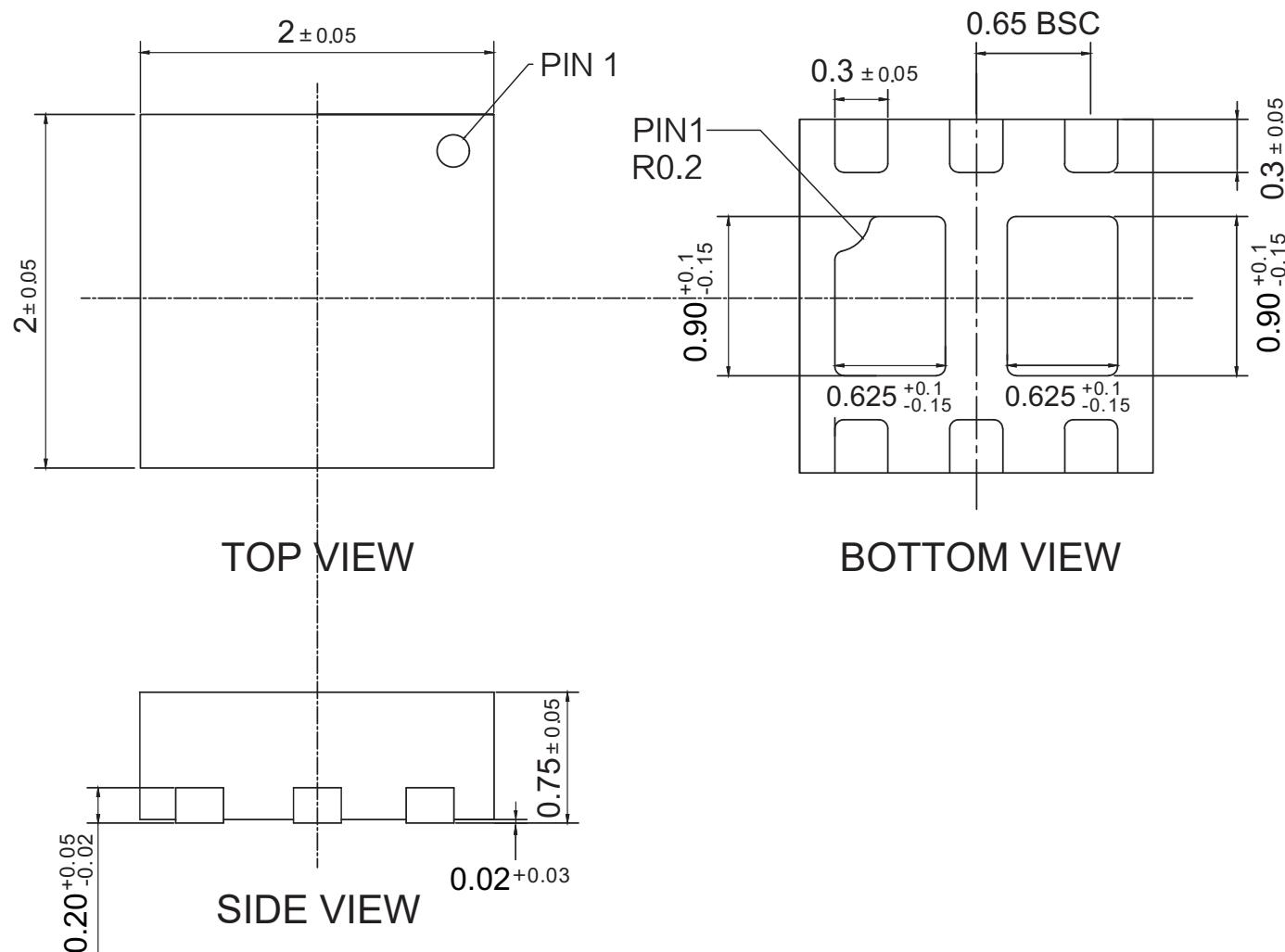
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Package Outline

DFN2x2-6L-0002

Dimensions in mm



Ordering Information

Device	Package	Shipping
PJM05C20DFA	DFN2x2A-6L	3,000PCS/Reel&7inches

单击下面可查看定价，库存，交付和生命周期等信息

[>>PJSEMI\(平晶微\)](#)