

SAW Rx filter

Automotive telematics TD-LTE band 40

Series/type: B4352

Ordering code: B39242B4352P810

Date: April 14, 2016

Version: 2.0

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1 Application

- Low-loss RF filter for TD-LTE Band 40 system, receive path (Rx)
- Suitable for diversity applications
- Usable pass band 100 MHz

2 Features

- Package size 1.4±0.1 mm × 1.1±0.1 mm
- Package height 0.45 mm (max.)
- Package code QCS5P
- Approximate weight 3 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Filter surface passivated
- AEC-Q200 qualified component family (operable temperature range -40 °C to +85 °C)
- Electrostatic Sensitive Device (ESD)



Figure 1: Picture of component with example of product marking.

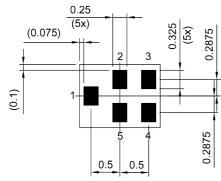


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3 Package

BOTTOM VIEW



Pad and pitch tolerance ±0.05

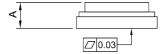
4 Pin configuration

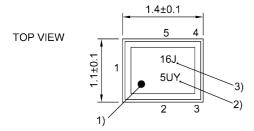
- 1 Input
- 4 Output

2, 3, 5

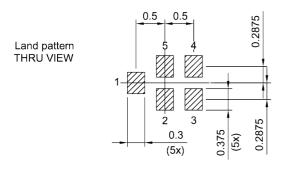
Ground

SIDE VIEW





- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number



Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.45 mm (max.). See Sec. Package information (p. 16).



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5 Matching circuit

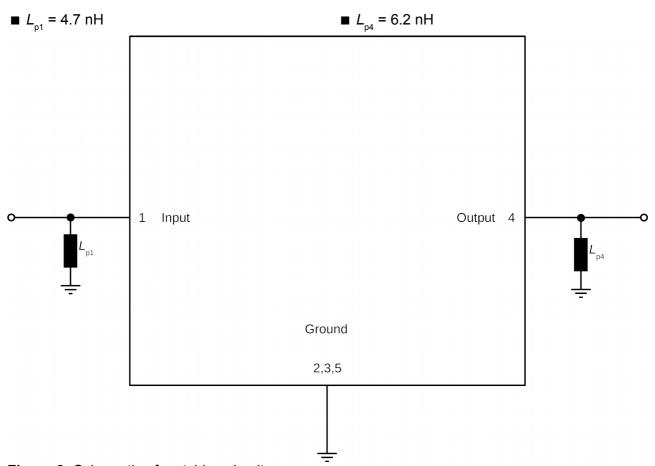


Figure 3: Schematic of matching circuit.



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6 Characteristics

Temperature range for specification $T_{\rm SPEC} = -40~{\rm ^{\circ}C}~...~+85~{\rm ^{\circ}C}$ Input terminating impedance $Z_{\rm IN} = 50~\Omega$ with par. 4.7 nH $^{1)}$ Output terminating impedance $Z_{\rm OUT} = 50~\Omega$ with par. 6.2 nH $^{1)}$

Characteristics				$\begin{array}{c} \textbf{min.} \\ \textbf{for } T_{\text{SPEC}} \end{array}$	typ. @+25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
Center frequency			f _C	_	2350	_	MHz
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	2300 2400	MHz		_	2.8	4.0	dB
Amplitude ripple (p-p)			Δα				
	2300 2400	MHz		_	1.4	2.8	dB
Maximum VSWR			$VSWR_{max}$				
@ input port	2300 2400	MHz		_	1.8	2.3	
@ output port	2300 2400	MHz		_	1.9	2.3	
Minimum attenuation			$\boldsymbol{\alpha}_{_{min}}$				
	50 1559	MHz		40	54	_	dB
	1559 1606	MHz		40	57	_	dB
	1606 2125	MHz		36	40	_	dB
	2125 2215	MHz		30	38	_	dB
	2215 2240	MHz		30	36	_	dB
	2430 2440	MHz		12	35	_	dB
	2440 2450	MHz		31	36	_	dB
	2450 2500	MHz		32	41	_	dB
	2500 2690	MHz		33	37	_	dB
	4600 4800	MHz		26	35	_	dB
	4900 5950	MHz		24	33	_	dB

See Sec. Matching circuit (p. 5).



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7 Maximum ratings

Operable temperature	T _{OP} = -40 °C +85 °C	
Storage temperature	T _{STG} = -40 °C +85 °C	
DC voltage	V _{DC} = 0 V	
Input power @ input port: 2300 2400 MHz	P _{IN} = 16 dBm	Continuous wave for 2000 h @ 55 °C.



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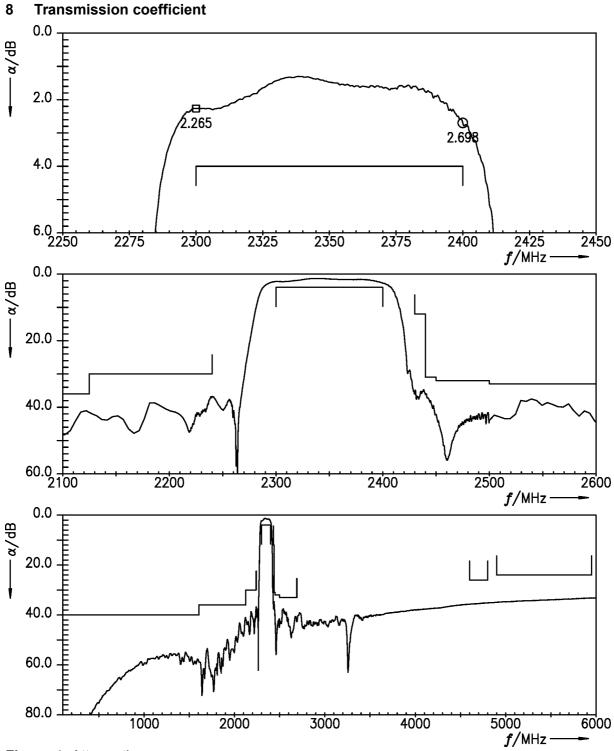


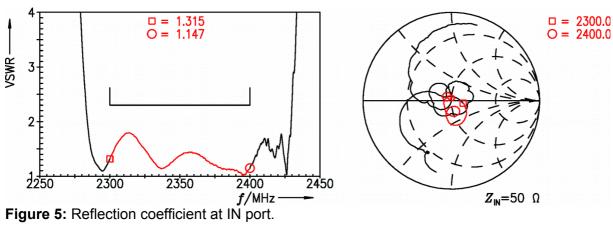
Figure 4: Attenuation.



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9 **Reflection coefficients**



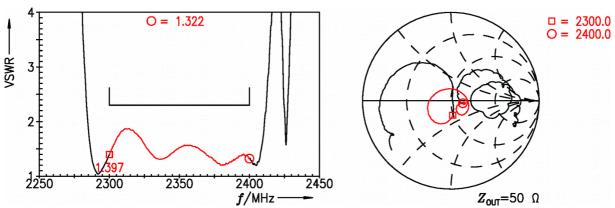


Figure 6: Reflection coefficient at OUT port.

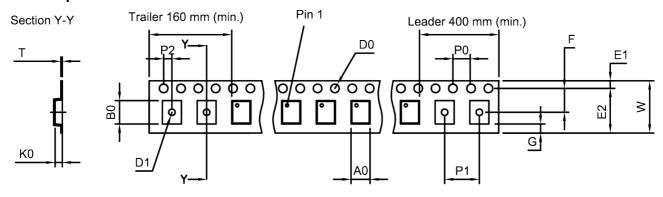


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10 Packing material

10.1 Tape



User direction of unreeling

Figure 7: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	1.27±0.05 mm	E	6.25 mm (min.)	F	4.0 _{±0.1} mm
B ₀	1.57±0.05 mm		F 3.5±0.05 mm	F	2.0±0.05 mm
D ₀	1.5+0.1/-0 mm		G 0.75 mm (min.)		T 0.25±0.03 mm
D ₁	0.5±0.1 mm	k	0.62±0.05 mm		V 8.0+0.3/-0.1 mm
E ₁	1.75±0.1 mm	F	0 4.0±0.1 mm		

Table 1: Tape dimensions.

10.2 Reel with diameter of 180 mm

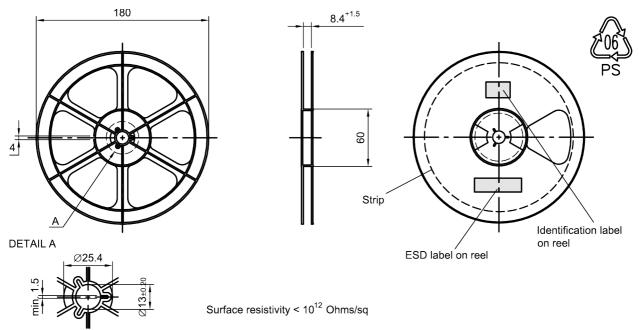


Figure 8: Drawing of reel (first-angle projection) with diameter of 180 mm.



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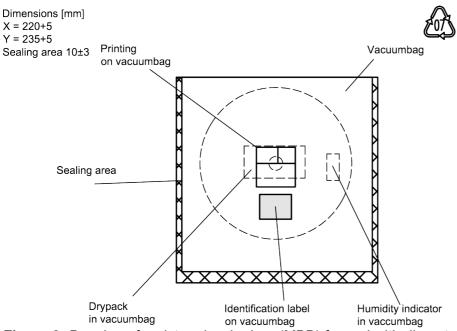


Figure 9: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

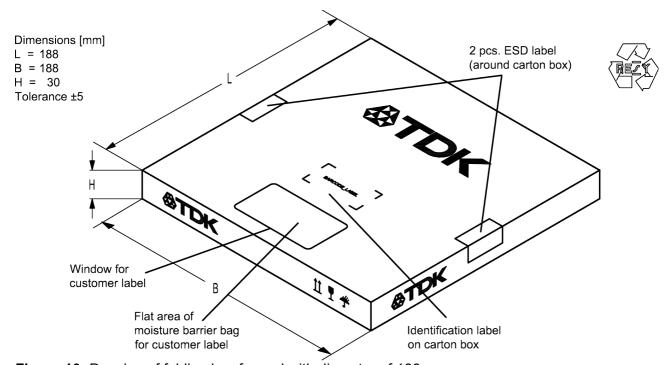


Figure 10: Drawing of folding box for reel with diameter of 180 mm.

11 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:



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The 4 digit type number of the ordering code, e.g., B3xxxxB**1234**xxxx, is encoded by a special BASE32 code into a 3 digit marking.

Example of decoding type number marking on device in decimal code.

16J => 1234 1 x 32^2 + 6 x 32^1 + 18 (=J) x 32^0 = 1234

The BASE32 code for product type B4352 is 480.

■ Lot number:

The last 5 digits of the lot number, e.g., are encoded based on a special BASE47 code into a 3 digit marking.

Example of decoding lot number marking on device in decimal code.

5UY => 12345 5 x 47² + 27 (=U) x 47¹ + 31 (=Y) x 47⁰ = 12345

Adopted BASE32 code for type number				
Decimal	Base32	Decimal	Base32	
value	code	value	code	
0	0	16	G	
1	1	17	Н	
2	2	18	J	
3	3	19	K	
4	4	20	М	
5	5	21	N	
6	6	22	Р	
7	7	23	Q	
8	8	24	R	
9	9	25	S	
10	Α	26	Т	
11	В	27	V	
12	С	28	W	
13	D	29	X	
14	E	30	Y	
15	F	31	Z	

Adopted BASE47 code for lot number				
Decimal	Base47	Decimal	Base47	
value	code	value	code	
0	0	24	R	
1	1	25	S	
2	2	26	Т	
3	3	27	U	
4	4	28	V	
5	5	29	W	
6	6	30	Х	
7	7	31	Y	
8	8	32	Z	
9	9	33	b	
10	Α	34	d	
11	В	35	f	
12	С	36	h	
13	D	37	n	
14	E	38	r	
15	F	39	t	
16	G	40	V	
17	Н	41	\	
18	J	42	?	
19	K	43	{	
20	L	44	}	
21	М	45	<	
22	Ν	46	>	
23	Р			

Table 2: Lists for encoding and decoding of marking.



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12 Soldering profile

The recommended soldering process is in accordance with IEC $60068-2-58-3^{rd}$ edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
T > 220 °C	30 s to 70 s
T > 230 °C	min. 10 s
T > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

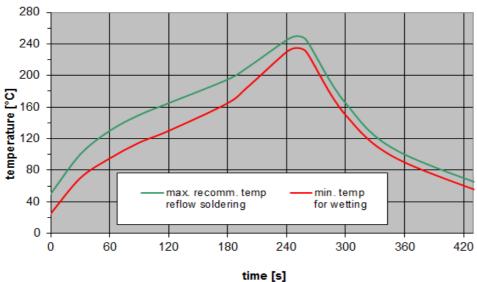


Figure 11: Recommended reflow profile for convection and infrared soldering – lead-free solder.



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13 ESD protection of SAW filters

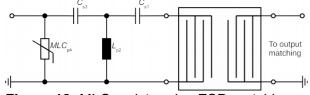
SAW filters are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.



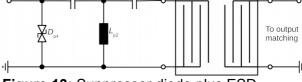


Figure 12: MLC varistor plus ESD matching.

Figure 13: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

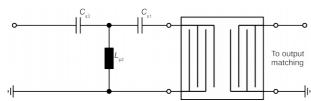


Figure 14: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor $L_{\rm p2}$ could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report: "**ESD protection for SAW filters**". This report can be found under <u>www.epcos.com/rke</u>. Click on "Applications Notes".



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14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm.

14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.



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15 Cautions and warnings

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15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.



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