

SAW duplexer Small cell & femtocell LTE band 3

Series/type: B8044

Ordering code: B39182B8044P810

Date: September 29, 2017

Version: 2.1

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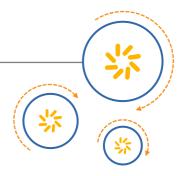
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RF360 Europe GmbH
A Qualcomm – TDK Joint Venture



SAW components

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SAW duplexer 1747.5 / 1842.5 MHz

Data sheet

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SAW duplexer

1747.5 / 1842.5 MHz

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SAW duplexer 1747.5 / 1842.5 MHz

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1 Application

- Low-loss SAW duplexer for LTE small cell & femtocell systems (Band 3)
- Usable pass band 75 MHz
- Rx = uplink = 1710 MHz 1785 MHz
- Tx = downlink = 1805 MHz 1880 MHz

2 Features

- Industrial grade qualified family
- Package size 2.5±0.1 mm × 2.0±0.1 mm
- Package height 0.5 mm (max.)
- Approximate weight 0.01 g
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 2a (MSL2a)



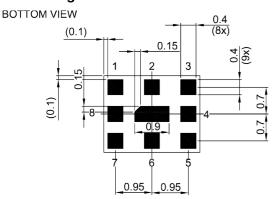
Figure 1: Picture of component with example of product marking.



SAW duplexer 1747.5 / 1842.5 MHz

Data sheet

3 Package



4 Pin configuration

1 TX

■ 3 RX

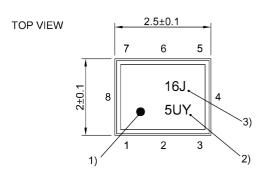
■ 6 ANT

■ 2, 4, 5, 7, Ground 8, 9

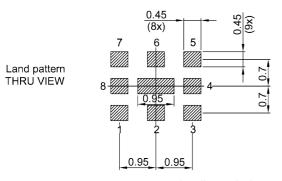
Pad and pitch tolerance ±0.05

SIDE VIEW





- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number



Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.5 mm (max.). See Sec. Package information (p. 27).



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5 Matching circuit

■ L_{p1} = 3.6 nH

■ L_{p6} = 3.0 nH

■ $L_{p3} = 7.5 \text{ nH}$

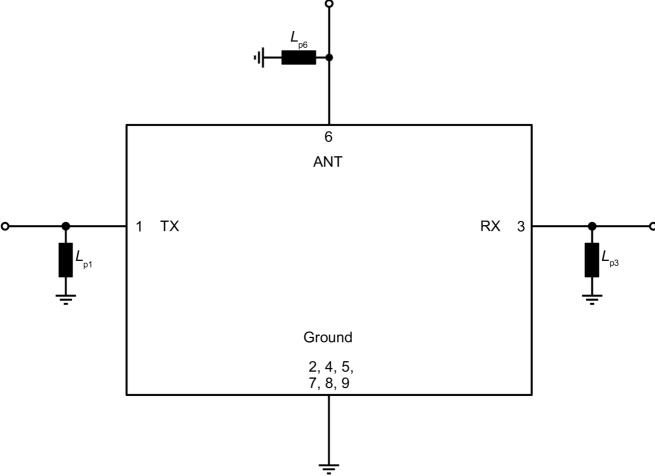


Figure 3: Schematic of matching circuit.



SAW duplexer 1747.5 / 1842.5 MHz

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6 Characteristics

6.1 TX - ANT

Temperature range for specification $T_{\rm SPEC} = -10~{\rm ^{\circ}C}~...~+85~{\rm ^{\circ}C}$ TX terminating impedance $Z_{\rm TX} = 50~\Omega$ with par. $3.6~{\rm nH^{10}}$ ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ with par. $3.0~{\rm nH^{10}}$ RX terminating impedance $Z_{\rm RX} = 50~\Omega$ with par. $7.5~{\rm nH^{10}}$

Characteristics TX – ANT				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
Center frequency			f _C	_	1842.5	_	MHz
Average insertion attenuation			$\alpha_{_{INT,avg}}^{2)}$				
	1805 1810	MHz		_	2.2	3.3	dB
	1810 1875	MHz		_	1.9	2.8	dB
	1875 1880	MHz		_	1.6	2.7	dB
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	1805 1880	MHz		_	2.7	4.0	dB
	1840 1870	MHz		_	1.4	2.5	dB
Amplitude ripple (p-p)			Δα				
	1805 1880	MHz		_	1.7	3.0	dB
	1840 1870	MHz		_	0.4	1.0	dB
Maximum VSWR			VSWR _{max}				
@ TX port	1805 1880	MHz		_	1.7	2.0	
@ ANT port	1805 1880	MHz		_	1.7	2.0	
Maximum error vector magnitude			EVM _{max} ³⁾				
	1807.4 1877.6	MHz		_	2.0	3.5	%
Minimum attenuation			$\boldsymbol{\alpha}_{\text{min}}$				
	10 1710	MHz		30	38	_	dB
	1710 1745	MHz		42	50	_	dB
	1745 1775	MHz		45	52	_	dB
	1775 1783	MHz		45	52	_	dB
	1783 1785	MHz		35	51	_	dB
	1900 1911	MHz		5	38	_	dB
	1911 1920	MHz		35	56	_	dB
	1920 1980	MHz		40	50	_	dB
	1980 2250	MHz		35	45	_	dB
	2250 2425	MHz		15	23	_	dB
	2425 3740	MHz		35	50	_	dB
	3740 5725	MHz		30	44	_	dB

See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



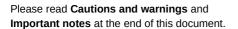
SAW duplexer 1747.5 / 1842.5 MHz

Data sheet

Temperature range for specification $T_{\rm SPEC} = -40~{\rm ^{\circ}C}~...~+95~{\rm ^{\circ}C}$ TX terminating impedance $Z_{\rm TX} = 50~\Omega$ with par. 3.6 nH $^{\rm 1}$) ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ with par. 3.0 nH $^{\rm 1}$) RX terminating impedance $Z_{\rm RX} = 50~\Omega$ with par. 7.5 nH $^{\rm 1}$)

Characteristics TX – ANT				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	
Average insertion attenuation			α _{INT,avg} ²⁾				
	1805 1810	MHz	,	_	2.2	4.5	dB
	1810 1875	MHz		_	1.9	3.5	dB
	1875 1880	MHz		_	1.6	3.2	dB
Maximum insertion attenuation			α_{max}				
	1805 1880	MHz		_	2.7	6.2	dB
	1840 1870	MHz		_	1.4	2.7	dB
Amplitude ripple (p-p)			Δα				
	1805 1880	MHz		_	1.7	5.2	dB
	1840 1870	MHz		_	0.4	1.2	dB
Maximum VSWR			VSWR _{max}				
@ TX port	1805 1880	MHz		_	1.7	2.5	
@ ANT port	1805 1880	MHz		_	1.7	2.5	
Maximum error vector magnitude			EVM _{max} ³⁾				
	1807.4 1877.6	MHz	THE A	_	2.0	7.0	%
Minimum attenuation			α_{min}				
	10 1710	MHz		30	38	_	dB
	1710 1745	MHz		40	50	_	dB
	1745 1775	MHz		45	52	_	dB
	1775 1783	MHz		40	52	_	dB
	1783 1785	MHz		30	51	_	dB
	1900 1911	MHz		3	38	_	dB
	1911 1920	MHz		35	56	_	dB
	1920 1980	MHz		40	50	_	dB
	1980 2250	MHz		35	45	_	dB
	2250 2425	MHz		15	23	_	dB
	2425 3740	MHz		35	50	_	dB
	3740 5725	MHz		30	44	_	dB

See Sec. Matching circuit (p. 6).



Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

³⁾ Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



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Data sheet

6.2 ANT - RX

Temperature range for specification $T_{\rm SPEC} = -10~{\rm ^{\circ}C}~...~+85~{\rm ^{\circ}C}$ TX terminating impedance $Z_{\rm TX} = 50~\Omega$ with par. $3.6~{\rm nH^{10}}$ ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ with par. $3.0~{\rm nH^{10}}$ RX terminating impedance $Z_{\rm RX} = 50~\Omega$ with par. $7.5~{\rm nH^{10}}$

Characteristics ANT - RX				min.	typ. @ +25 °C	max.	
Center frequency			f _C	for T _{SPEC}	1747.5	for T _{SPEC}	MHz
				_	1141.5	_	IVII IZ
Average insertion attenuation			$\alpha_{\text{INT,avg}}^{2)}$				
	1710 1715	MHz		_	3.2	4.3	dB
	1715 1780	MHz		_	2.7	3.5	dB
	1780 1785	MHz		_	2.2	4.3	dB
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	1710 1785	MHz		_	3.8	5.3	dB
	1745 1775	MHz		_	1.6	3.0	dB
Amplitude ripple (p-p)			Δα				
	1710 1785	MHz		_	1.5	4.0	dB
	1745 1775	MHz		_	0.3	1.5	dB
Maximum VSWR			VSWR _{max}				
@ ANT port	1710 1785	MHz		_	1.7	2.1	
@ RX port	1710 1785	MHz		_	1.6	2.0	
Maximum error vector magnitude			EVM _{max} ³⁾				
	1712.4 1782.6	MHz		_	2.4	4.9	%
Minimum attenuation			$\boldsymbol{\alpha}_{\text{min}}$				•
	10 1660	MHz		40	42	_	dB
	1660 1690	MHz		30	36	_	dB
	1805 1840	MHz		40	65	_	dB
	1840 1880	MHz		45	57	_	dB
	1880 2400	MHz		40	43	_	dB
	2400 2500	MHz		40	42	_	dB
	2500 3550	MHz		35	38	_	dB
	3550 4200	MHz		32	36	_	dB
	4200 5325	MHz		28	34	_	dB

See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

³⁾ Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



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Temperature range for specification = -40 °C ... +95 °C TX terminating impedance = 50 Ω with par. 3.6 nH¹⁾ Z_{ANT} ANT terminating impedance = 50 Ω with par. 3.0 nH¹⁾ RX terminating impedance = 50 Ω with par. 7.5 nH¹⁾

Characteristics ANT – RX				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
Average insertion attenuation			α _{INT,avg} ²⁾				
	1710 1715	MHz		_	3.2	5.5	dB
	1715 1780	MHz		_	2.7	3.8	dB
	1780 1785	MHz		_	2.2	5.0	dB
Maximum insertion attenuation			α_{max}				
	1710 1785	MHz		_	3.8	7.5	dB
	1745 1775	MHz		_	1.6	3.2	dB
Amplitude ripple (p-p)			Δα				
	1710 1785	MHz		_	1.5	6.3	dB
	1745 1775	MHz		_	0.3	1.7	dB
Maximum VSWR			$VSWR_{max}$				
@ ANT port	1710 1785	MHz		_	1.7	2.5	
@ RX port	1710 1785	MHz		_	1.6	2.5	
Maximum error vector magnitude			EVM _{max} ³⁾				
	1712.4 1782.6	MHz	Thur.	_	2.4	9.0	%
Minimum attenuation			$\boldsymbol{\alpha}_{\text{min}}$				
	10 1660	MHz		40	42	_	dB
	1660 1690	MHz		27	36	_	dB
	1805 1840	MHz		30	65	_	dB
	1840 1880	MHz		45	57	_	dB
	1880 2400	MHz		40	43	_	dB
	2400 2500	MHz		40	42	_	dB
	2500 3550	MHz		35	38	_	dB
	3550 4200	MHz		32	36	_	dB
	4200 5325	MHz		28	34	_	dB

¹⁾

See Sec. Matching circuit (p. 6). Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels. 2)

Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



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6.3 TX - RX

Temperature range for specification $T_{\rm SPEC} = -10~{\rm ^{\circ}C}~...~+85~{\rm ^{\circ}C}$ TX terminating impedance $Z_{\rm TX} = 50~\Omega$ with par. $3.6~{\rm nH^{1}}$ ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ with par. $3.0~{\rm nH^{1}}$ RX terminating impedance $Z_{\rm px} = 50~\Omega$ with par. $7.5~{\rm nH^{1}}$

Characteristics TX - RX				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	
Average isolation			α _{INT,avg} ²⁾				
	1710 1785	MHz		50	53	_	dB
	1805 1880	MHz		55	59	_	dB
Minimum isolation			$\boldsymbol{\alpha}_{_{min}}$				
	1710 1712	MHz		49	55	_	dB
	1710 1712	MHz		50 ³⁾	55	_	dB
	1712 1783	MHz		50	53	_	dB
	1783 1785	MHz		45	55	_	dB
	1805 1880	MHz		55	59	_	dB

See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

³⁾ Valid for temperature $T = +25 \,^{\circ}\text{C...} + 85 \,^{\circ}\text{C.}$



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Temperature range for specification $T_{\rm SPEC} = -40~{\rm ^{\circ}C}~...~+95~{\rm ^{\circ}C}$ TX terminating impedance $Z_{\rm TX} = 50~\Omega$ with par. 3.6 nH $^{\rm 1}$) ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ with par. 3.0 nH $^{\rm 1}$) RX terminating impedance $Z_{\rm RX} = 50~\Omega$ with par. 7.5 nH $^{\rm 1}$)

Characteristics TX – RX				min.	typ.	max.	
					@ +25 °C		
Average isolation			α _{INT,avg} ²⁾				
	1710 1712	MHz		45	53	_	dB
	1712 1782	MHz		50	53	_	dB
	1782 1785	MHz		45	59	_	dB
	1805 1880	MHz		50	59	_	dB
Minimum isolation			$\boldsymbol{\alpha}_{\text{min}}$				
	1710 1712	MHz		48	55	_	dB
	1710 1712	MHz		50 ³⁾	55	_	dB
	1712 1782	MHz		50	53	_	dB
	1782 1785	MHz		40	55	_	dB
	1805 1880	MHz		50	59	_	dB

See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ii}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

Valid for temperature $T = +25 \,^{\circ}\text{C...} + 95 \,^{\circ}\text{C.}$



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7 Maximum ratings

Operable temperature	T _{OP} = -40 °C +95 °C	
Storage temperature	T _{STG} = −40 °C +95 °C	
DC voltage	$ V_{DC} ^{2)} = 0 V$	
ESD voltage		
	$V_{\rm ESD}^{3)} = 375 \rm V$	Machine model.
	V _{ESD} ⁴⁾ = 800 V	Human body model.
Input power @ TX port: 1805 1880 MHz	$P_{IN} = 28 \text{ dBm}^{5), 6}$	5 MHz LTE downlink signal for 100000 h @ 55 °C. P _{IN}
		average – 39 dBm peak. Source and load impedance 50Ω .

Not valid for packaging material. Storage temperature for packaging material is −25 °C to +40 °C.

²⁾ In case of applied DC voltage blocking capacitors are mandatory.

³⁾ According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses.

⁴⁾ According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.

Expected lifetime according to accelerated power durability tests, and wear out models.

T_{SPEC} is the ambient temperature of the PCB at component position. Specified min./max values from section 6 "characteristics" for maximum input power 28 dBm are valid for temperature up to 55 °C.



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8 Transmission coefficients

8.1 TX - ANT 0.0 $-\alpha/dB$ 2.0 1.653 2/697 4.0 6.0 1780 1880 1900 1800 1820 1840 1860 f/MHz 0.0 20.0 40.0 60.0 80.0 <u>F</u> 1500 1550 1600 1650 1700 1750 1800 1850 1900 1950 f/MHz 0.0 20.0 40.0 60.0 80.0 1000 2000 3000 4000 5000 6000

Figure 4: Attenuation TX – ANT.

f/MHz -



Data sheet

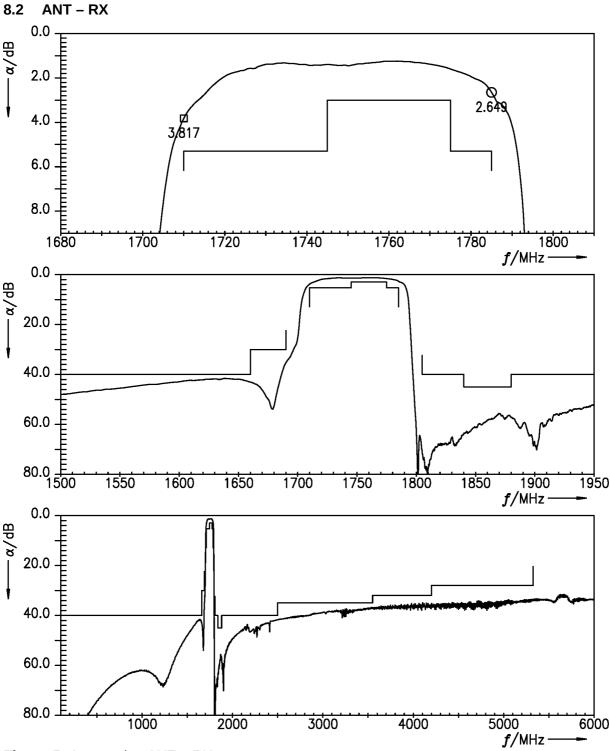


Figure 5: Attenuation ANT – RX.



Data sheet

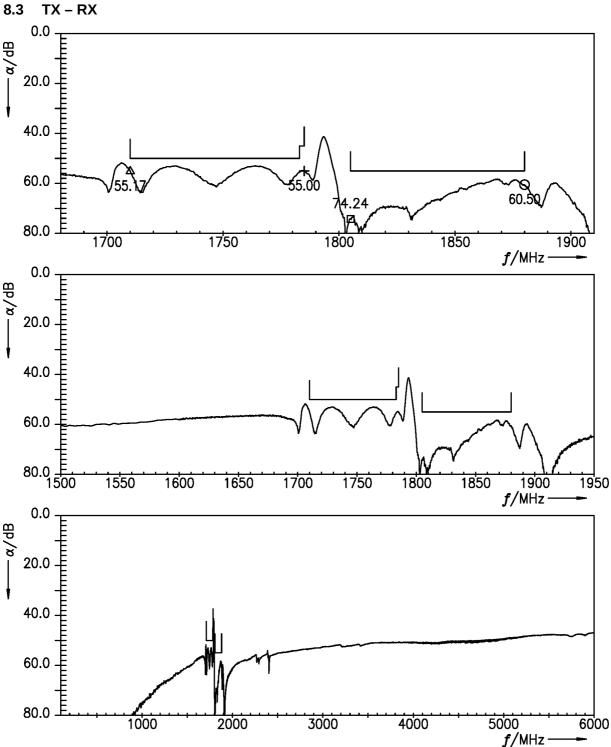


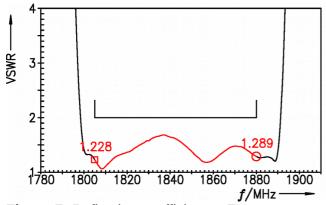
Figure 6: Isolation TX – RX.



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9 Reflection coefficients



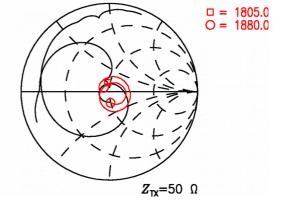
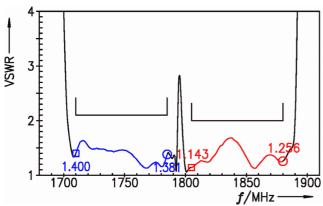


Figure 7: Reflection coefficient at TX port.



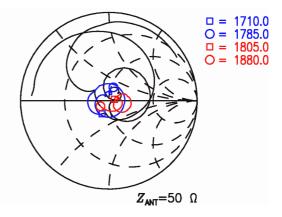
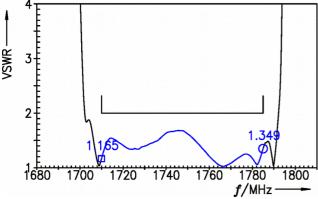


Figure 8: Reflection coefficient at ANT port.



 $Z_{RX}=50 \Omega$

Figure 9: Reflection coefficient at RX port.



Data sheet

10 EVMs

10.1 TX - ANT

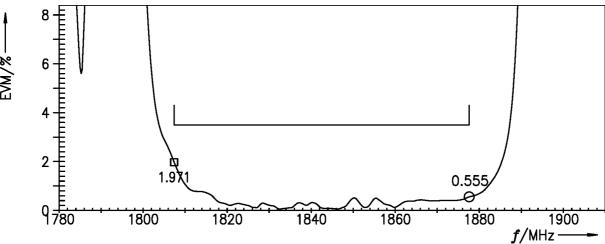


Figure 10: Error vector magnitude TX – ANT.



Data sheet

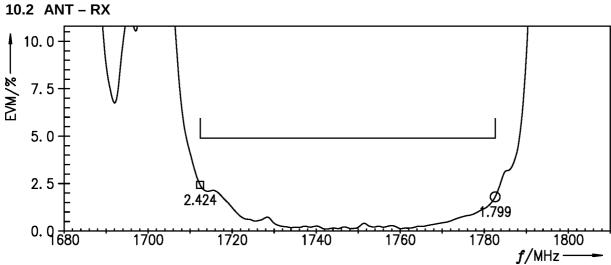


Figure 11: Error vector magnitude ANT – RX.

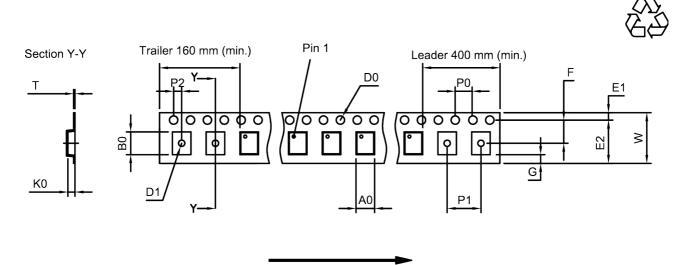


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11 Packing material

11.1 Tape



User direction of unreeling **Figure 12:** Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	2.25±0.05 mm	E ₂	6.25 mm (min.)	 P_1	4.0±0.1 mm
B_0	2.75±0.05 mm	F	3.5±0.05 mm	P_2	2.0±0.05 mm
D ₀	1.5+0.1/-0 mm	G	0.75 mm (min.)	 Т	0.25±0.03 mm
D_1	1.0 mm (min.)	K ₀	0.6±0.05 mm	 W	8.0+0.3/-0.1 mm
E ₁	1.75±0.1 mm	P ₀	4.0 _{±0.1} mm		

Table 1: Tape dimensions.



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11.2 Reel with diameter of 180 mm

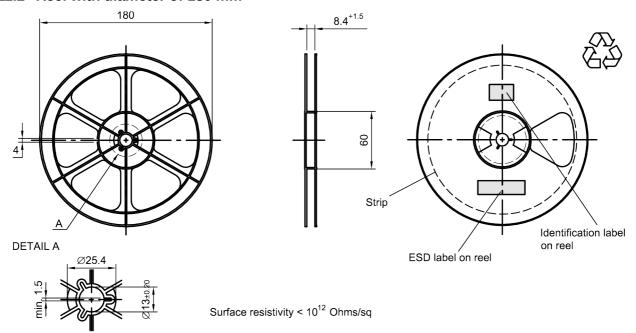


Figure 13: Drawing of reel (first-angle projection) with diameter of 180 mm.

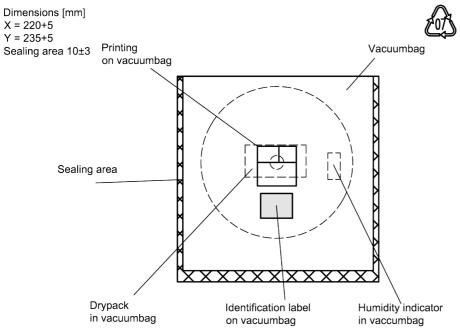


Figure 14: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.



Data sheet

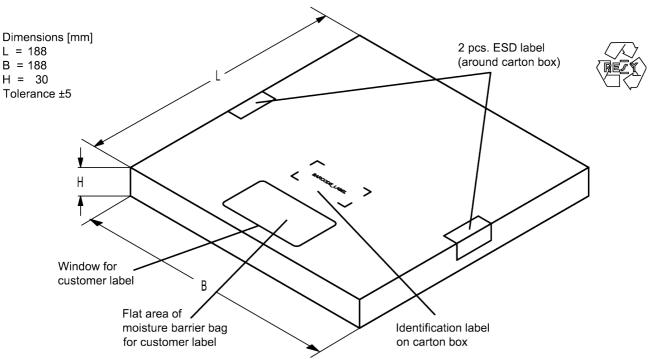


Figure 15: Drawing of folding box for reel with diameter of 180 mm.

11.3 Reel with diameter of 330 mm

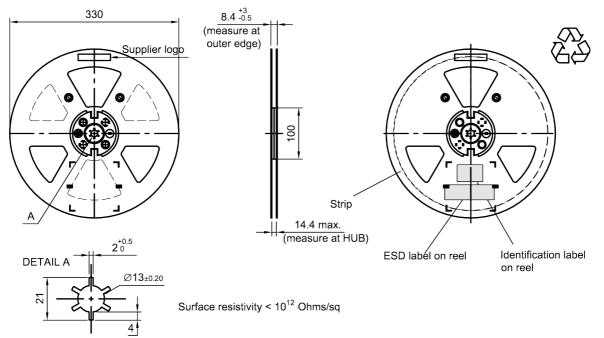


Figure 16: Drawing of reel (first-angle projection) with diameter of 330 mm.



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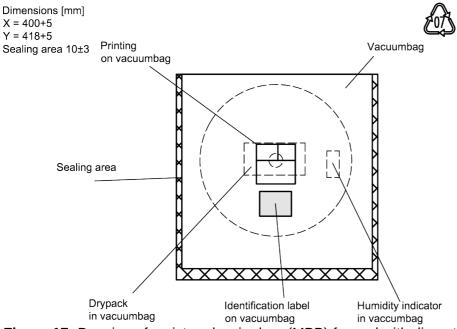


Figure 17: Drawing of moisture barrier bag (MBB) for reel with diameter of 330 mm.

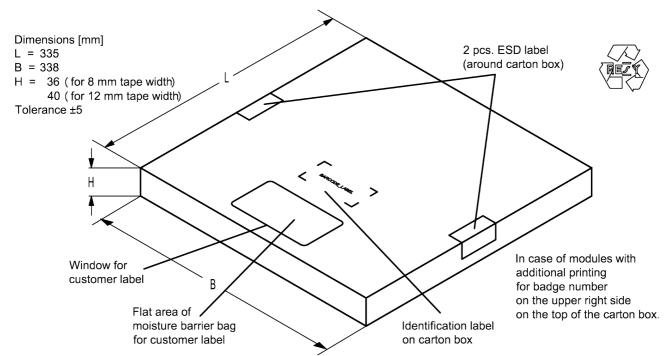


Figure 18: Drawing of folding box for reel with diameter of 330 mm.



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12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:

The 4 digit type number of the ordering code, e.g., B3xxxxB**1234**xxxx, is encoded by a special BASE32 code into a 3 digit marking.

Example of decoding type number marking on device in decimal code.

16J=>1234 $1 \times 32^2 + 6 \times 32^1 + 18$ (=J) $\times 32^0$ =1234

The BASE32 code for product type B8044 is 7VC.

■ Lot number:

The last 5 digits of the lot number, e.g., are encoded based on a special BASE47 code into a 3 digit marking.

Example of decoding lot number marking on device in decimal code.

5UY => 12345 $5 \times 47^2 + 27 = (=U) \times 47^1 + 31 = (=Y) \times 47^0 = (=V) \times$

Adopted BASE32 code for type number									
Decimal	Base32	Decimal	Base32						
value	code	value	code						
0	0	16	G						
1	1	17	Н						
2	2	18	J						
3	3	19	K						
4	4	20	M						
5	5	21	N						
6	6	22	Р						
7	7	23	Q						
8	8	24	R						
9	9	25	S						
10	Α	26	Т						
11	В	27	V						
12	С	28	W						
13	D	29	X						
14	E	30	Y						
15	F	31	Z						

Adopted BASE47 code for lot number								
Decimal	Base47	Decimal	Base47					
value	code	value	code					
0	0	24	R					
1	1	25	S					
2	2	26	Т					
3	3	27	U					
4	4	28	V					
5	5	29	W					
6	6	30	Х					
7	7	31	Υ					
8	8	32	Z					
9	9	33	b					
10	Α	34	d					
11	В	35	f					
12	С	36	h					
13	D	37	n					
14	E	38	r					
15	F	39	t					
16	G	40	V					
17	Н	41	\					
18	J	42	?					
19	K	43	{					
20	L	44	}					
21	М	45	<					
22	N	46	>					
23	Р							

Table 2: Lists for encoding and decoding of marking.



SAW components	B8044
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13 Soldering profile

The recommended soldering process is in accordance with IEC $60068-2-58-3^{rd}$ edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
T > 220 °C	30 s to 70 s
T > 230 °C	min. 10 s
T > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature <i>T</i>	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

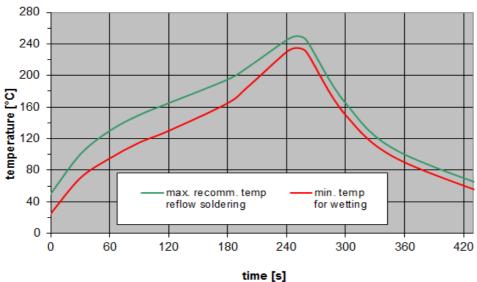


Figure 19: Recommended reflow profile for convection and infrared soldering – lead-free solder.



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14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm.

14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

14.4 Ordering codes and packing units

Ordering code	Packing unit
B39182B8044P810	5000 pcs

Table 4: Ordering codes and packing units.



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15 Cautions and warnings

15.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.rf360jv.com/orderingcodes.

15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.



Important notes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, RF360 Europe GmbH and its affiliates are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an RF360 product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
- 3. The warnings, cautions and product-specific notes must be observed.
- 4. In order to satisfy certain technical requirements, some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous). Useful information on this will be found in our Material Data Sheets on the Internet (www.rf360jv.com/material). Should you have any more detailed questions, please contact our sales offices.
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