

# **SAW Duplexer**

LTE Band 2

Series/type: B8618

Ordering code: B39202B8618P810

Date: October 13, 2015

Version: 2.1

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# 1 Application

- Low-loss SAW duplexer for mobile telephone LTE Band 2 (PCS) systems.
- Low insertion attenuation.
- Low amplitude ripple.
- Usable pass band 60 MHz.
- Single ended to balanced transformation in Antenna Rx path.
- Impedance transformation 50Ω to 100Ω in Antenna Rx path.

## 2 Features

- Package size 1.8 mm × 1.4 mm.
- Package height (max.) 0.475 mm.
- Approximate weight 0.0035 g.
- RoHS compatible.
- Package for Surface Mount Technology (SMT).
- Ni, gold-plated terminals.
- Electrostatic Sensitive Device (ESD).
- Moisture Sensitivity Level 3 (MSL3).



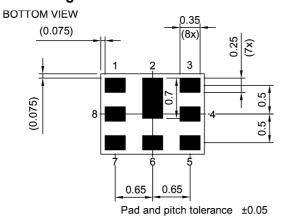
**Figure 1:** Picture of component with example of marking.



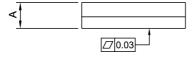
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## 3 Package

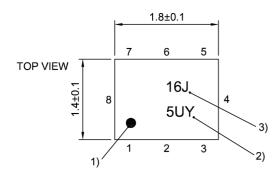


SIDE VIEW

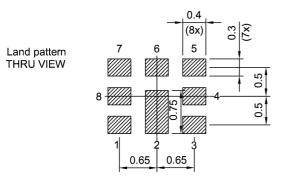


# 4 Pin configuration

- 1, 8 RX balanced
- 3 TX
- 6 ANT
- 2, 4, 5, 7 Ground



- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number



Landing pad tolerance -0.02

**Figure 2:** Drawing of package with package height A = 0.475 mm (max.). See Simplified drawings (p. 21).



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# 5 Matching circuit

■  $L_{p1,8}$  = 9.5 nH

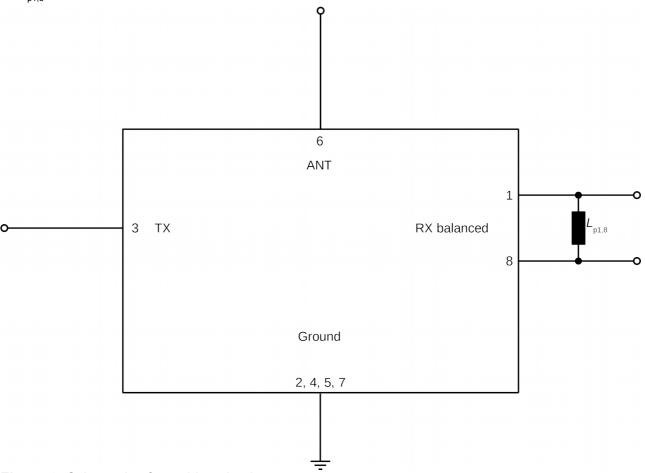


Figure 3: Schematic of matching circuit.



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#### 6 Characteristics

# 6.1 TX - ANT

Temperature range for specification  $T = -30 \,^{\circ}\text{C}$  to +85  $^{\circ}\text{C}$ 

TX terminating impedance  $Z_{\text{TX}} = 50 \ \Omega$ ANT terminating impedance  $Z_{\text{ANT}} = 50 \ \Omega$ 

RX terminating impedance  $Z_{\text{px}} = 100 \,\Omega$  with par. 9.5 nH (differential mode)

RX terminating impedance  $Z_{RX} = 25 \Omega$  (common mode)

Characteristics TX – ANT¹)				min.	<b>typ.</b> @+25 °C	max.	
Center frequency			f <sub>C</sub>	_	1880	_	MHz
Maximum insertion attenuation			$\alpha_{max}$				
	1850.24 1909.76	MHz		_	1.9	2.5	dB
Amplitude ripple (p-p)			$\Delta \alpha^{2)}$				
	1850.24 1909.76	MHz		_	0.3	1.5	dB
Maximum VSWR			VSWR <sub>max</sub>				
@ TX port	1850.24 1909.76	MHz		_	1.4	2.0	
@ ANT port	1850.24 1909.76	MHz		_	1.4	2.0	
Maximum error vector magnitude			EVM <sub>max</sub> <sup>3)</sup>				
	1852.4 1907.6	MHz		_	0.5	3.0	%
Minimum attenuation			$\alpha_{_{min}}$				
	50 787	MHz		30	42	_	dB
	728 764	MHz		40	43	_	dB
	869 894	MHz		40	43	_	dB
	1226 1250	MHz		43	50	_	dB
	1559 1606	MHz		43	54	_	dB
	1605.9 1680	MHz		30	54	_	dB
	1930.24 1989.76	MHz		44	57	_	dB
	2010 2025	MHz		20	52	_	dB
	2110 2155	MHz		44	49	_	dB
	2400 2500	MHz		25	35	_	dB
	3700 3820	MHz		26	29	_	dB
	4900 5950	MHz		21	29	_	dB
4)	5550 5730	MHz		23	27	_	dB

Specified min/max values are valid for a testing power of +10 dBm.

<sup>2)</sup> Over any channel with band width of 5 MHz.

<sup>&</sup>lt;sup>3)</sup> Error Vector Magnitude (EVM) based on definition given in 3GPP TS 25.141.



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## 6.2 ANT - RX

Temperature range for specification  $T = -30 \,^{\circ}\text{C}$  to +85  $^{\circ}\text{C}$ 

TX terminating impedance  $Z_{TX} = 50 \Omega$ ANT terminating impedance  $Z_{ANT} = 50 \Omega$ 

RX terminating impedance  $Z_{\rm RX}$  = 100  $\Omega$  with par. 9.5 nH (differential mode)

RX terminating impedance  $Z_{RX} = 25 \Omega$  (common mode)

Characteristics ANT – RX <sup>1)</sup>				min.	<b>typ.</b> @+25 °C	max.	
Center frequency			<b>f</b> <sub>C</sub>	_	1960	_	MHz
Maximum insertion attenuation			$\alpha_{\text{max}}$				
	1930.24 1989.76	MHz		_	2.7	3.5	dB
Maximum VSWR			$VSWR_{max}$				
@ ANT port	1930.24 1989.76	MHz		_	1.6	2.0	
@ RX port	1930.24 1989.76	MHz		_	1.6	2.0	
Minimum attenuation			$\boldsymbol{\alpha}_{_{min}}$				
	50 1850	MHz		45	49	_	dB
	80	MHz		50	>60	_	dB
	1850.24 1909.76	MHz		45	52	_	dB
	2050 2075	MHz		25	39	_	dB
	2075 2350	MHz		30	37	_	dB
	2350 2550	MHz		20	31	_	dB
	2550 6000	MHz		40	51	_	dB
	5610 5845	MHz		48	52	_	dB

<sup>&</sup>lt;sup>1)</sup> Specified min/max values are valid for a testing power of +10 dBm.



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## 6.3 TX - RX

Temperature range for specification  $T = -30 \,^{\circ}\text{C}$  to +85  $^{\circ}\text{C}$ 

TX terminating impedance  $Z_{TX} = 50 \Omega$ ANT terminating impedance  $Z_{ANT} = 50 \Omega$ 

RX terminating impedance  $Z_{\rm RX}$  = 100  $\Omega$  with par. 9.5 nH (differential mode)

RX terminating impedance  $Z_{RX} = 25 \Omega$  (common mode)

Characteristics TX – RX¹)				min.	<b>typ</b> . @+25 °C	max.	
Minimum differential-mode isolation		α	min				
	1574 1577	MHz		40	67	_	dB
	1850.24 1909.76	MHz		54	57	_	dB
	1930.24 1989.76	MHz		55	61	_	dB
	37003820	MHz		20	58	_	dB
	5550 5850	MHz		20	49	_	dB
Minimum common-mode isolation		α	min				
	1850.24 1909.76	MHz		43	48	_	dB

<sup>1)</sup> Specified min/max values are valid for a testing power of +10 dBm.



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# 7 Maximum ratings

Storage temperature	$T_{\text{STG}} = -40 ^{\circ}\text{C} \text{ to } +90 ^{\circ}\text{C}$					
DC voltage	$V_{DC} = 0 \text{ V (max.)}^{1)}$					
ESD voltage						
	$V_{\rm ESD}^{2)}$ 300 V (max.)	Human body model.				
	V <sub>ESD</sub> 3) 600 V (max.)	Charged device model.				
Input power @ TX port: 1850.24 1909.76 MHz	P <sub>IN</sub> = 29 dBm	Continuous wave for 5000 h @ 50 °C.				
elsewhere	= 10 dBm	-				

DC resistance at RX output might be less than 100  $M\Omega$  at elevated temperatures. Hence, using blocking capacitors is recommended.

<sup>&</sup>lt;sup>2)</sup> According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.

<sup>&</sup>lt;sup>3)</sup> According to JESD22-C101C (CDM – Field Induced Charged Device Model), 3 negative & 3 positive pulses.



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# 8 Transmission coefficients

# 8.1 TX - ANT 0.0 α/dB 1.0 1.649 2.0 .813 3.0 4.0 5.0 1860 1880 1900 1920 1840 *f*/MHz 0.0 20.0 40.0 60.0 80.0 <del>|</del> 750 1800 1850 1900 2050 1950 2000 2100 *f/*MHz 0.0 20.0 40.0 60.0

Figure 4: Attenuation TX – ANT.

1000

2000

80.0

3000

4000

5000

**f/**MHz -

6000



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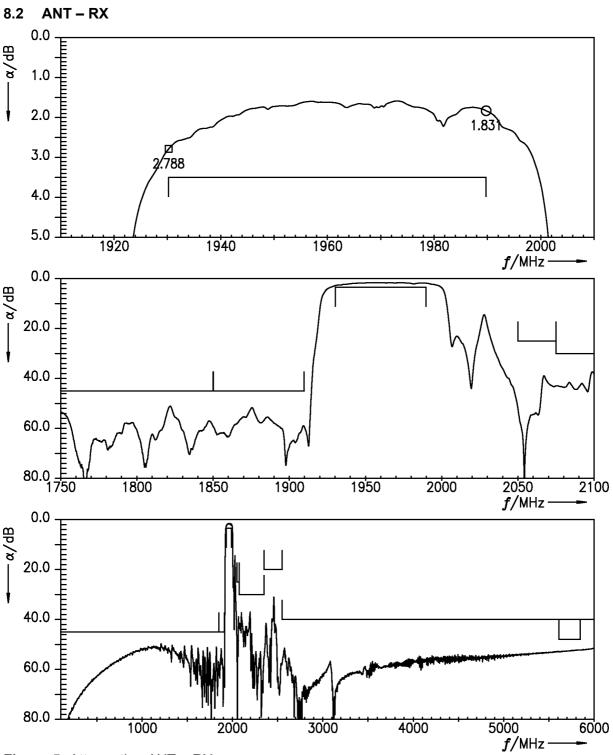


Figure 5: Attenuation ANT – RX.



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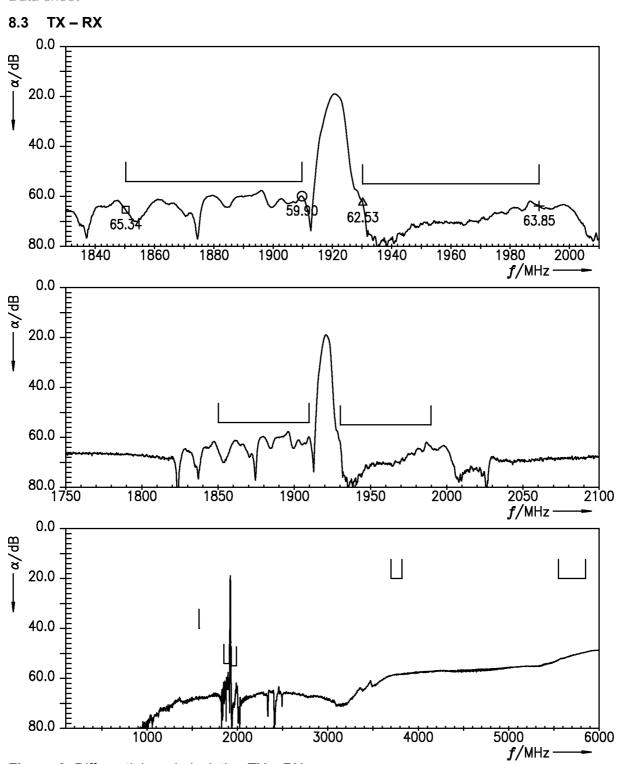


Figure 6: Differential-mode isolation TX – RX.

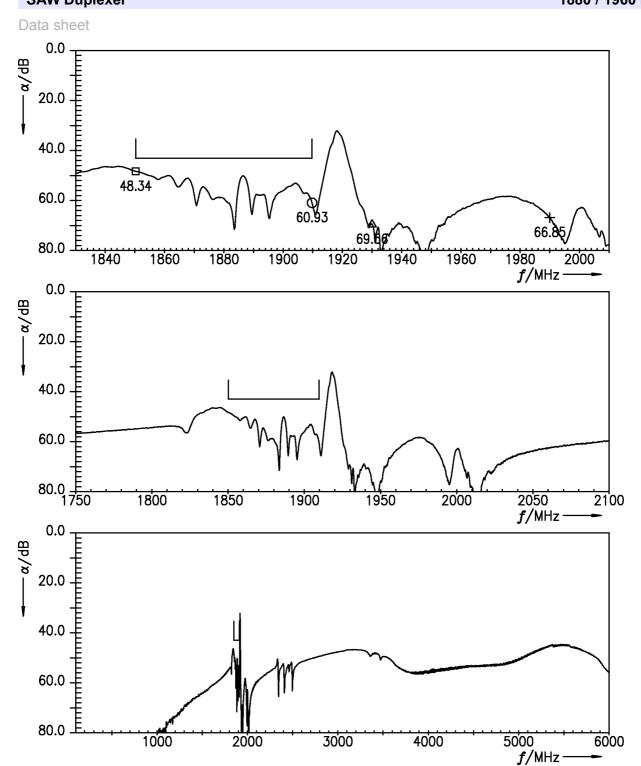


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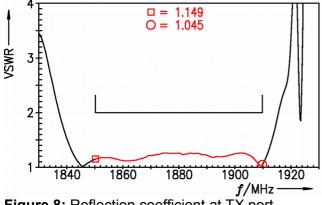
**Figure 7:** Common-mode isolation TX – RX.



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## 9 Reflection coefficients



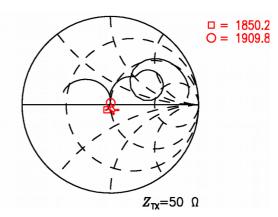
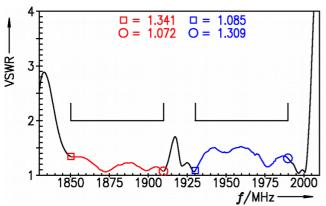


Figure 8: Reflection coefficient at TX port.



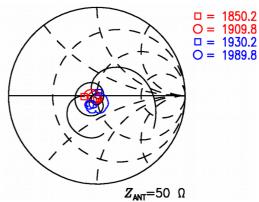
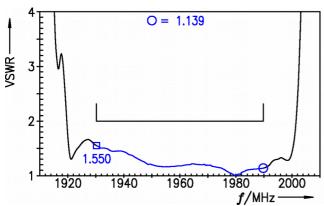


Figure 9: Reflection coefficient at ANT port (TX and RX frequencies).



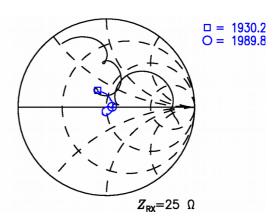


Figure 10: Reflection coefficient at RX port.

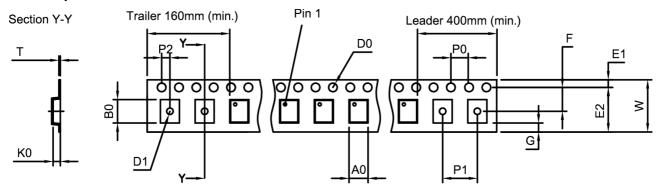


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# 10 Packing material

## 10.1 Tape



User direction of unreeling

Figure 11: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A <sub>0</sub>	1.62±0.05 mm
B <sub>0</sub>	2.04±0.05 mm
D <sub>0</sub>	1.5±0.05 mm
D <sub>1</sub>	0.8±0.05 mm
E <sub>1</sub>	1.75±0.1 mm

E	2	6.25 mm (min.)
F	=	3.5±0.05 mm
(	3	0.75 mm (min.)
K	0	0.62±0.05 mm
Р	0	4.0 <sub>±0.1</sub> mm

P <sub>1</sub>	4.0 <sub>±0.1</sub> mm
P <sub>2</sub>	2.0±0.05 mm
Т	0.25±0.02 mm
W	8.0 <sub>±0.1</sub> mm

Table 1: Tape dimensions.

## 10.2 Reel with diameter of 180 mm

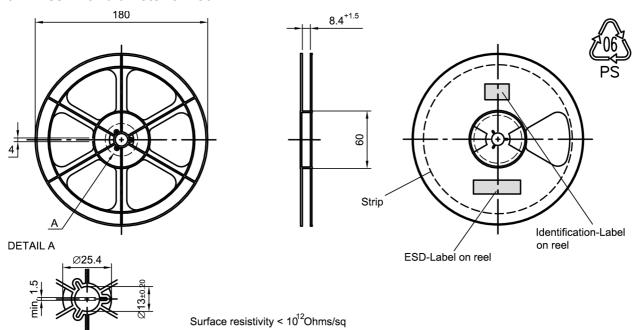
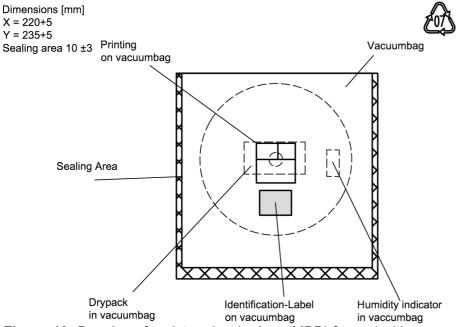


Figure 12: Drawing of reel (first-angle projection) with diameter of 180 mm.



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**Figure 13:** Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

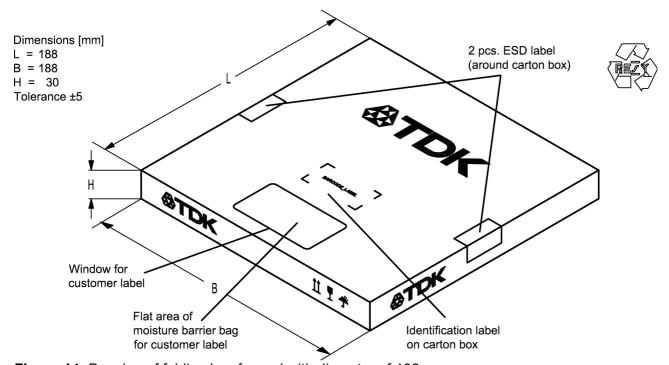


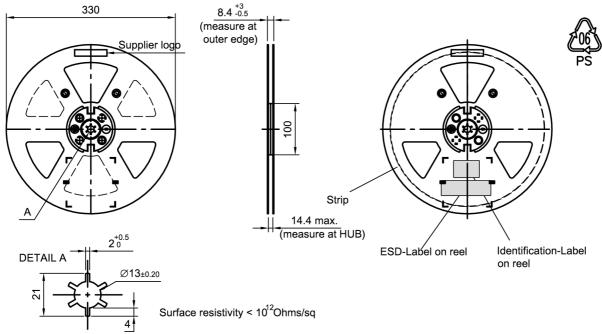
Figure 14: Drawing of folding box for reel with diameter of 180 mm.



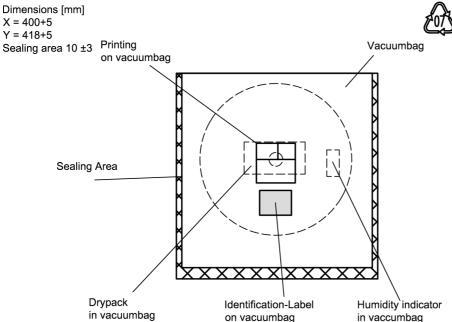
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## 10.3 Reel with diameter of 330 mm



**Figure 15:** Drawing of reel (first-angle projection) with diameter of 330 mm.



**Figure 16:** Drawing of moisture barrier bag (MBB) for reel with diameter of 330 mm.



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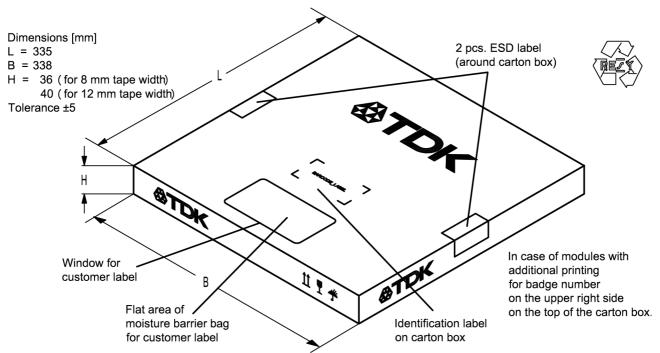


Figure 17: Drawing of folding box for reel with diameter of 330 mm.

#### 11 Marking

Products are marked with product type number and lot number encoded according to Table 2:

#### ■ Type number:

The 4 digit type number of the ordering code, is encoded by a special BASE32 code into a 3 digit marking.

Example of decoding type number marking on device e.g., B3xxxxB1234xxxx, in decimal code.

16J => 1234 1 x 32<sup>2</sup> + 6 x 32<sup>1</sup> + 18 (=J) x 32<sup>0</sup> = 1234

The BASE32 code for product type B8618 is 8DA.

#### ■ Lot number:

The last 5 digits of the lot number, e.g., are encoded based on a special BASE47 code into a 3 digit marking.

Example of decoding lot number marking on device in decimal code.

5UY=>12345 $5 \times 47^2 + 27 (=U) \times 47^1 + 31 (=Y) \times 47^0$ =12345



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Adopted BASE32 code for type number						
Decimal	Base32	Decimal	Base32			
value	code	value	code			
0	0	16	G			
1	1	17	Н			
2	2	18	J			
3	3	19	K			
4	4	20	М			
5	5	21	N			
6	6	22	Р			
7	7	23	Q			
8	8	24	R			
9	9	25	S			
10	Α	26	Т			
11	В	27	V			
12	С	28	W			
13	D	29	Х			
14	Е	30	Y			
15	F	31	Z			

Adopted BASE47 code for lot number						
Decimal	Base47	Decimal	Base47			
value	code	value	code			
0	0	24	R			
1	1	25	S			
2	2	26	Т			
3	3	27	U			
4	4	28	V			
5	5	29	W			
6	6	30	Х			
7	7	31	Y			
8	8	32	Z			
9	9	33	b			
10	Α	34	d			
11	В	35	f			
12	С	36	h			
13	D	37	n			
14	E	38	r			
15	F	39	t			
16	G	40	V			
17	Н	41	\			
18	J	42	?			
19	K	43	{			
20	L	44	}			
21	М	45	<			
22	N	46	>			
23	Р					

Table 2: Lists for encoding and decoding of marking.



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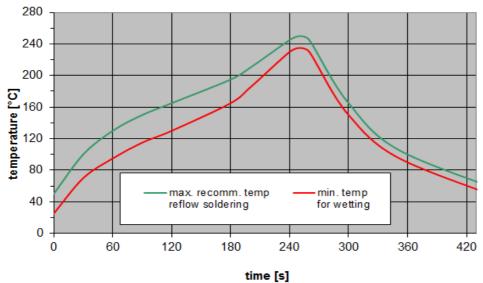
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# 12 Soldering profile

The recommended soldering process is in accordance with IEC  $60068-2-58-3^{rd}$  edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
<i>T</i> > 220 °C	30 s to 70 s
<i>T</i> > 230 °C	min. 10 s
<i>T</i> > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature $T_{peak}$	250 °C +0/-5 °C
wetting temperature $T_{min}$	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).



**Figure 18:** Recommended reflow profile for convection and infrared soldering – lead-free solder.



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#### 13 Annotations

#### 13.1 Matching coils

See TDK inductor pdf-catalog <a href="http://www.tdk.co.jp/tefe02/coil.htm#aname1">http://www.tdk.co.jp/tefe02/coil.htm#aname1</a> and Data Library for circuit simulation <a href="http://www.tdk.co.jp/etvcl/index.htm">http://www.tdk.co.jp/etvcl/index.htm</a>.

# 13.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

# 13.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.

# 13.4 Ordering code and packing units

Ordering code	Packing units	
B39202B8618P810 B39202B8618P810S 5	15000 pcs 5000 pcs	

Table 4: Ordering codes and packing units.

#### 14 Cautions and warnings

#### 14.1 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

#### 14.2 Simplified drawings

# Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

#### **Dimensions**

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

#### **Projection method**

Unless otherwise specified first-angle projection is applied.



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#### **Contact and Important notes**

For further information please contact your local EPCOS sales office or visit our web page at <a href="https://www.epcos.com">www.epcos.com</a>.

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