

ID	R _{Ds} (ON)(Typ)	VDSS
10A	1.0Ω	800V

Applications:

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability

Ordering Information

Part Number	Package	Marking	Packing	Qty.
RS10N80F	T0-220F	RS10N80F	Tube	50 PCS

Absolute Maximun Ratings Tc= 25°C unless otherwise specified

Symbol	Parameter	RS10N80F	Units
VDSS	Drain-to-Source Voltage	800	V
ID	Continuous Drain Current TC=25 $^{\circ}$ C	10	Δ
IDM	Pulsed Drain Current (Note*1)	40	A
PD	Power Dissipation	44	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 10mH, VDD = 50V, RG = 25 Ω	405	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	°C
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the" Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS10N80F	Units	Test Conditions
RØJC	Junction-to-Case	2.85	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^\circ\!\!C$
RθJA	Junction-to- Ambient	62.5		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25° C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	800			V	VGS=0V,ID=250μ Α
IDSS	Drain- to- Source Leakage Current			1	μA	VDS=800V,VGS= 0V
	Gate- to- Source Forward Leakage			100	~ ^	VGS=30V ,VDS=0 V
IGSS	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS= 0V

ON Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		1.0	1.2	Ω	VGS=10V,ID=5A
VGS(TH)	Gate Threshold Voltage	3		4	V	VGS=VDS,ID=25 0μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		45			
trise	Rise Time		17			VDS=400V
td(OFF)	Turn- OFF Delay Time		355		nS	ID=10A RG=25Ω
tfall	Fall Time		475			



Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		1519			VGS=0V
Coss	Output Capacitance		162		pF	VDS=25V
Crss	Reverse Transfer Capacitance		34			f=1.0MHz
Qg	Total Gate Charge		57			VDS=640V
Qgs	Gate- to- Source Charge		24		nC	ID=10A
Qgd	Gate-to-Drain(" Miller") Charge		7.5			VGS=10V

Dynamic Characteristics Essentially independent of operating temperature

Source- Drain Diode Characteristics

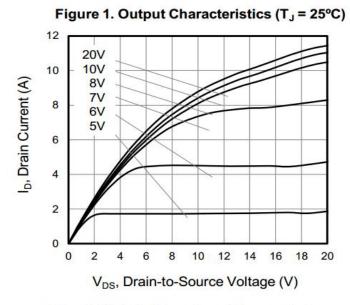
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			10	А	Integral pn- diode
ISM	Maximum Pulsed Current			40	Α	in MOSFET
VSD	Diode Forward Voltage			1.4	V	IS=5A,VGS=0V
trr	Reverse Recovery Time		562		nS	VGS=0V
Qrr	Reverse Recovery Charge		4.4		μC	IS=10A,di/dt=100 A/µs

Notes:

- * 1. Repetitive rating, pulse width limited by maximum junction temperature.
- * 2. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%



Typical Feature Curve





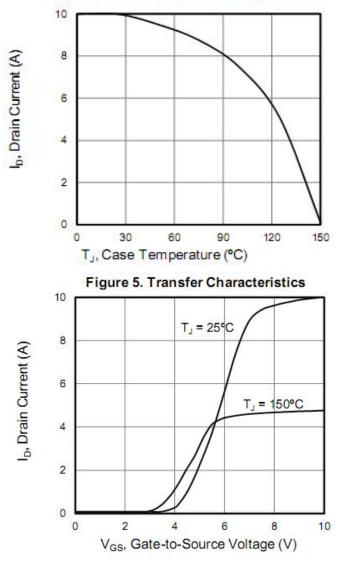


Figure 2. Body Diode Forward Voltage

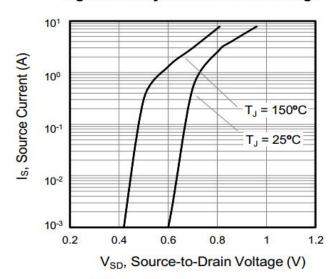


Figure 4. BV_{DSS} Variation vs. Temperature

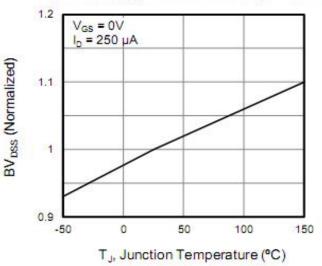
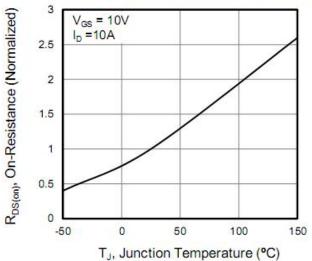


Figure 6. On-Resistance vs. Temperature



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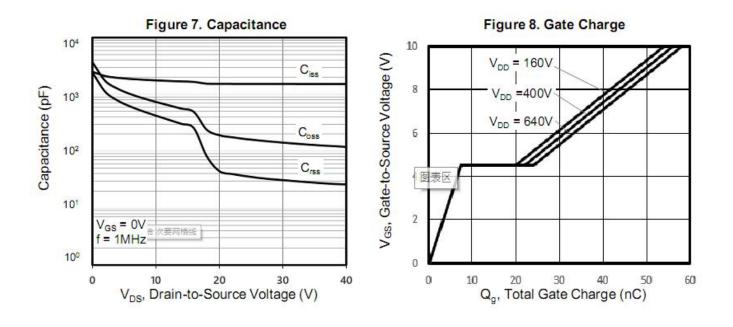
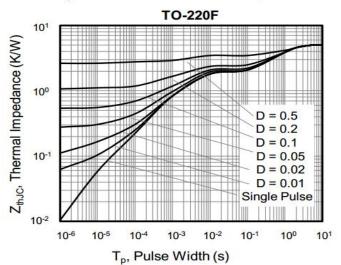


Figure 9. Transient Thermal Impedance





Test Circuits and Waveforms

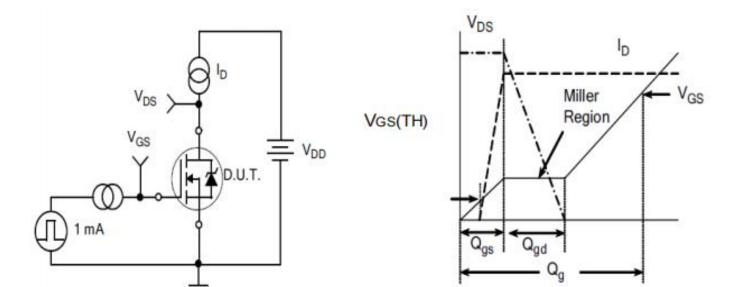
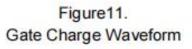


Figure10. Gate Charge Test Circuit



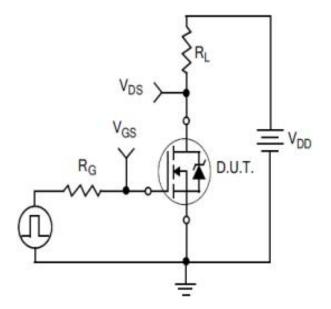


Figure12. Resistive Switching Test Circuit

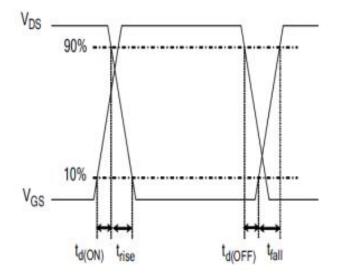
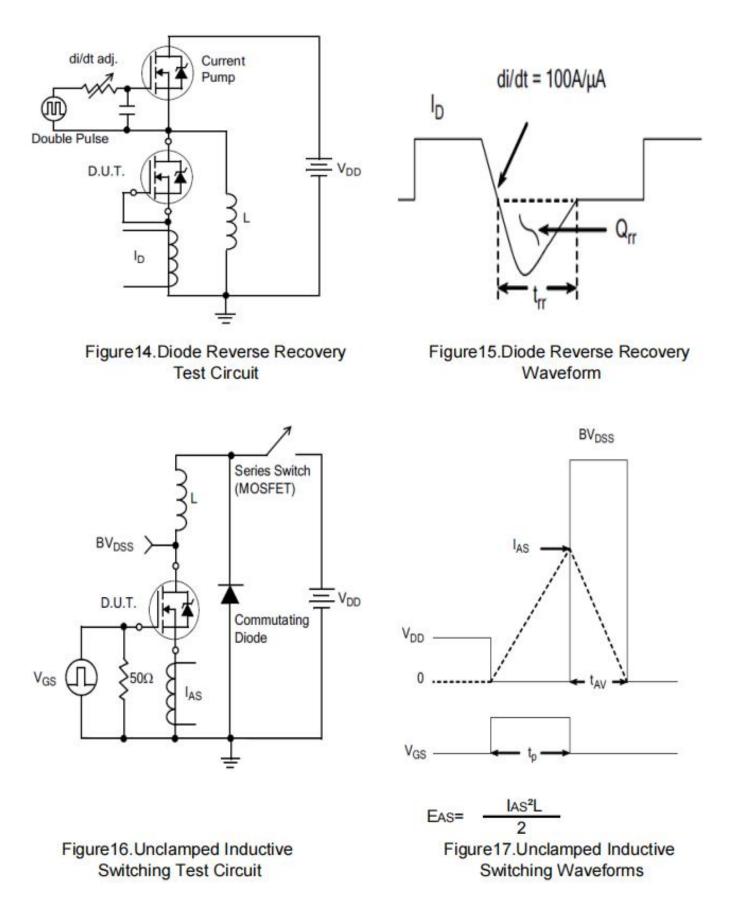


Figure13. Resistive Switching Waveforms

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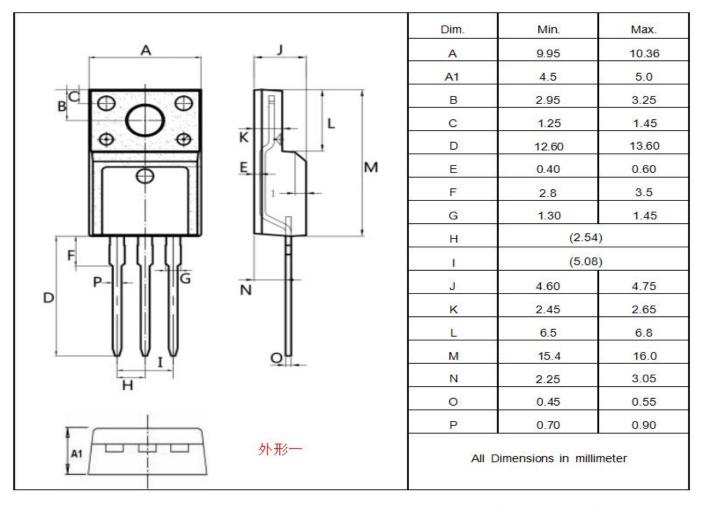


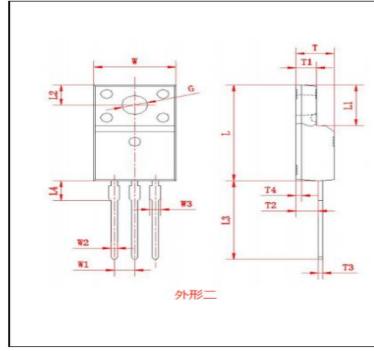
Test Circuits and Waveforms





Package outline drawing(TO-220F Unit: mm)





Dim.	Min.	Max.		
W	9.95	10.36		
W1	(2.54)			
W2	0.70	0.90		
W3	1.25	1.47		
L	15.67	16.07		
L1	6.48	6.88		
L2	3.2	3.4		
L3	12.6	13.6		
L4	(3.23	(3.23)		
т	4.50	4.90		
T1	2.34	2.74		
Т2	2.25	2.95		
T3	0.45	0.60		
T4	(0.	70)		
G	3.08	3.28		



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