

VRRM	IF (TC≤135°C)	QC
650V	25A	62nC

Applications:

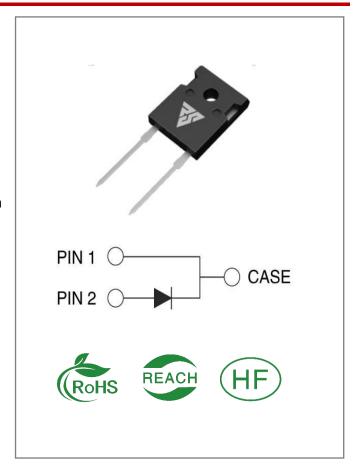
- Switch Mode Power Supplies
- Power Factor Correction
- Motor drive, PV Inverter, Wind Power Station

Features:

- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- Positive Temperature Coefficient on VF
- Temperature-independent Switching
- 175°C Operating Junction Temperature

Benefits:

- Replace Bipolar with Unipolar Device
- Reduction of Heat Sink Size
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses



Ordering Information

Part Number	Package	Marking	Packing	Qty.
RSS20065W	TO-247-2	RSS20065W	Tube	30 PCS



Maximum Ratings (TJ= 25°C unless otherwise specified)

Symbo I	Parameter	Valu e	Unit	Test Conditions	Not e
VRRM	Repetitive Peak Reverse Voltage	650	V	TC = 25°C	
VRSM	Surge Peak Reverse Voltage	650	V	TC = 25°C	
VR	DC Blocking Voltage	650	V	TC = 25°C	
IF	Forward Current	54 25 20	А	TC ≤ 25°C TC ≤ 135°C TC ≤ 148°C	Fig.
IFSM	Non-Repetitive Forward Surge Current	170 154	А	TC = 25° C, tp = 10ms, Half Sine Wave TC = 110° C, tp = 10ms, Half Sine Wave	
IFRM	Repetitive Peak Forward Surge Current	159	Α	TC = 25° C, tp = 10ms, Half Sine Wave	
Ptot	Power Dissipation	204	W	TC = 25°C	Fig. 4
TC	Maximum Case Temperature	148	$^{\circ}$		
TJ,TST G	Operating Junction and Storage Temperature	-55 to17 5	$^{\circ}\!$		

Electrical Characteristics (TJ= 25 °C unless otherwise specified)

Symbo I	Parameter	Тур.	Max	Unit	Test Conditions	Note
VF	Forward Voltage	1.35 1.7	1.6 -	V	IF = 20A, TJ = 25°C IF = 20A, TJ = 175°C	Fig.1
IR	Reverse Current	6 15	100	μΑ	VR = 650V, TJ = 25 $^{\circ}$ C VR = 650V, TJ = 175 $^{\circ}$ C	Fig.2
С	Total Capacitance	906 122 118	/	pF	VR = 1V, TJ = 25° C, f = 1MHz VR = 200V, TJ = 25° C, f = 1MHz VR = 400V, TJ = 25° C, f = 1MHz	Fig.5
QC	Total Capacitive Charge	62	/	nC	VR =400V,	Fig.6
Ec	Capacitance Stored Energy	10		uJ	VR =400V,	Fig.7

Thermal Characteristics (TJ= 25°C unless otherwise specified)

Symbol	Parameter	Тур.	Unit	Note
RθJC	Thermal Resistance from Junction to Case	0.735	°C/W	Fig.8



Typical Feature Curve

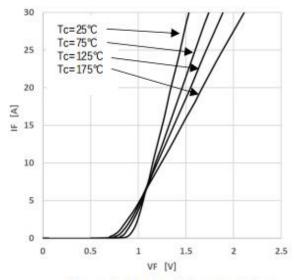


Figure 1 Forward Characteristics

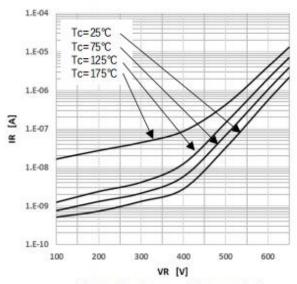


Figure 2 Reverse Characteristics

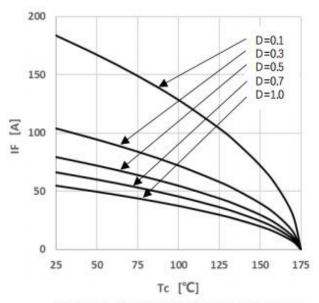


Figure 3 Peak Forward Current Derating

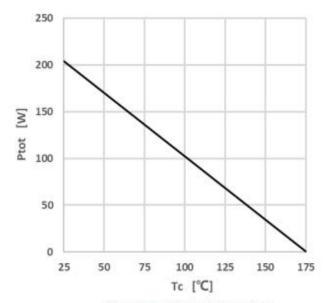


Figure 4 Power Dissipation

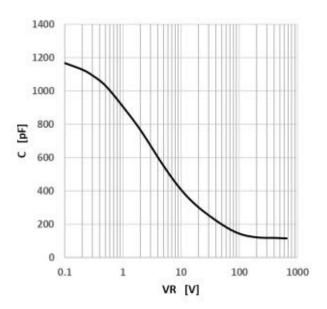


Figure 5 Capacitance vs. Reverse Voltage

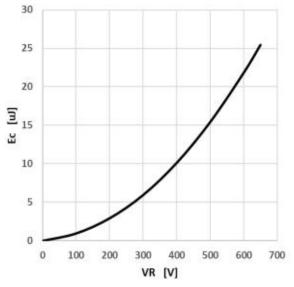


Figure 7 Capacitance Stored Energy

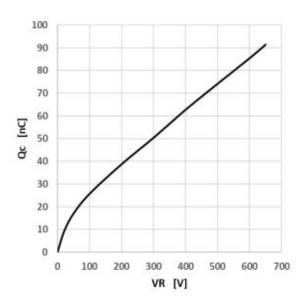


Figure 6 Capacitance Charge vs. Reverse Voltage

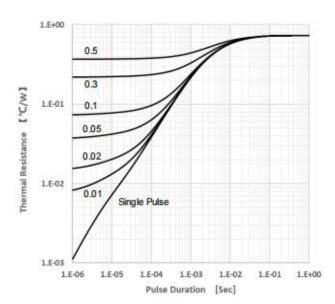
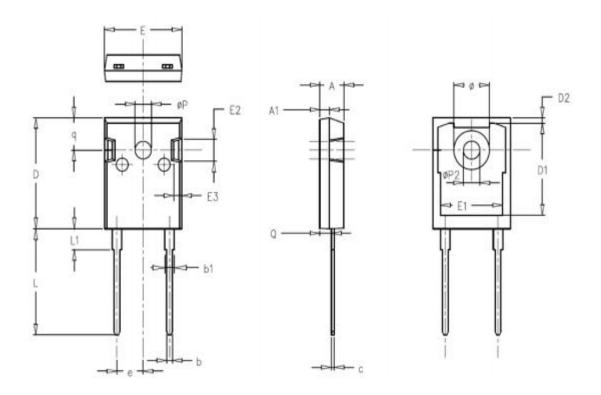


Figure 8 Transient Thermal Impedance



Package outline drawing(TO-247-2 Unit: mm)



SYMBOL	MILLIMETERS		NOTES SW	CAMBOI	М	MILLIMETERS			
	N ormal	MIN.	MAX.	N OTES	SYMBOL	Normal	MIN.	MAX.	N OTES
Α	4.98	4.68	5.36		øΡ	3.66	3.45	3.85	
A 1	1.99	1.90	2.10		е	5.44	BSC		
Q	2.41	2.30	2.60	Š	q	6.24	5.99	6.58	
С	0.60	0.48	0.72		øP2	3.45	3.24	3.64	
Ь	1.20	1.00	1.40		ø	7.14	7.10	7.30	
b1	2.07	1.90	2.30		D1	16.56	16.10	17.10	
D	21.10	20.80	21.80		D2	0.98	0.80	1.36	
E	15.98	15.38	16.20		E1	13.30	13.00	13.52	
L	20.28	19.50	20.50		E2	5.64	5.10	6.10	
L1	4.01	3.75	4.35		E3	2.33	1.90	2.70	



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- c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.
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