

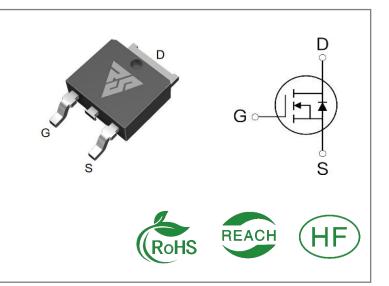
ID	R _{DS} (ON)(Typ)	VDSS
6A	1.2Ω	500V

Applications:

- Switch Mode Power Supply(SMPS)
- Adapter & Charger
- AC-DC Switching Power Supply

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	t Number Package Marki		Packing	Qty.	
RS6N50D	T0-252	RS6N50D	Tape&reel	2500 PCS	

Absolute Maximun Ratings Tc= 25°C unless otherwise specified

Symbol	Parameter	RS6N50D	Units
VDSS	Drain-to-Source Voltage	500	V
ID	Continuous Drain Current TC=25℃	6	۸
IDM	Pulsed Drain Current (Note*1)	24	A
PD	Power Dissipation	75	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 10mH, VDD = 50V, RG = 25 Ω	80	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	°C
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the" Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS6N50D	Units	Test Conditions
RθJC	Junction-to-Case	1.67	°C / W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^\circ\!\!C$
RθJA	Junction-to- Ambient	60		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25° C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	500			V	VGS=0V,ID=250μ Α
IDSS	Drain- to- Source Leakage Current			1	μA	VDS=500V,VGS= 0V
IGSS	Gate- to- Source Forward Leakage			100	- 4	VGS=30V ,VDS=0 V
	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS= 0V

ON Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		1.2	1.5	Ω	VGS=10V,ID=3A
VGS(TH)	Gate Threshold Voltage	3		4	V	VGS=VDS,ID=25 0μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter		Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		36			
trise	Rise Time		7.8		6	VDS=250V
td(OFF)	Turn- OFF Delay Time		80.5		nS	ID=6A RG=25Ω
tfall	Fall Time		23.5			



Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		535			VGS=0V
Coss	Output Capacitance		63		pF	VDS=25V
Crss	Reverse Transfer Capacitance		4.7			f=1.0MHz
Qg	Total Gate Charge		14.4			VDS=400V
Qgs	Gate- to- Source Charge		2.8		nC	ID=6A
Qgd	Gate-to-Drain(" Miller") Charge		6.8			VGS=10V

Source- Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			6	А	Integral pn- diode
ISM	Maximum Pulsed Current			24	А	in MOSFET
VSD	Diode Forward Voltage			1.4	V	IS=3A,VGS=0V
trr	Reverse Recovery Time		460		nS	VGS=0V
Qrr	Qrr Reverse Recovery Charge		1.31 3		μC	IS=6A,di/dt=100A /µs

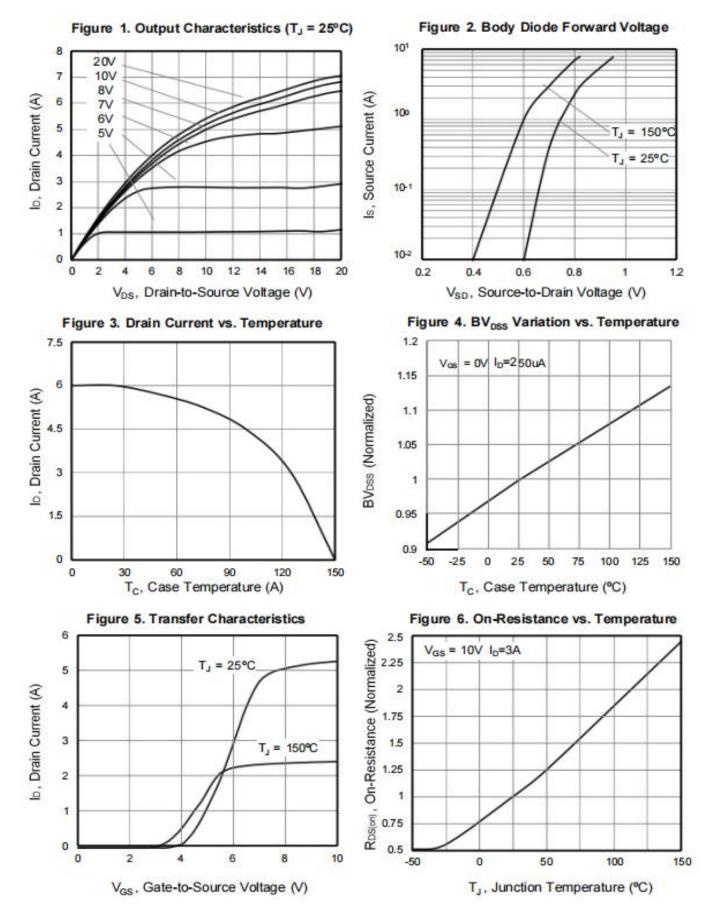
Notes:

* 1. Repetitive rating, pulse width limited by maximum junction temperature.

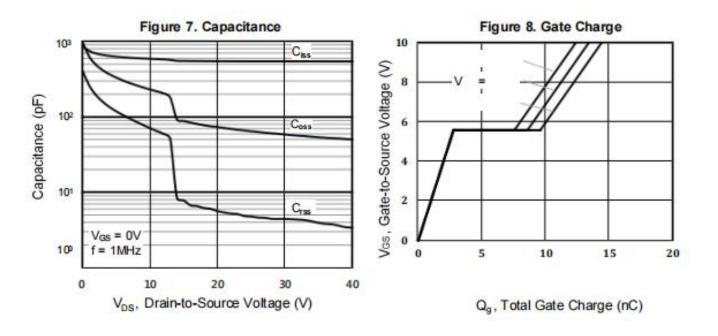
* 2. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%

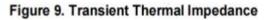


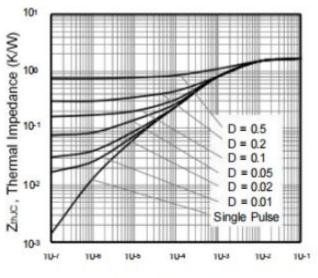
Typical Feature Curve











T_p, Pulse Width (s)



Test Circuits and Waveforms

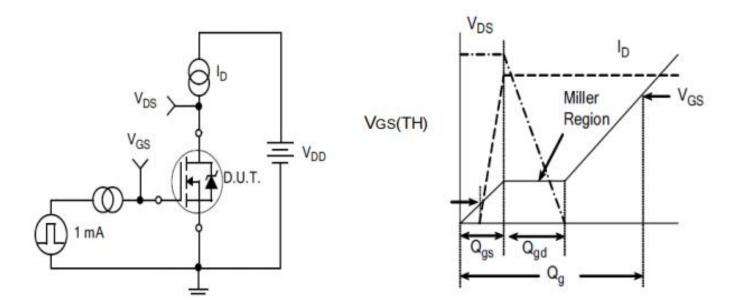
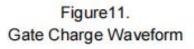


Figure10. Gate Charge Test Circuit



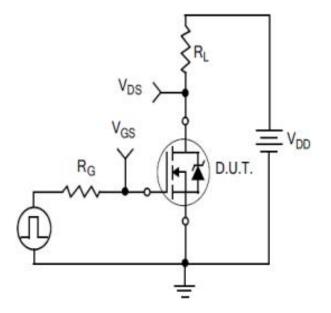


Figure12. Resistive Switching Test Circuit

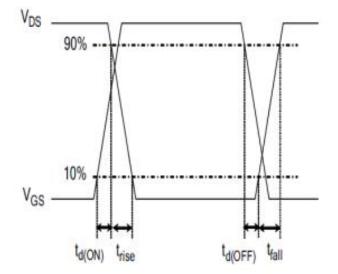
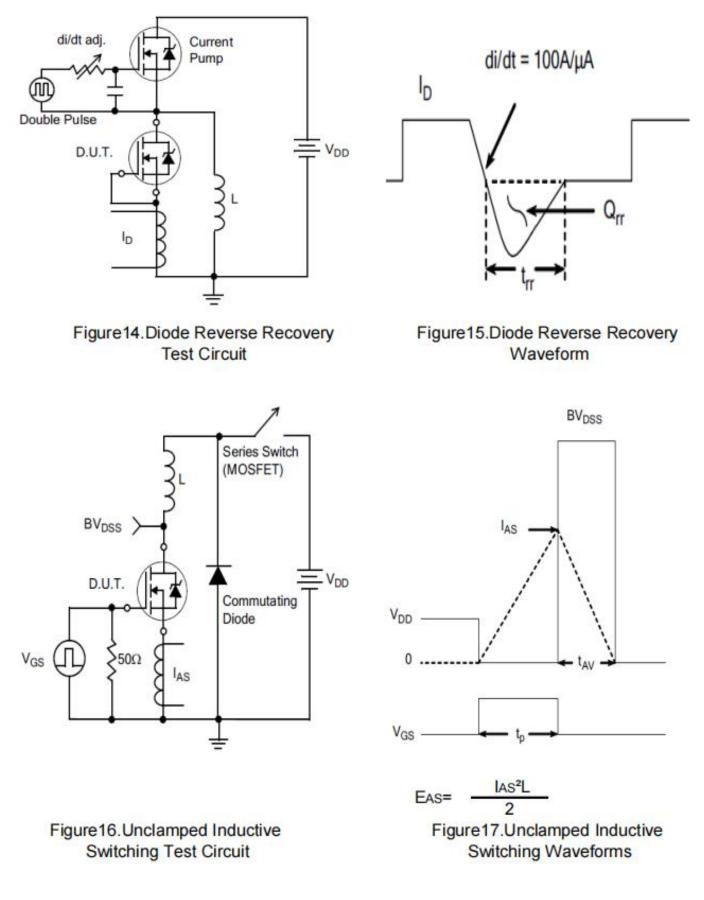


Figure13. Resistive Switching Waveforms

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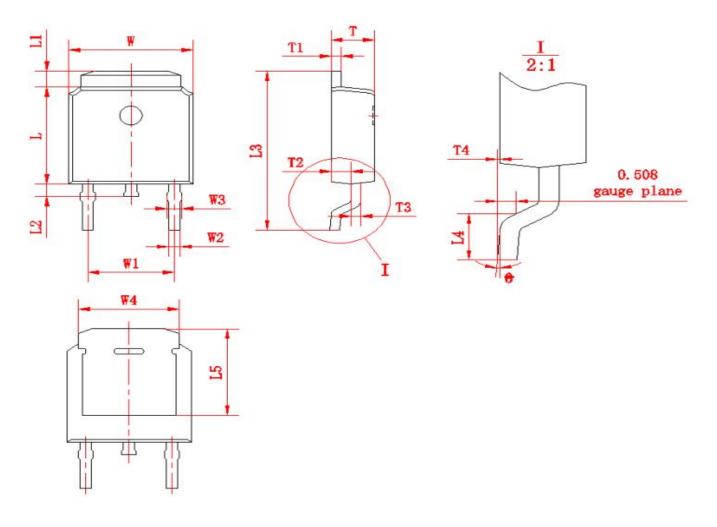


Test Circuits and Waveforms





Package outline drawing(TO-252 Unit: mm)



符号	尺寸		符号	尺寸		符号	尺寸	
小五之	Min	Max	小石	Min	Max	10 J	Min	Max
W	6.50	6.70	L1	0.80	1.20	T1	0.48	0.58
W1	(4.572)		L2	0.60	1.00	T2	0.95	1.15
W2	0.6	0.8	L3	9.70	10.30	Т3	0.48	0.58
W3	0.68	0.88	L4	1.30	1.70	T4	0.00	0.12
W4	(5	.3)	L5	(5.20)		0	0	8
L	6.00	6.20	Т	2.20	2.40			



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