

VRRM	IF ( TC≤135℃)	QC
650V	8.5A	18nC

# **Applications:**

- Switch Mode Power Supplies
- Power Factor Correction
- Motor drive, PV Inverter, Wind Power Station

#### **Features:**

- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- Positive Temperature Coefficient on VF
- Temperature-independent Switching
- 175°C Operating Junction Temperature

#### **Benefits:**

- Replace Bipolar with Unipolar Device
- Reduction of Heat Sink Size
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses

4 3 2 1 5,6,7,8
4 1,2,3
ROHS REACH HF

5 6 7

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Part Number	Package	Marking	Packing	Qty.
RSS06065G	DFN5*6	RSS06065G	Tape&reel	5000 PCS



# Maximum Ratings (TJ= 25°C unless otherwise specified)

Symbo I	Parameter	Valu e	Unit	Test Conditions	Not e
VRRM	Repetitive Peak Reverse Voltage	650	V	TC = 25℃	
VRSM	Surge Peak Reverse Voltage	650	V	TC = 25℃	
VR	DC Blocking Voltage	650	V	TC = 25℃	
IF	Forward Current	18.5 8.5 6	А	TC ≤ 25 ℃ TC ≤ 135 ℃ TC ≤ 150 ℃	Fig. 3
IFSM	Non-Repetitive Forward Surge Current	70 65	A	TC = 25℃, tp = 10ms, Half Sine Wave TC = 110℃, tp = 10ms, Half Sine Wave	
IFRM	Repetitive Peak Forward Surge Current	55	А	TC = 25℃, tp = 10ms, Half Sine Wave	
Ptot	Power Dissipation	76	W	TC = 25℃	Fig. 4
ТС	Maximum Case Temperature	150	°C		
TJ,TST G	Operating Junction and Storage Temperature	-55 to17 5	°C		

# **Electrical Characteristics** (TJ= $25^{\circ}$ C unless otherwise specified)

Symbo I	Parameter	Тур.	Max	Unit	Test Conditions	Note
VF	Forward Voltage	1.34 1.67	1.5 -	V	IF = 6A, TJ = 25℃ IF = 6A, TJ = 175℃	Fig.1
IR	Reverse Current	1.2 4.5	50 -	μA	VR = 650V, TJ = 25℃ VR = 650V, TJ = 175℃	Fig.2
с	Total Capacitance	261 35 33	/	pF	VR = 1V, TJ = 25°C, f = 1MHz VR = 200V, TJ = 25°C, f = 1MHz VR = 400V, TJ = 25°C, f = 1MHz	Fig.5
QC	Total Capacitive Charge	18	/	nC	VR =400V,	Fig.6
Ec	Capacitance Stored Energy	2.9		uJ	VR =400V,	Fig.7

# **Thermal Characteristics** (TJ= $25^{\circ}$ C unless otherwise specified)

Symbol	Parameter	Тур.	Unit	Note
RθJC	Thermal Resistance from Junction to Case	1.95	°C/W	Fig.8

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### **Typical Feature Curve**

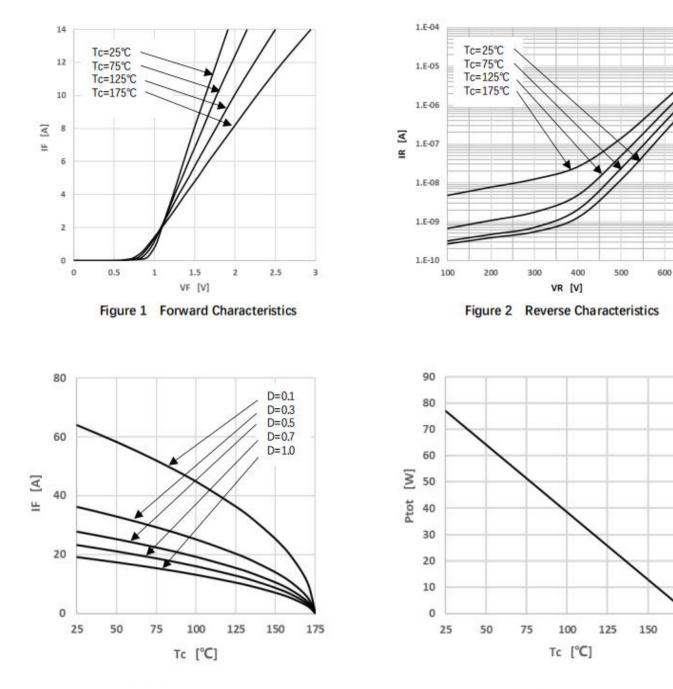


Figure 3 Peak Forward Current Derating

Figure 4 Power Dissipation

175



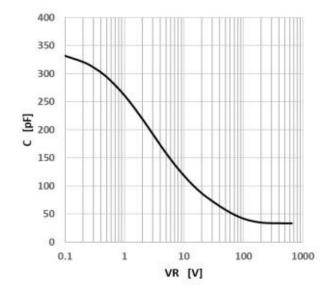


Figure 5 Capacitance vs. Reverse Voltage

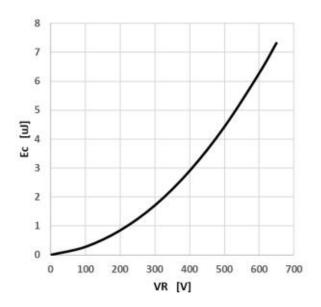


Figure 7 Capacitance Stored Energy

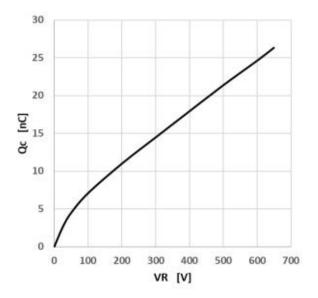


Figure 6 Capacitance Charge vs. Reverse Voltage

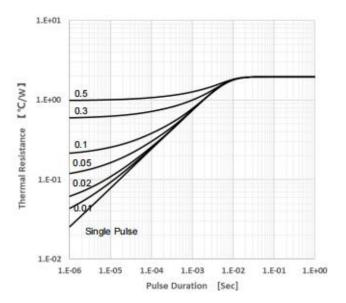
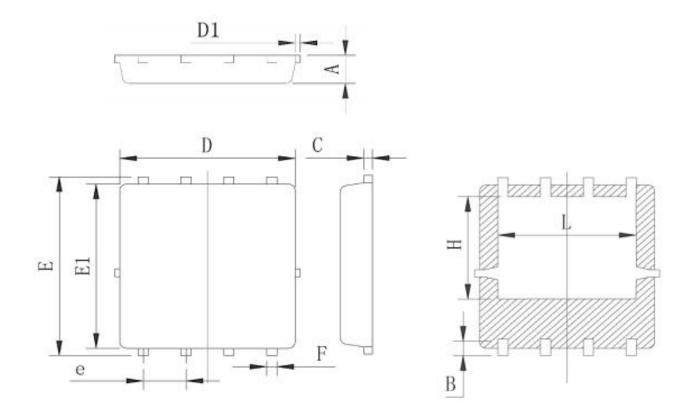


Figure 8 Transient Thermal Impedance



# Package outline drawing(DFN5\*6 Unit: mm)



0.95	
0.55	1.00
0.58	0.68
0.254	0.30
5.20	5.40
	0.15
6.05	6.20
5.55	5.70
1.27	1.32
0.30	0.35
3.47	3.67
4.00	4.20
	0.58 0.254 5.20 6.05 5.55 1.27 0.30 3.47



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