

SAW RF filter

Short range devices

Series/type: B3790

Ordering code: B39431B3790Z810

Date: December 17, 2012

Version: 2.1

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SAW RF filter 433.92 MHz

Data sheet



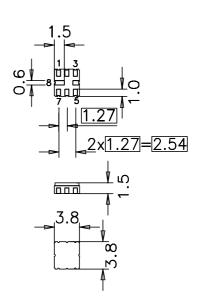
Application

- Low-loss RF filter for remote control receivers
- Balanced and unbalanced operation possible



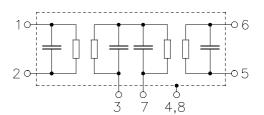
Features

- Package size 3.8 x 3.8 x 1.5 mm³
- Package code QCC8B
- RoHS compatible
- Approximate weight 0.07 g
- Package for Surface Mount Technology (SMT)
- Ni, gold-plated terminals
- Lead free soldering compatible with J STD20C
- Passivation layer Elpas
- AEC-Q200 qualified component family
- Electrostatic Sensitive Device (ESD)



Pin configuration¹⁾

- 1 Input ground (recommended) or input
- 2 Input (recommended) or input ground
- Output (recommended) or output ground 5
- 6 Output ground (recommended) or output
- 7 External coupling coil
- Ground (case) **4,8**
- to be grounded **3**
- 1) The recommended pin configuration usually offers best suppression of electrical crosstalk. The filter characteristics refer to this configuration.





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Characteristics

= -30 °C to +80 °C Temperature range for specification:

Terminating source impedance: $50\,\Omega$ and matching network Terminating load impedance: $50\,\Omega$ and matching network

		min.	typ. @ 25 °C	max.	
Center frequency	f _C	_	433.92	_	MHz
(center frequency between 3dB pionts)	J				
Minimum insertion attenuation	α_{min}				
(including losses in matching network)	- 111111				
433.86 433.98 MHz			3.6	4.3	dB
					J
Pass band (relative to αmin)					
433.86 433.98 MHz			0.5	1.5	dB
					J
Relative attenuation (relative to αmin)	$\alpha_{\rm rel}$				
10.00 250.00 MHz		60	65	_	dB
250.00 330.00 MHz		53	58	_	dB
330.00 430.00 MHz		55	60	_	dB
430.00 433.32 MHz		32	48	_	dB
					J
434.52 437.00 MHz		29	34	_	dB
437.00 530.00 MHz		55	60	_	dB
530.00 1000.00 MHz		60	65	_	dB
000.00 1000.00 WH12					42
Impedance for pass band matching1)					
Input: $Z_{IN} = R_{IN} C_{IN}$			510 1.0	_	Ω pF
Output: $Z_{OUT} = R_{OUT} C_{OUT}$			510 1.0	_	$\Omega \parallel pF$
2 a tp a = 001 001 11 9 001			5.5		11 %.

¹⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After removal of the SAW filter the input impedance of the input and output matching network is calculated. The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details we refer to EPCOS application note #4.



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SMD

Characteristics

= -40 °C to +90 °C Temperature range for specification:

Terminating source impedance: $50\,\Omega$ and matching network Terminating load impedance: $50\,\Omega$ and matching network

		min.	typ. @ 25 °C	max.	
Center frequency	f _C	_	433.92	_	MHz
(center frequency between 3dB pionts)	C				
Minimum insertion attenuation	α_{min}				
(including losses in matching network)					
433.86 433.98 MHz		_	3.6	4.4	dB
Pass band (relative to αmin)					
433.86 433.98 MHz		_	0.5	3.0	dB
Relative attenuation (relative to αmin)	α_{rel}				
10.00 250.00 MHz	101	60	65		dB
250.00 330.00 MHz		53	58	_	dB
330.00 430.00 MHz		55	60	_	dB
430.00 433.32 MHz		30	48	_	dB
434.52 437.00 MHz		29	34		dB
437.00 530.00 MHz		55	60		dB
530.00 1000.00 MHz		60	65	_	dB
Impedance for pass band matching1)					
Input: $Z_{IN} = R_{IN} C_{IN}$			510 1.0	_	$\Omega \parallel pF$
Output: Z _{OUT} = R _{OUT} C _{OUT}		_	510 1.0	_	Ω pF

¹⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After removal of the SAW filter the input impedance of the input and output matching network is calculated. The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details we refer to EPCOS application note #4.

Maximum ratings

Operable temperature range	Т	-45/+125	°C	
Storage temperature range	T_{stg}	-45/+125	°C	
DC voltage	V_{DC}	6	V	
Source power	P_S	5	dBm	source impedance 50 Ω

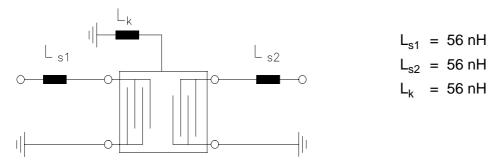


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Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)



Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection



Optimised PCB layout for SAW filters in QCC8B package, pinning 2,5 (top side, scale 1:1)

The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.



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ESD protection of SAW filters

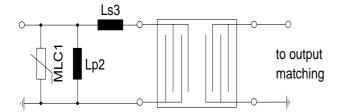
SAW filters are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, "ESD matching" has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below two figures show recommended "ESD matching" topologies.

Depending on the input impedance of the SAW filter and the source impedance, the needed component values have to be determined from case to case.



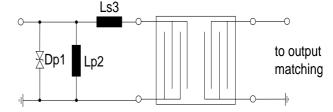
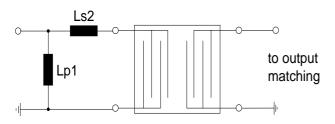


Fig. 1 MLC varistor plus ESD matching

Fig. 2 Suppressor diode plus ESD matching

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.



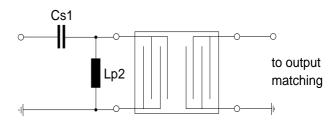


Fig. 3 shunt L – series L matching

Fig. 4 series C – shunt L matching

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to EPCOS Application report:

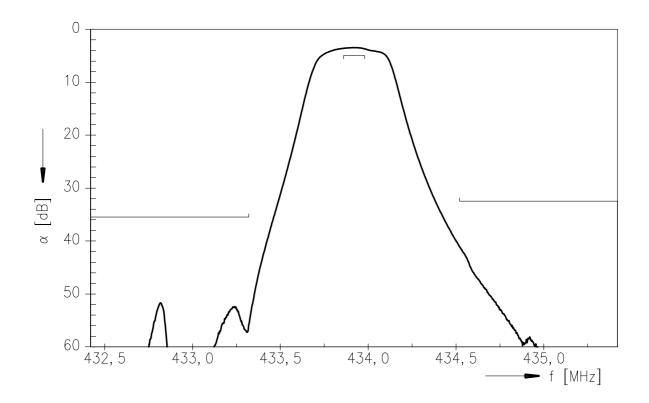
"ESD protection for SAW filters". This report can be found under www.epcos.com/rke. Click on "data sheets" and then "Applications" under category "Further information".



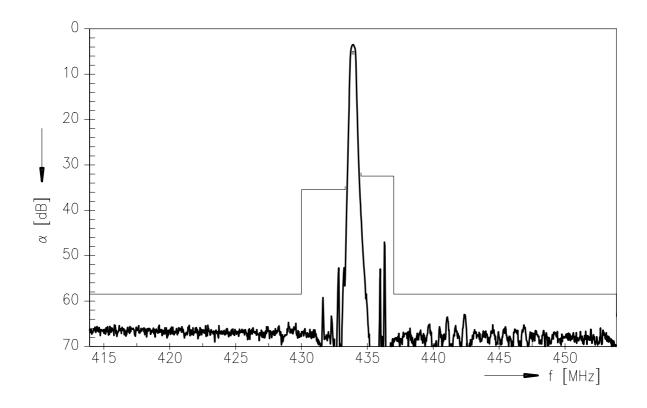
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Normalized frequency response



Normalized frequency response (wideband)



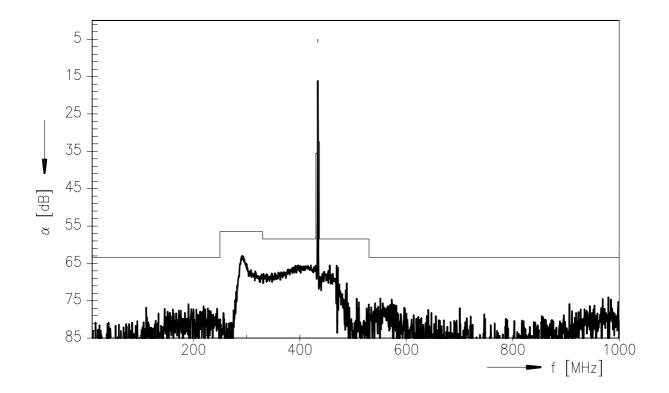


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Transfer response (ultimate rejection)





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References

Туре	B3790
Ordering code	B39431B3790Z810
Marking and package	C61157-A7-A46
Packaging	F61074-V8167-Z000
Date codes	L_1126
S-parameters	B3790_NB.s2p B3790_WB.s2p See file header for port/pin assignment table
Soldering profile	S_6001
RoHS compatible	RoHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8 th , 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.
Matching coils	See Inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm

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Published by EPCOS AG Systems, Acoustics, Waves Business Group P.O. Box 80 17 09, 81617 Munich, GERMANY

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