# 300mA, 0.5% Accuracy Low Dropout, Ultra Low Noise Voltage Regulator

#### **General Description**

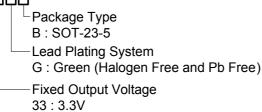
The RT9050 is a high-performance, 300mA LDO regulator, offering extremely high PSRR and ultra-low dropout. The RT9050 is Ideal for portable RF and wireless applications with demanding performance and space requirements.

The RT9050 provides quiescent current to be as low as  $25\mu$ A to extend the battery life. The RT9050 also works with low-ESR ceramic capacitors, reducing the amount of board space necessary for power applications, especially for hand-held wireless devices.

The RT9050 consumes typical  $0.7\mu$ A in shutdown mode and has fast turn-on time to be less than 40 $\mu$ s. The other features include ultra-low dropout voltage, high output accuracy, current limiting protection, and high ripple rejection ratio. The RT9050 is available in the SOT-23-5 package.

# **Ordering Information**

RT9050-000



Note :

Richtek products are :

- RoHS compliant and compatible with the current requirements of IPC/JEDEC J-STD-020.
- Suitable for use in SnPb or Pb-free soldering processes.

# **Marking Information**

For marking information, contact our sales representative directly or through a Richtek distributor located in your area.

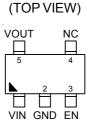
#### Features

- Wide Operating Voltage Range : 3.8V to 5.5V
- Low Dropout : 150mV at 300mA
- Ultra-Low-Noise without Bypass Capacitor
- Ultra-Fast in Line/Load Transient Response
- Current Limit Protection
- Thermal Shutdown Protection
- High Power Supply Rejection Ratio
- Only 1µF Output Capacitor Required for Stability
- TTL-Logic-Controlled Shutdown Input
- RoHS Compliant and Haloge Free

#### Applications

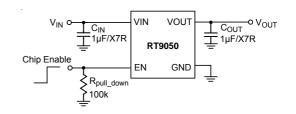
- CDMA/GSM Cellular Handsets
- Portable Information Appliances
- Laptop, Palmtops, Notebook Computers
- Hand-Held Instruments
- Mini PCI & PCI-Express Cards
- PCMCIA & New Cards

## **Pin Configurations**



SOT-23-5

## **Typical Application Circuit**

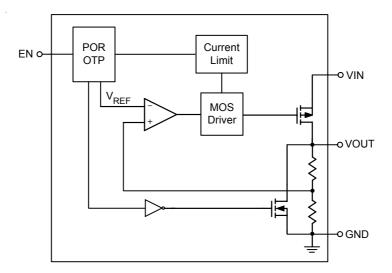




#### **Functional Pin Description**

Pin No.	Pin Name	Pin Function
1	VIN	Power Supply Input.
2	GND	Ground Pin.
3	EN	Chip Enable (Active High). It is recommended to add a 100k $\Omega$ resistor between the EN and GND.
4	NC	No Internal Connection.
5	VOUT	Regulator Output.

## **Function Block Diagram**



#### Absolute Maximum Ratings (Note 1)

Supply Input Voltage	6V
EN Input Voltage	6V
• Power Dissipation, $P_D @ T_A = 25^{\circ}C$	
SOT-23-5	0.4W
Package Thermal Resistance (Note 2)	
SOT-23-5, θ <sub>JA</sub>	250°C/W
Lead Temperature (Soldering, 10 sec.)	260°C
Junction Temperature	150°C
Storage Temperature Range	–65°C to 150°C
ESD Susceptibility (Note 3)	
НВМ	2kV
MM	200V

## Recommended Operating Conditions (Note 4)

Supply Input Voltage	- 3.8V to 5.5V
Junction Temperature Range	<ul> <li>–40°C to 125°C</li> </ul>
Ambient Temperature Range	40°C to 85°C

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#### **Electrical Characteristics**

 $(V_{IN} = V_{OUT} + 0.5V, V_{EN} = V_{IN}, C_{IN} = C_{OUT} = 1\mu F/X7R$  (Ceramic),  $T_A = 25^{\circ}C$ , unless otherwise specified)

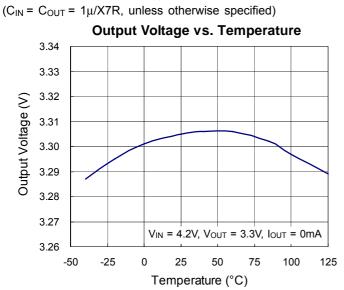
Parameter		Symbol	Test Conditions	Min	Тур	Мах	Unit
Output Noise V	Output Noise Voltage				30		$\mu V_{RMS}$
Output Voltage	utput Voltage Accuracy		$1mA \le I_{OUT} \le 150mA$ , T <sub>J</sub> = 25°C	-0.5	0	+0.5	%
Quiescent Current (Note 5)		lq	I <sub>OUT</sub> = 0mA		25	50	μA
Shutdown Current		I <sub>SHDN</sub>	V <sub>EN</sub> = 0V		0.7	1.5	μA
Current Limit		I <sub>LIM</sub>	$R_{LOAD} = 0\Omega$	300	400	600	mA
Dropout Voltage (Note 6)		V <sub>DROP</sub>	I <sub>OUT</sub> = 300mA		125	240	mV
Load Regulation	_oad Regulation (Note 7)		1mA < I <sub>OUT</sub> < 300mA			1	%
	Logic-Low Voltage	VIL		0		0.6	v
EN Threshold	Logic-High Voltage	VIH		1.6		5.5	
Enable Pin Cur	rrent	I <sub>EN</sub>		- 0.1 1		μA	
Power Supply Rejection Rate		PSRR	I <sub>OUT</sub> = 100mA, f = 10kHz		-50		dB
Line Regulation		$\Delta V_{\text{LINE}}$	V <sub>IN</sub> = (V <sub>OUT</sub> + 0.5V) to 5.5V, I <sub>OUT</sub> = 1mA	-	0.01	0.2	%/V
Thermal Shutdown Temperature		T <sub>SD</sub>			170	-	°C
Thermal Shutdown Hysteresis		$\Delta T_{SD}$			30		

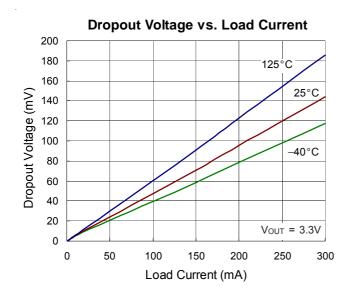
- **Note 1.** Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.
- Note 2.  $\theta_{JA}$  is measured in the natural convection at  $T_A = 25^{\circ}C$  on a low effective thermal conductivity single layer test board of JEDEC 51-3 thermal measurement standard.
- Note 3. Devices are ESD sensitive. Handling precaution is recommended.
- Note 4. The device is not guaranteed to function outside its operating conditions.
- Note 5. Quiescent, or ground current, is the difference between input and output currents. It is defined by  $I_Q = I_{IN} I_{OUT}$  under no load condition ( $I_{OUT} = 0$ mA). The total current drawn from the supply is the sum of the load current plus the ground pin current.
- Note 6. The dropout voltage is defined as  $V_{IN}$  - $V_{OUT}$ , which is measured when  $V_{OUT}$  is  $V_{OUT(NORMAL)}$  100mV.
- **Note 7.** Regulation is measured at constant junction temperature by using a 2ms current pulse. Devices are tested for load regulation in the load range from 10mA to 300mA.
- Note 8. The output voltage variation is typical ±0.5% within recommended operating temperature range.

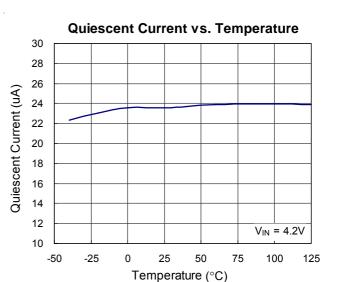


# **RT9050**

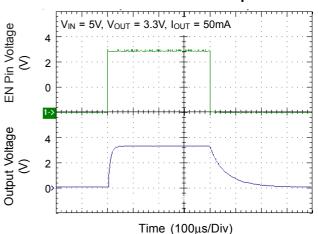
### **Typical Operating Characteristics**

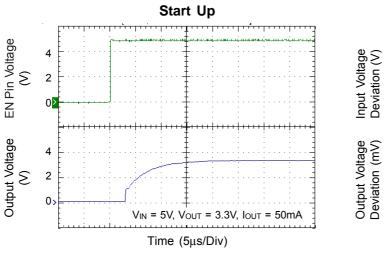


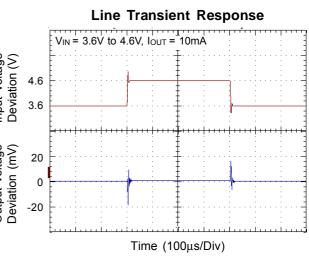




**EN Pin Shutdown Response** 





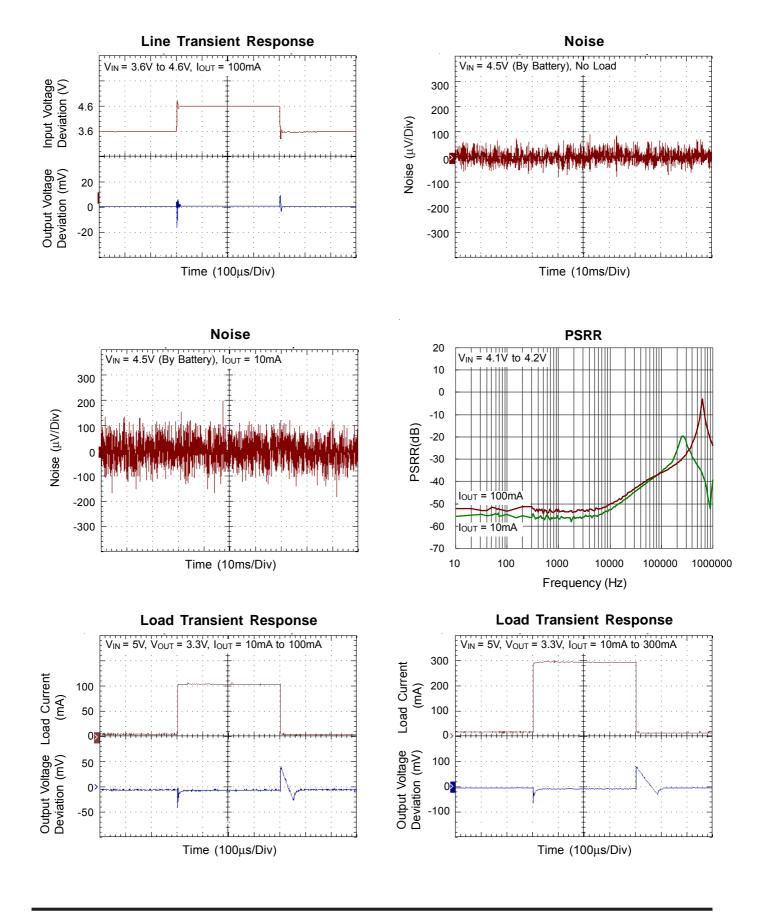


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# **RT9050**

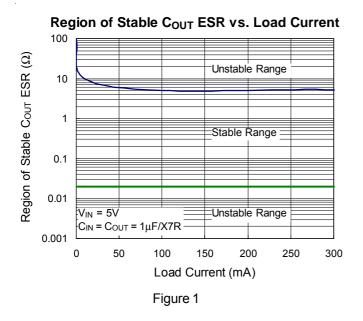


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### **Applications Information**

Like any low-dropout regulator, the external capacitors used with the RT9050 must be carefully selected for regulator stability and performance. Using a capacitor whose value is > 1 $\mu$ F / X7R on the RT9050 input and the amount of capacitance can be increased without limit. The input capacitor must be located at a distance of not more than 0.5 inch from the input pin of the IC and returned to a clean analog ground. Any good quality ceramic can be used for this capacitor. The capacitor with larger value and lower ESR (equivalent series resistance) provides better PSRR and line-transient response.

The output capacitor must meet both requirements for minimum amount of capacitance and ESR in all LDOs application. The RT9050 is designed specifically to work with low ESR ceramic output capacitor in space-saving and performance consideration. Using a ceramic capacitor whose value is at least 1 $\mu$ F with ESR is > 20m $\Omega$  on the RT9050 output ensures stability. The RT9050 still works well with output capacitor of other types due to the wide stable ESR range. Figure 1. shows the curves of allowable ESR range as a function of load current for various output capacitor values. Output capacitor of larger capacitance can reduce noise and improve load transient response, stability, and PSRR. The output capacitor should be located at not more than 0.5 inch from the VOUT pin of the RT9050 and returned to a clean analog ground.



#### Enable

The RT9050 goes into sleep mode when the EN pin is in a logic low condition. During this condition, the RT9050 has an EN pin to turn on or turn off the regulator, When the EN pin is in logic high, the regulator will be turned on. The shutdown current is  $0.7\mu$ A typical. The EN pin may be directly tied to V<sub>IN</sub> to keep the part on. The Enable input is CMOS logic and cannot be left floating.

#### PSRR

The power supply rejection ratio (PSRR) is defined as the gain from the input to output divided by the gain from the supply to the output. The PSRR is found to be

$$PSRR = 20 \times log \left( \frac{\Delta Gain \, Error}{\Delta Supply} \right)$$

Note that in heavy load measuring,  $\Delta$ supply will cause  $\Delta$ temperature. And  $\Delta$ temperature will cause  $\Delta$ output voltage. So the temperature effect is include in heavy load PSRR measuring.

#### **Current Limit**

The RT9050 contains an independent current limiter, which monitors and controls the pass transistor's gate voltage, limiting the output current to 0.4A (typ.). The output can be shorted to ground indefinitely without damaging the part.

#### **Thermal Considerations**

Thermal protection limits power dissipation in the RT9050. When the operation junction temperature exceeds 170°C, the OTP circuit starts the thermal shutdown function and turns the pass element off. The pass element will be turned on again after the junction temperature cools by 30°C.

For continuous operation, do not exceed absolute maximum operation junction temperature 125°C. The power dissipation definition in the device is calculated as follows :

$$P_{D} = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_{Q}$$

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The maximum power dissipation depends on the thermal resistance of IC package, PCB layout, the rate of surroundings airflow and temperature difference between junction to ambient. The maximum power dissipation can be calculated by following formula :

 $P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}$ 

Where  $T_{J(MAX)}$  is the maximum operation junction temperature,  $T_A$  is the ambient temperature and the  $\theta_{JA}$  is the junction to ambient thermal resistance.

For recommended operating conditions specification of the RT9050, the maximum junction temperature is 125°C. The junction to ambient thermal resistance for SOT-23-5 package is 250°C/W on the standard JEDEC 51-3 single-layer thermal test board. The maximum power dissipation at  $T_A = 25$ °C can be calculated by following formula :

 $P_{D(MAX)} = (125^{\circ}C - 25^{\circ}C) / (250^{\circ}C/W) = 0.4W$  for SOT-23-5 package

The maximum power dissipation depends on operating ambient temperature for fixed  $T_{J(MAX)}$  and thermal resistance  $\theta_{JA}$ . For RT9050 package, the Figure 2 of derating curve allows the designer to see the effect of rising ambient temperature on the maximum power dissipation allowed.

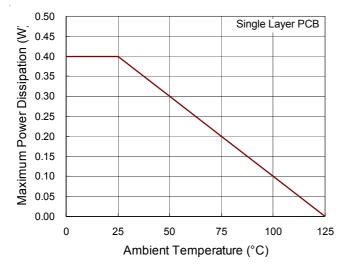
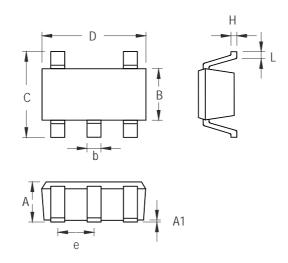


Figure 2. Derating Curve for RT9050 Package



### **Outline Dimension**



Gumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
А	0.889	1.295	0.035	0.051	
A1	0.000	0.152	0.000	0.006	
В	1.397	1.803	0.055	0.071	
b	0.356	0.559	0.014	0.022	
С	2.591	2.997	0.102	0.118	
D	2.692	3.099	0.106	0.122	
е	0.838	1.041	0.033	0.041	
Н	0.080	0.254	0.003	0.010	
L	0.300	0.610	0.012	0.024	

SOT-23-5 Surface Mount Package

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