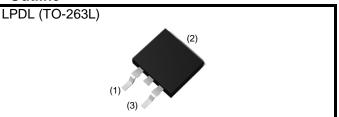


# RGS60NL65DHRBTL

650V 30A Field Stop Trench IGBT

V <sub>CES</sub>	650V
Ι <sub>C</sub>	30A
V <sub>CE(sat) (Typ.)</sub>	1.65V
P <sub>D</sub>	228W

#### Outline



#### Features

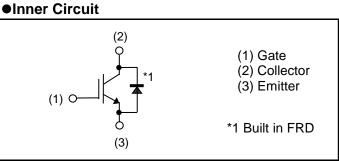
- 1) Qualified to AEC-Q101
- 2) Low Collector Emitter Saturation Voltage
- 3) Short Circuit Withstand Time 8µs
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating ; RoHS Compliant

#### Application

General Inverter

for Automotive and Industrial Use

Heater for Automotive



#### Packaging Specifications

	Packaging	Taping
	Reel Size (mm)	330
Type	Tape Width (mm)	24
Туре	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGS60NL65D

### •Absolute Maximum Ratings (at T<sub>c</sub> = 25°C unless otherwise specified)

Parameter		Value	Unit
Collector - Emitter Voltage		650	V
Gate - Emitter Voltage		±30	V
$T_{\rm C} = 25^{\circ}{\rm C}$	Ι <sub>C</sub>	59	Α
$T_c = 100^{\circ}C$	Ι <sub>C</sub>	40	Α
Pulsed Collector Current		90	Α
$T_{\rm C} = 25^{\circ}{\rm C}$	$T_{\rm C} = 25^{\circ}{\rm C}$ $I_{\rm F}$		Α
$T_{\rm C} = 100^{\circ}{\rm C}$	١ <sub>F</sub>	25	Α
Diode Pulsed Forward Current		90	Α
$T_{\rm C} = 25^{\circ}{\rm C}$	P <sub>D</sub>	228	W
$T_{\rm C} = 100^{\circ}{\rm C}$	P <sub>D</sub>	119	W
Operating Junction Temperature		-40 to +175	°C
Storage Temperature		-55 to +175	°C
	$T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$ $T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$ $T_{c} = 25^{\circ}C$ $T_{c} = 100^{\circ}C$		$\begin{array}{c c c c c c c c c c c c c c c c c c c $

\*1 Pulse width limited by T<sub>jmax.</sub>

#### Thermal Resistance

Parameter	Symbol	Values			Unit
Farameter	Symbol	Min.	Тур.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.63	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	1.55	°C/W

## •IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions		Unit			
Falameter	Symbol	Conditions	Min.	Тур.	Max.	Onic	
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	I <sub>C</sub> = 10μΑ, V <sub>GE</sub> = 0V	650	-	-	V	
		$V_{CE} = 650V, V_{GE} = 0V,$					
Collector Cut - off Current	I <sub>CES</sub>	T <sub>j</sub> = 25°C	-	-	10	μA	
		Tj = 175°C	-	0.1	-	mA	
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA	
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	V <sub>CE</sub> = 5V, I <sub>C</sub> = 1.5mA	5.0	6.0	7.0	V	
		$I_{C} = 30A, V_{GE} = 15V,$					
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	T <sub>j</sub> = 25°C	-	1.65	2.10	V	
		T <sub>j</sub> = 175°C	-	2.15	-	V	



•IGBT Electrical Characteristics	(at T	$= 25^{\circ}C$	unless	otherwise	specified)
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Deremeter	Symbol	Conditions		11-20		
Parameter	Farameter Symbol Conditions		Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V,	-	980	-	
Output Capacitance	C <sub>oes</sub>	V <sub>GE</sub> = 0V,	-	80	-	pF
Reverse transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	13	-	
Total Gate Charge	Qg	V <sub>CE</sub> = 400V,	-	36	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 30A,	-	10	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	15	-	
Turn - on Delay Time	t <sub>d(on)</sub>		-	31	-	
Rise Time	t <sub>r</sub>	$I_{\rm C} = 30$ A, $V_{\rm CC} = 400$ V,	-	13	-	
Turn - off Delay Time	t <sub>d(off)</sub>	V <sub>GE</sub> = 15V, R <sub>G</sub> = 10Ω, T <sub>i</sub> = 25°C	-	94	-	ns -
Fall Time	t <sub>f</sub>	Inductive Load	-	91	-	
Turn - on Switching Loss	Eon	*E <sub>on</sub> include diode reverse recovery	-	0.65	-	
Turn - off Switching Loss	E <sub>off</sub>	, , , , , , , , , , , , , , , , , , ,	-	0.79	-	mJ
Turn - on Delay Time	t <sub>d(on)</sub>		-	31	-	
Rise Time	t <sub>r</sub>	$I_{\rm C} = 30$ A, $V_{\rm CC} = 400$ V,	-	15	-	1
Turn - off Delay Time	t <sub>d(off)</sub>	V <sub>GE</sub> = 15V, R <sub>G</sub> = 10Ω, T <sub>i</sub> = 175°C	-	111	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	138	-	1
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> include diode reverse recovery	-	0.73	-	
Turn - off Switching Loss	E <sub>off</sub>	, ,	-	1.03	-	mJ
Reverse Bias Safe Operating Area	RBSOA	$I_{C} = 90A, V_{CC} = 520V,$ $V_{P} = 650V, V_{GE} = 15V,$ $R_{G} = 50\Omega, T_{j} = 175^{\circ}C$	FULL SQUARE		-	
Short Circuit Withstand Time	t <sub>sc</sub>	V <sub>CC</sub> ≤ 360V, V <sub>GE</sub> = 15V, T <sub>j</sub> = 25°C	8	-	-	μs
Short Circuit Withstand Time	t <sub>sc</sub> *2	V <sub>CC</sub> ≤ 360V, V <sub>GE</sub> = 15V, T <sub>j</sub> = 150°C	6	-	-	μs

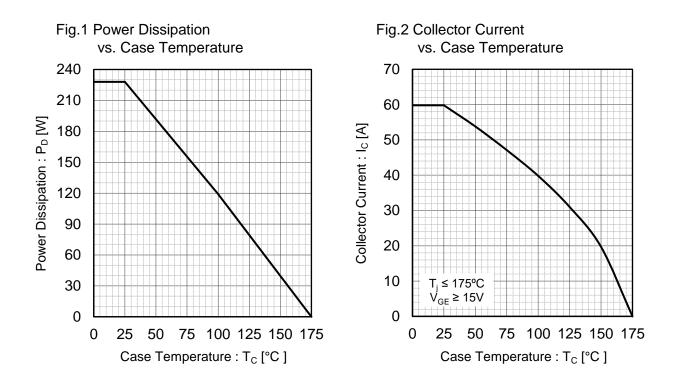
\*2 Design assurance without measurement



# •FRD Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Doromotor	C: una ha a h	Conditions	Values				
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
		I <sub>F</sub> = 25A,					
Diode Forward Voltage	$V_{F}$	T <sub>j</sub> = 25°C	-	1.5	1.95	V	
		T <sub>j</sub> = 175°C	-	1.6	-		
Diode Reverse Recovery Time	t <sub>rr</sub>		-	95	-	ns	
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	$I_F = 25A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 25^{\circ}C$	-	6.9	-	А	
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.37	-	μC	
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	16	-	μJ	
Diode Reverse Recovery Time	t <sub>rr</sub>		-	127	-	ns	
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	I <sub>F</sub> = 25A, V <sub>CC</sub> = 400V,	-	8.3	-	А	
Diode Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt = 200A/µs, T <sub>j</sub> = 175°C	-	0.64	-	μC	
Diode Reverse Recovery Energy	Err		-	34	-	μJ	





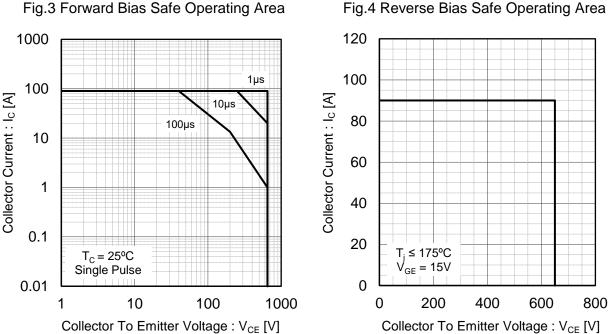


Fig.4 Reverse Bias Safe Operating Area

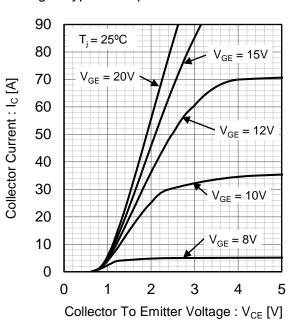
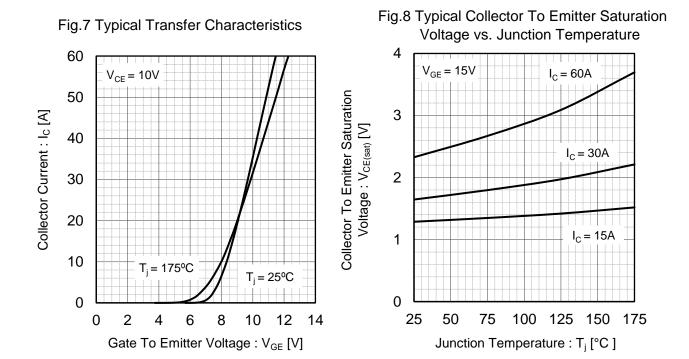


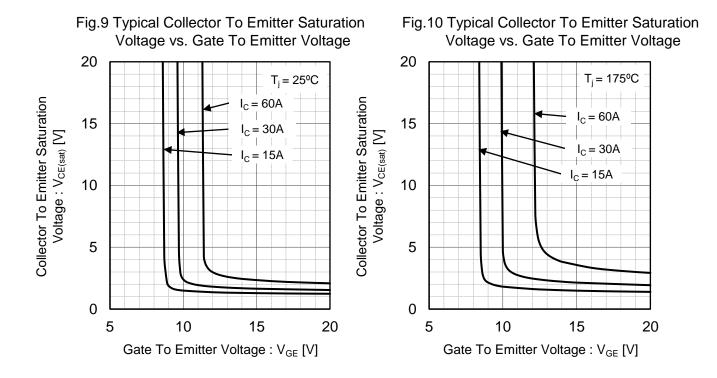
Fig.5 Typical Output Characteristics

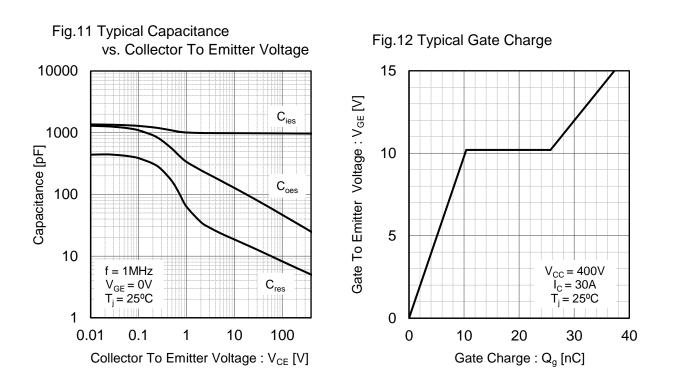
90 T<sub>j</sub> = 175°C 80 70 Collector Current : I<sub>c</sub> [A]  $V_{GE} = 20V$ 60  $V_{GE} = 15V$ 50  $V_{GE} = 12V$ 40 30  $V_{GE} = 10V$ 20 10  $V_{GE} = 8V$ 0 2 0 1 3 4 5 Collector To Emitter Voltage : V<sub>CE</sub> [V]

Fig.6 Typical Output Characteristics



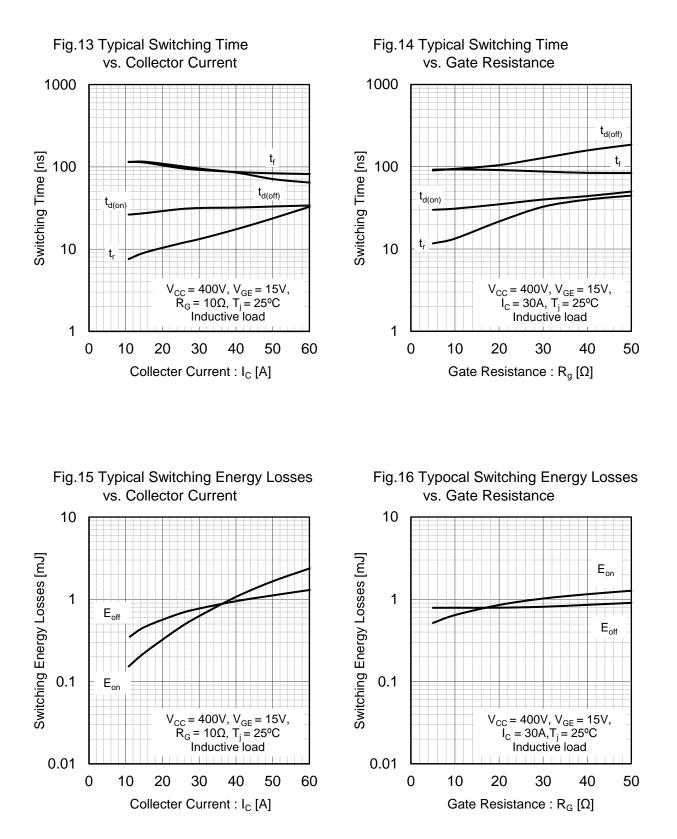




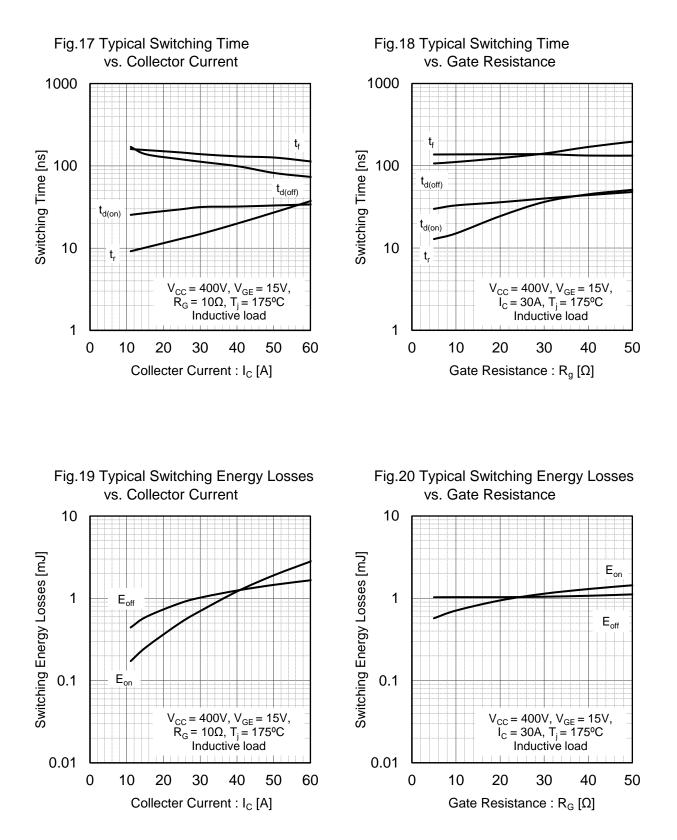


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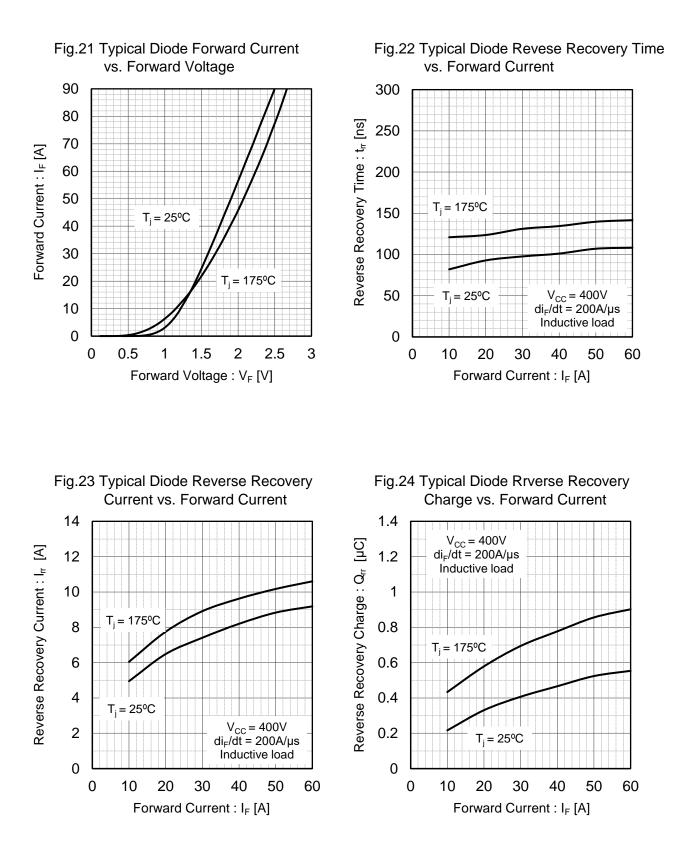












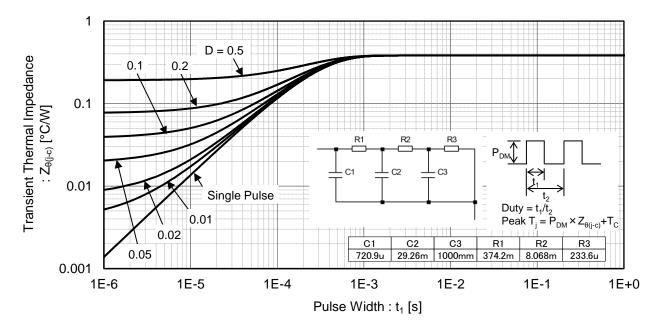
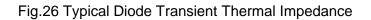
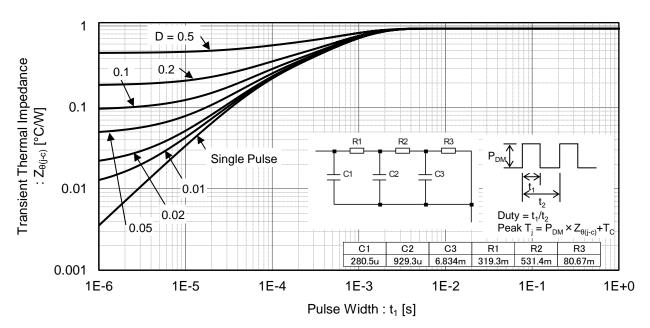


Fig.25 Typical IGBT Transient Thermal Impedance







#### Inductive Load Switching Circuit and Waveform

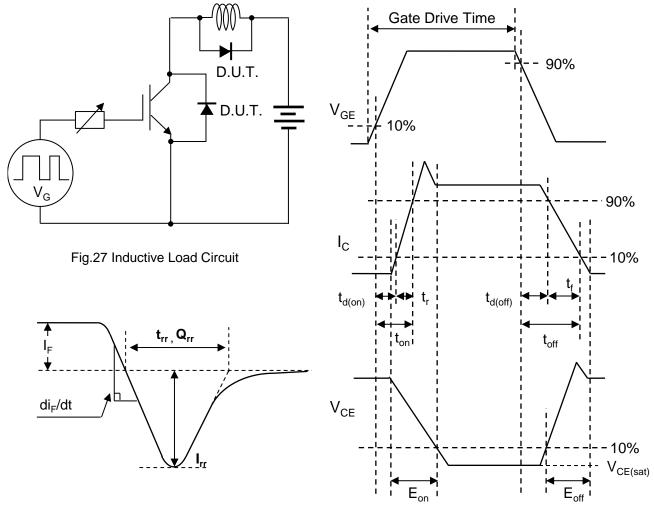


Fig.29 Diode Reverse Recovery Waveform

Fig.28 Inductive Load Waveform



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