

V _{DSS}	-20V
R _{DS(on)} (Max.)	68mΩ
Ι _D	±2.5A
P _D	1.25W

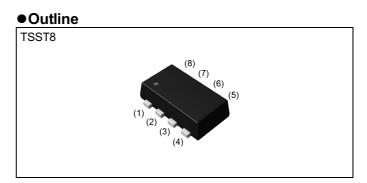
Features

- 1) Low on resistance.
- 2) -1.5V Drive.

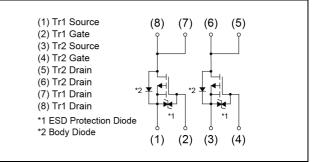
Application

Switching

- 3) Built-in G-S Protection Diode.
- 4) Small Surface Mount Package (TSST8).
- 5) Pb-free lead plating ; RoHS compliant



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
,	Basic ordering unit (pcs)	3000
	Taping code	TR
	Marking	J21

• Absolute maximum ratings ($T_a = 25^{\circ}C$) <It is the same ratings for the Tr1 and Tr2>

U (d	•			
Parameter		Symbol	Value	Unit
Drain - Source voltage		V _{DSS}	-20	V
Continuous drain current		Ι _D	±2.5	А
Pulsed drain current	I _{D,pulse} *1	±10	А	
Gate - Source voltage		V _{GSS}	±10	V
Power dissipation element		P _D *2	1.25	
			1.0	W
	total	P _D *3	0.6	
Junction temperature	·	Tj	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

•Thermal resistance

Parameter		Sumbol	Values			Unit
		Symbol	Min.	Тур.	Max.	Unit
	total	R_{thJA}^{*2}	-	-	100	°C/W
Thermal resistance, junction - ambient	element		-	-	125	
	total	R_{thJA}^{*3}	-	-	208	

•Electrical characteristics (T_a = 25°C) <It is the same characteristics for the Tr1 and Tr2>

Deremeter	Cumph of	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -1mA	-20	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I _D = -1mA referenced to 25°C	-	-21.9	-	mV/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V		-	-1	μA
Gate - Source leakage current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±10V	-	-	±10	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} = -10V, I _D = -1mA	-0.3	-	-1.0	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta \tau_j}$	I _D = -1mA referenced to 25°C	-	2.4	-	mV/°C
		V _{GS} = -4.5V, I _D = -2.5A	-	49	68	
Static drain - source	D *4	V _{GS} = -2.5V, I _D = -1.2A	-	68	95	
on - state resistance	R _{DS(on)} *4	V _{GS} = -1.8V, I _D = -1.2A	-	100	150	mΩ
		V _{GS} = -1.5V, I _D = -0.5A	-	140	280	
Gate input resistance	R _G	f = 1MHz, open drain	-	12	-	Ω
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = -10V, I _D = -2.5A	2.5	-	-	S



•Electrical characteristics (T_a = 25°C) <It is the same characteristics for the Tr1 and Tr2>

Parameter	Sumbol	Conditions	Values			Unit
Parameter	Symbol	Symbol Conditions -		Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	1270	-	
Output capacitance	C _{oss}	V _{DS} = -10V	-	100	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	90	-	
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq -10V, V_{GS} = -4.5V$	-	9	-	
Rise time	t _r *4	I _D = -1.2A	-	30	-	20
Turn - off delay time	t _{d(off)} *4	R _L = 8.3Ω	-	120	-	ns
Fall time	t_{f}^{*4}	R _G = 10Ω	-	85	-	

•Gate charge characteristics (T_a = 25°C) <It is the same characteristics for the Tr1 and Tr2>

Parameter	Sumbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*4}	$V_{DD} \simeq -10V$	-	12	-	
Gate - Source charge	Q _{gs} *4	I _D = -2.5A	-	2.5	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = -4.5V	-	2.0	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
Farameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Body diode continuous forward current	۱ _S	T - 25°0	-	-	-0.8	Δ
Body diode pulse current	I _{SP} *1	∙ T _a = 25°C	-	-	-10	A
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = -2.5A	-	-	-1.2	V

*1 Pw \leq 10µs , Duty cycle \leq 1%

- *2 Mounted on a ceramic board (30×30×0.8mm)
- *3 Mounted on a FR4 (20×20×0.8mm)
- *4 Pulsed



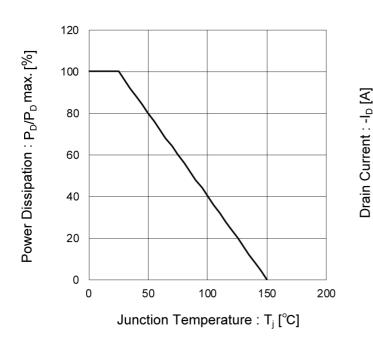


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

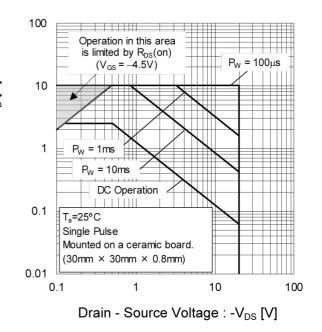
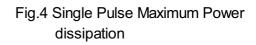
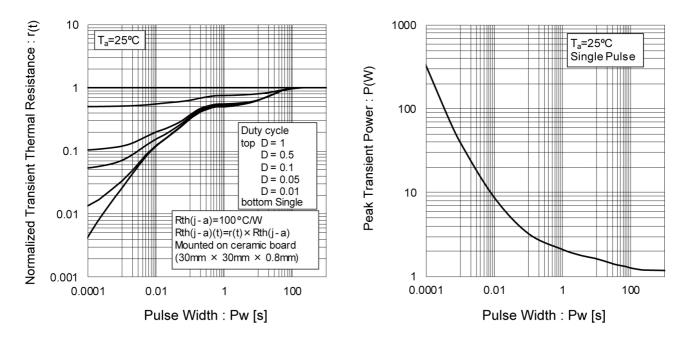


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width







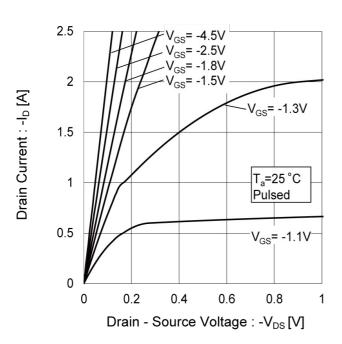


Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)

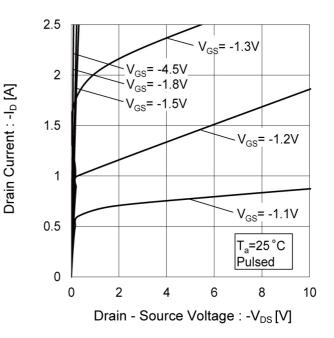


Fig.7 Breakdown Voltage vs. Junction Temperature

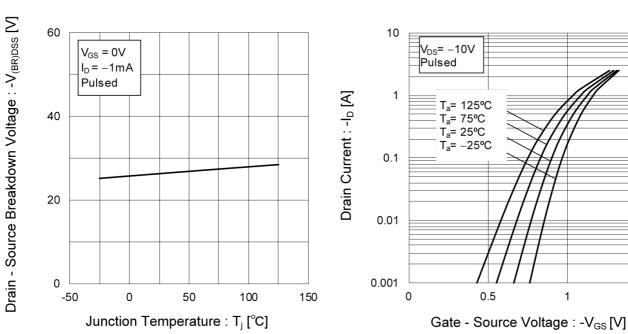


Fig.8 Typical Transfer Characteristics



1.5

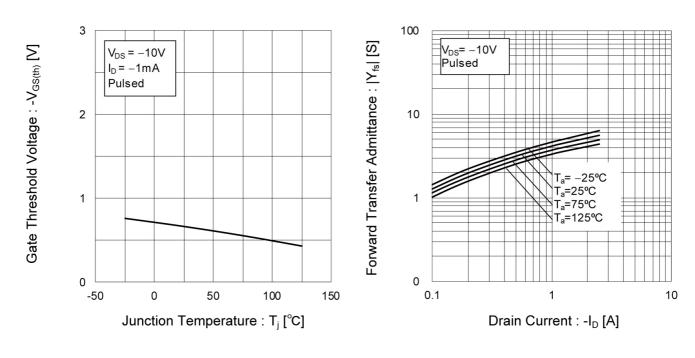


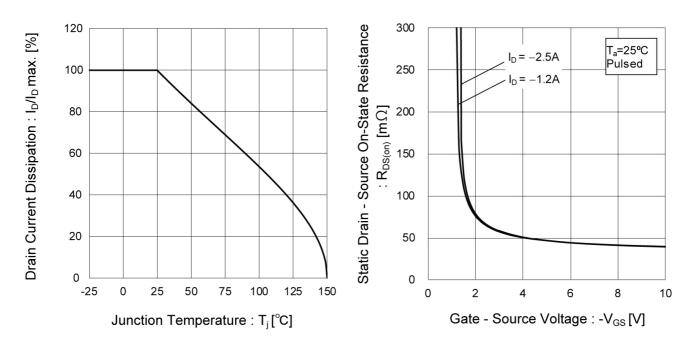
Fig.9 Gate Threshold Voltage vs. Junction Temperature

Fig.11 Drain Current Derating Curve

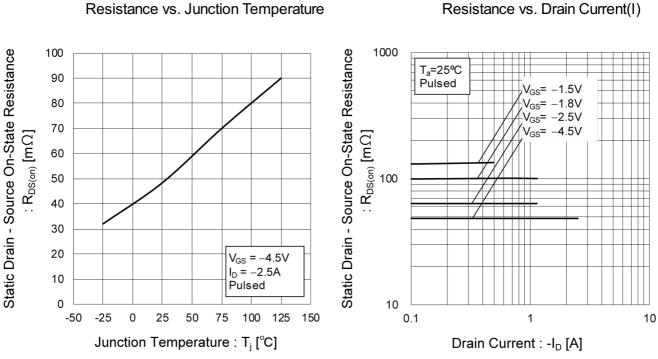
Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.10 Forward Transfer Admittance vs.

Drain Current









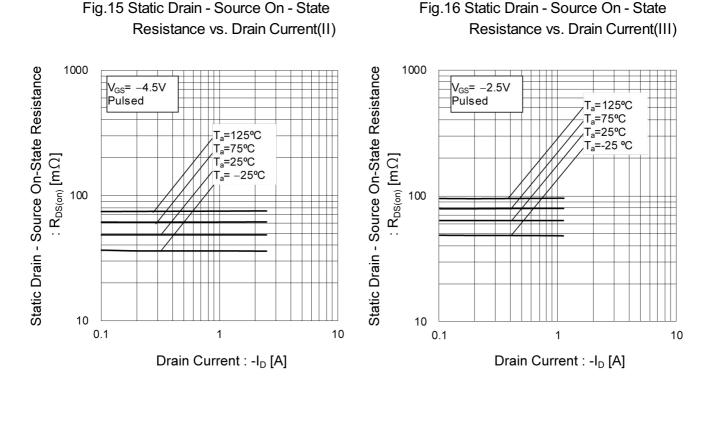
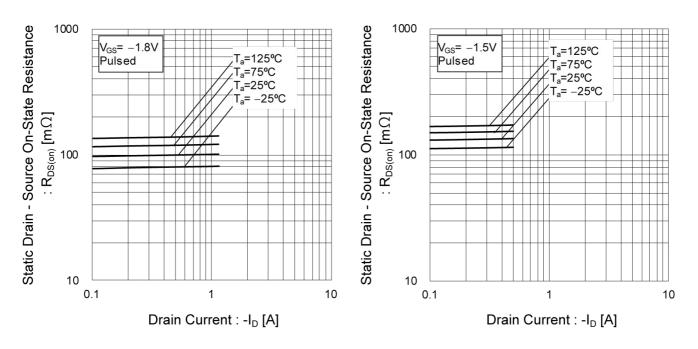


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV) Fig.18 Static Drain - Source On - State Resistance vs. Drain Current(V)





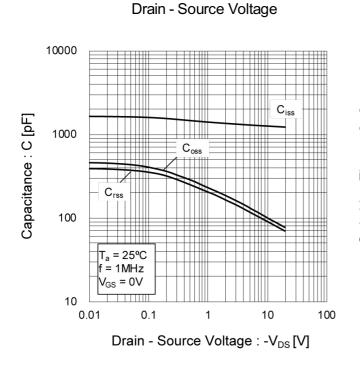


Fig.19 Typical Capacitance vs.

Fig.20 Switching Characteristics

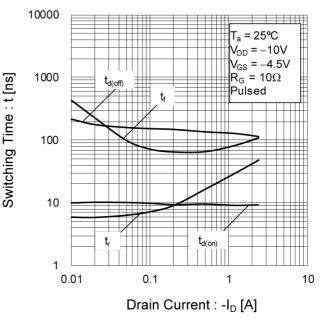


Fig.21 Dynamic Input Characteristics

Gate - Source Voltage : -V_{GS} [V]

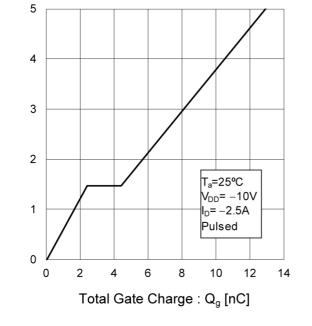
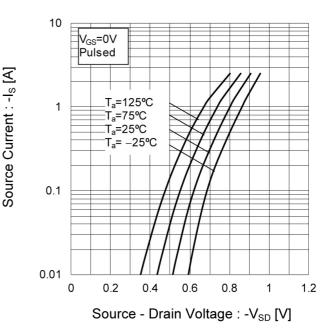


Fig.22 Source Current vs. Source Drain Voltage





•Measurement circuits <It is the same for the Tr1 and Tr2>

Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

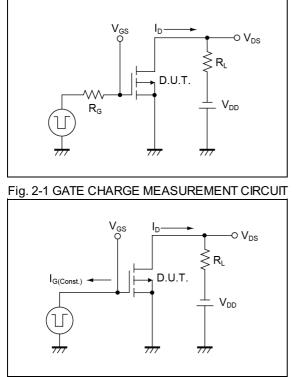


Fig. 1-2 SWITCHING WAVEFORMS

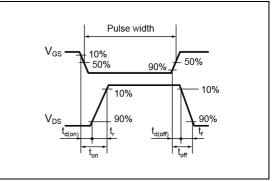
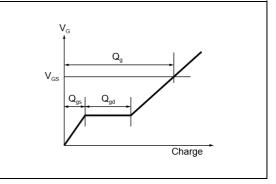


Fig. 2-2 GATE CHARGE WAVEFORM



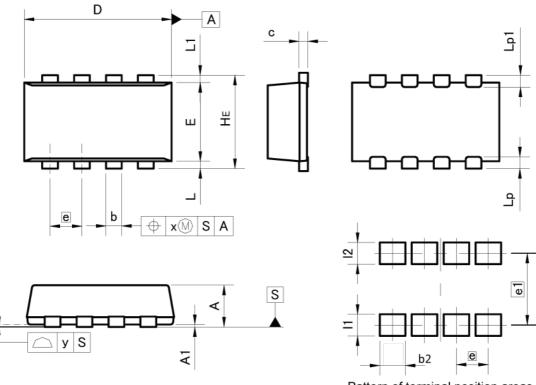
Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions

TSST8



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM MIN MAX MIN A 0.75 0.85 0.030 A1 0.00 0.05 0.000 b 0.22 0.42 0.009 c 0.12 0.22 0.005 D 2.90 3.10 0.114 E 1.50 1.70 0.059 e 0.65 0.026 HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	MAX
A1 0.00 0.05 0.000 b 0.22 0.42 0.009 c 0.12 0.22 0.005 D 2.90 3.10 0.114 E 1.50 1.70 0.059 e 0.65 0.026 HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	
b 0.22 0.42 0.009 c 0.12 0.22 0.005 D 2.90 3.10 0.114 E 1.50 1.70 0.059 e 0.65 0.026 HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	0.033
c 0.12 0.12 0.005 D 2.90 3.10 0.114 E 1.50 1.70 0.059 e 0.65 0.026 HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	0.002
D 2.90 3.10 0.114 E 1.50 1.70 0.059 e 0.65 0.026 HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	0.017
E 1.50 1.70 0.059 e 0.65 0.026 HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	0.009
e 0.65 0.026 HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	0.122
HE 1.80 2.00 0.071 L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	0.067
L 0.05 0.25 0.002 L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	
L1 0.05 0.25 0.002 Lp 0.15 0.34 0.006	0.079
Lp 0.15 0.34 0.006	0.010
	0.010
Lp1 0.15 0.24 0.006	0.013
	0.013
x – 0.10 –	0.004
y – 0.10 –	0.004
MILIMETERS INCHES	3
DIM MIN MAX MIN	MAX
b2 – 0.52 –	0.020
e1 1.46 0.057	
1 – 0.44 –	0.017

Dimension in mm/inches

_

12

0.44

_



0.017

Notice

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CLASSⅢ	CLASSⅢ	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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TT8J21 - Web Page

Distribution Inventory

Part Number	TT8J21
Package	TSST8
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes



单击下面可查看定价,库存,交付和生命周期等信息

>>ROHM Semiconductor(罗姆)