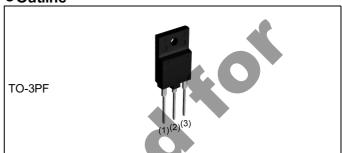
Nch 600V 24A Power MOSFET

| V_{DSS} | 600V |
|----------------------------|--------|
| R _{DS(on)} (Max.) | 0.165Ω |
| I _D | ±24A |
| P_D | 74W |

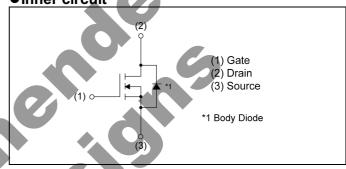
Outline



Features

- 1) Low on-resistance.
- 2) Ultra fast switching speed.
- 3) Parallel use is easy.
- 4) Pb-free lead plating; RoHS compliant





Packaging specifications

| | 3 | |
|------|-----------------|----------|
| | Packing | Tube |
| | Reel size (mm) | - |
| Tymo | Tape width (mm) | - |
| Туре | Quantity (pcs) | 360 |
| | Taping code | C8 |
| | Marking | R6024KNZ |

Application

Switching

● Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

| Parameter | | Symbol | Value | Unit |
|---|-----------------|--------------------|-------------|------|
| Drain - Source voltage | | $V_{\rm DSS}$ | 600 | V |
| Continuous drain current (T _c = 25 | 5°C) | I _D *1 | ±24 | Α |
| Pulsed drain current | | I _{DP} *2 | ±72 | Α |
| Cata Cauraa valtaga | static | V _{GSS} | ±20 | V |
| Gate - Source voltage | AC(f>1Hz) | | ±30 | V |
| Avalanche current, single pulse | | I _{AS} | 4.1 | Α |
| Avalanche energy, single pulse | | E _{AS} *3 | 497 | mJ |
| Power dissipation (T _c = 25°C) | P _D | 74 | W | |
| Junction temperature | T _j | 150 | °C | |
| Operating junction and storage te | mperature range | T _{stg} | -55 to +150 | °C |

●Thermal resistance

| Downwortow | Cymah al | Values | | | 1.1:4 |
|--|----------------------|--------|------|------|-------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R _{thJC} *4 | - | - | 1.7 | °C/W |
| Thermal resistance, junction - ambient | R _{thJA} | - | - | 40 | °C/W |
| Soldering temperature, wavesoldering for 10s | T _{sold} | - | | 265 | °C |

• Electrical characteristics $(T_a = 25^{\circ}C)$

| Parameter Symbol Conditions Min. Typ. Max. Un | IIL |
|--|----------|
| IVIII IJP. IVIEX. | |
| Drain - Source breakdown voltage $V_{(BR)DSS}$ $V_{GS} = 0V$, $I_D = 1mA$ 600 V | ' |
| $V_{DS} = 600V, V_{GS} = 0V$ | |
| Zero gate voltage drain current I_{DSS} $T_j = 25^{\circ}C$ $-$ 100 μ A | 4 |
| $T_j = 125^{\circ}C$ 1000 | |
| Gate - Source leakage current V_{GS} $V_{GS} = \pm 20V$, $V_{DS} = 0V$ - ± 100 nA | 4 |
| Gate threshold voltage $V_{GS(th)}$ $V_{DS} = 10V$, $I_D = 1mA$ 3 - 5 V | , |
| V _{GS} = 10V, I _D = 11.3A | |
| Static drain - source on - state resistance $R_{DS(on)}^{*5}$ $T_j = 25^{\circ}C$ - 0.150 0.165 Ω | <u>)</u> |
| $T_{\rm j} = 125^{\circ}{\rm C}$ - 0.32 - | |
| Gate resistance R_G f = 1MHz, open drain - 1.9 - Ω | <u>)</u> |

● Electrical characteristics (T_a = 25°C)

| Darramatar | Cymah al | Conditions | Values | | | Linit | |
|--------------------------------|--------------------------|---|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Forward Transfer Admittance | Y _{fs} *5 | V _{DS} = 10V, I _D = 12A | 6.5 | 13.0 | - | S | |
| Input capacitance | C _{iss} | V _{GS} = 0V | - | 2000 | | | |
| Output capacitance | C _{oss} | V _{DS} = 25V | - | 1500 | | pF | |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | - | 60 | - | | |
| Turn - on delay time | t _{d(on)} *5 | $V_{DD} \simeq 300V$, $V_{GS} = 10V$ | -(| 30 | - | | |
| Rise time | t _r *5 | I _D = 12A | 16 | 50 | - | no | |
| Turn - off delay time | t _{d(off)} *5 | R _L ≃ 27.4Ω | - | 60 | - | ns | |
| Fall time | t _f *5 | $R_G = 10\Omega$ | - | 12 | - | | |

● Gate charge characteristics (T_a = 25°C)

| Daramatar | Symbol Conditions | | Values | | Unit |
|----------------------|--|------|--------|------|-------|
| Parameter | Symbol Conditions | Min. | Тур. | Max. | Offic |
| Total gate charge | Q_g^{*5} $V_{DD} \approx 300V$ | - | 45 | - | |
| Gate - Source charge | Q_{gs}^{*5} $I_{D} = 24A$ | - | 13 | - | nC |
| Gate - Drain charge | Q_{gd}^{*5} $V_{GS} = 10V$ | - | 20 | - | |
| Gate plateau voltage | $V_{(plateau)}$ $V_{DD} \simeq 300V$, $I_D = 24A$ | - | 6.8 | - | V |

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 70mH, V_{DD}=50V, R_G=25 Ω , STARTING T_j=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Pulsed

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Parameter | Cymbol | Conditions | Values | | | Unit |
|-------------------------------|---------------------|--|--------|------|------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offit |
| Continuous forward current | I _S *1 | T _C = 25°C | - | - | 24 | A |
| Pulse forward current | I _{SP} *2 | | - | - | 72 | A |
| Forward voltage | V _{SD} *5 | V _{GS} = 0V, I _S = 24A | - | - \ | 1.5 | V |
| Reverse recovery time | t _{rr} *5 | | - | 510 | - | ns |
| Reverse recovery charge | Q _{rr} *5 | I _S = 24A di/dt = 100A/μs | | 9.0 | - | μC |
| Peak reverse recovery current | I _{rrm} *5 | | V | 32.5 | - | Α |

● Typical transient thermal characteristics

| Symbol | Value | Unit |
|------------------|-------|------|
| R _{th1} | 0.108 | |
| R _{th2} | 0.549 | K/W |
| R _{th3} | 1.22 | |

| , | Symbol | Value | Unit |
|---|------------------|---------|------|
| | C _{th1} | 0.00523 | |
| | C _{th2} | 0.045 | Ws/K |
| 7 | C _{th3} | 1.07 | |

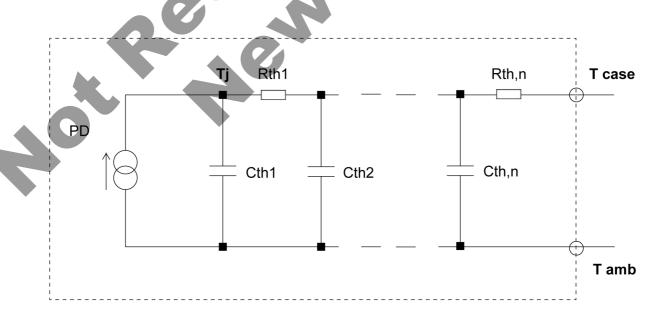


Fig.1 Power Dissipation Derating Curve

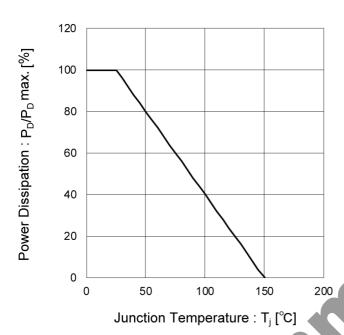
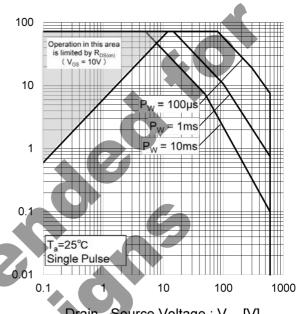
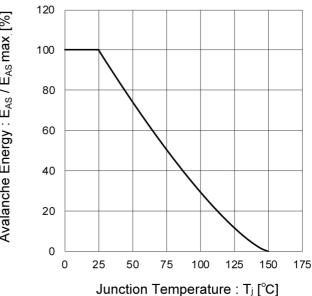


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage: V_{DS} [V]

Fig.3 Avalanche Energy Derating Curve vs. Junction Temperature



Drain Current : I_D [A]

Fig.4 Typical Output Characteristics(I)

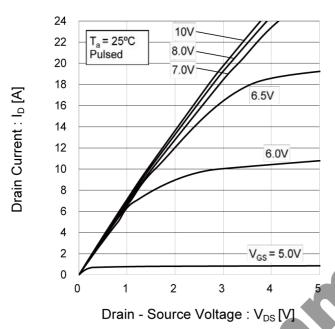
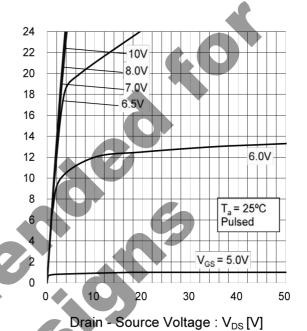


Fig.5 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.6 Breakdown Voltage vs.

Junction Temperature

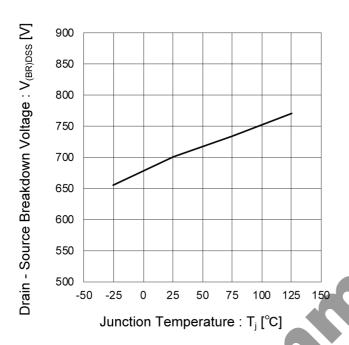


Fig.7 Typical Transfer Characteristics

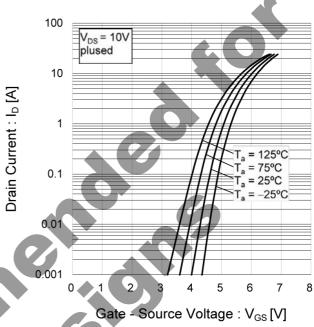


Fig.8 Gate Threshold Voltage vs.
Junction Temperature

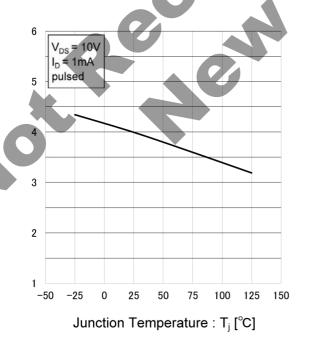
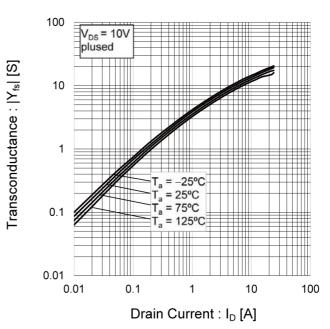


Fig.9 Forward Transfer Admittance vs.

Drain Current



Gate Threshold Voltage: V_{GS(th)} [V]

Fig.10 Static Drain - Source On - State Resistance vs. Gate Source Voltage

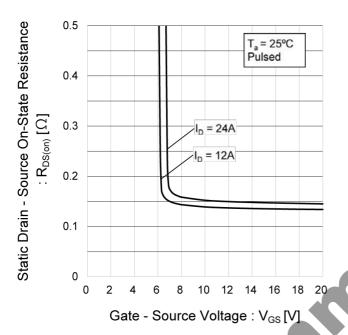


Fig.11 Static Drain - Source On - State Resistance vs. Junction Temperature

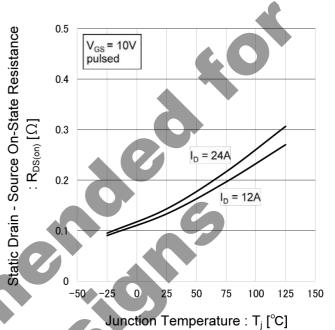


Fig.12 Static Drain - Source On - State Resistance vs. Drain Current(I)

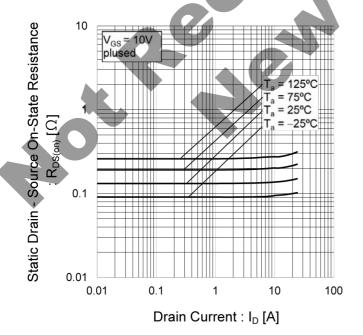


Fig.13 Typical Capacitance vs. Drain - Source Voltage

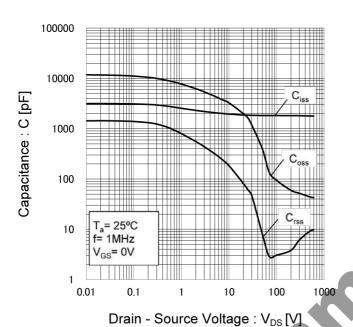


Fig.14 Switching Characteristics

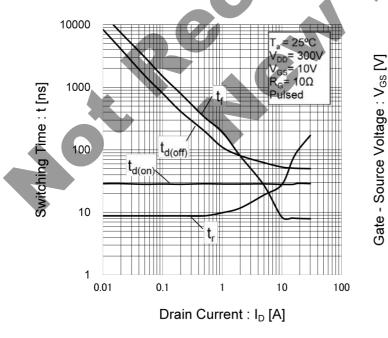


Fig.15 Dynamic Input Characteristics

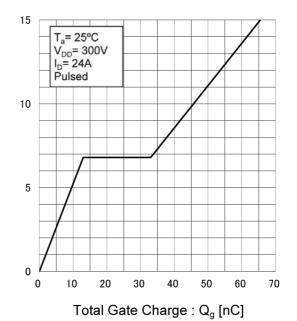


Fig.16 Inverse Diode Forward Current vs. Source - Drain Voltage

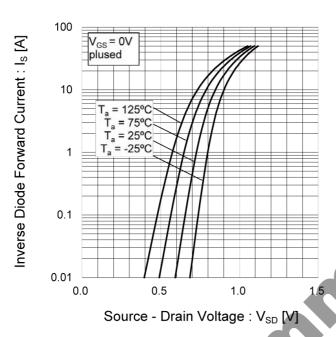
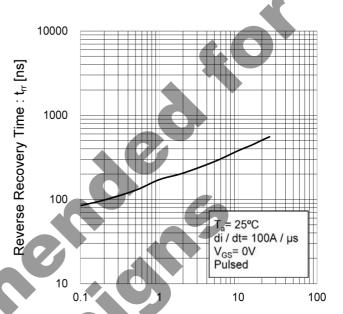


Fig.17 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

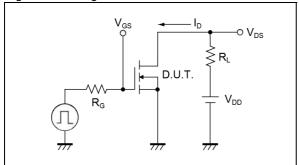


Fig.2-1 Gate Charge Measurement Circuit

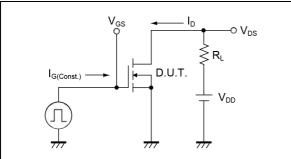


Fig.3-1 Avalanche Measurement Circuit

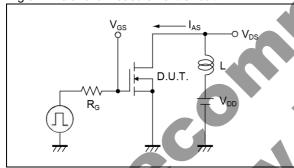


Fig.4-1 dv/dt Measurement Circuit

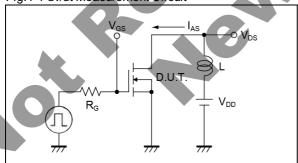


Fig.5-1 di/dt Measurement Circuit

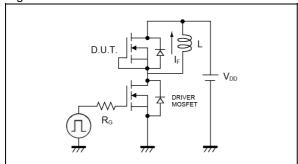


Fig.1-2 Switching Waveforms

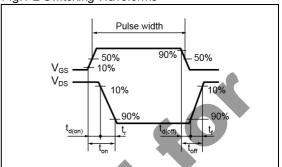


Fig.2-2 Gate Charge Waveform

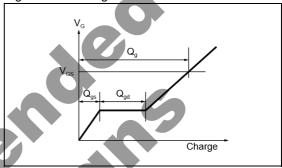


Fig.3-2 Avalanche Waveform

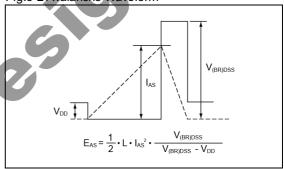


Fig.4-2 dv/dt Waveform

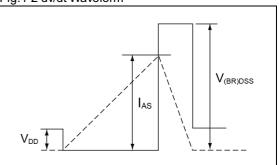
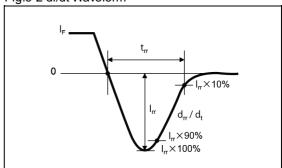
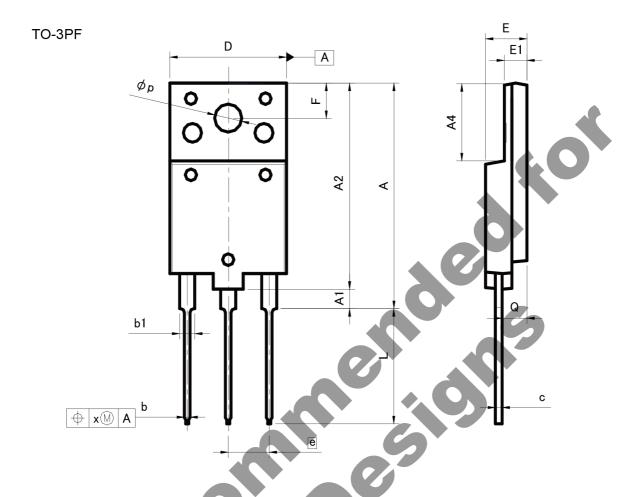


Fig.5-2 di/dt Waveform



Dimensions



| DIM | MILIM | ETERS | INC | HES |
|-----|-------|-------|------------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| A | 26.30 | 26.70 | 1.035 | 1.051 |
| A1 | 2.30 | 2.70 | 0.091 | 0.106 |
| A2 | 26.30 | 26.70 | 1.035 | 1.051 |
| A4 | 9.80 | 10.20 | 0.386 | 0.402 |
| b | 0.65 | 0.95 | 0.026 | 0.037 |
| b1 | 1.80 | 2.20 | 0.071 | 0.087 |
| С | 0.80 | 1.10 | 0.031 | 0.043 |
| D | 15.30 | 15.70 | 0.602 | 0.618 |
| E | 5.30 | 5.70 | 0.209 | 0.224 |
| е | 5.4 | 45 | 0.215 | 2:—2 |
| E1 | 2.80 | 3.20 | 0.110 | 0.126 |
| F | 4.30 | 4.70 | 0.169 | 0.185 |
| L | 14.60 | 15.00 | 0.575 | 0.591 |
| р | 3.40 | 3.80 | 0.134 | 0.150 |
| Q | 3.10 | 3.50 | 0.122 | 0.138 |
| х | - | 0.50 | s - - | 0.020 |

Dimension in mm/inches



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|---------|-----------|------------|----------|
| CLASSⅢ | CL ACCIII | CLASS II b | CLASSIII |
| CLASSIV | CLASSⅢ | CLASSⅢ | CLASSIII |

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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