

RQ3E180BN

Nch 30V 39A Middle Power MOSFET

Datasheet

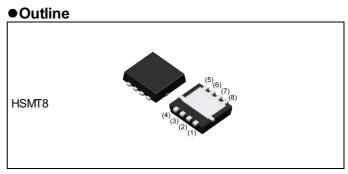
V _{DSS}	30V
R _{DS(on)} (Max.)	3.9mΩ
I _D	±39A
P _D	20W

Features

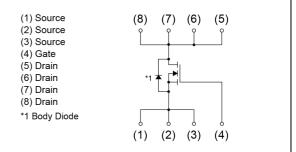
- 1) Low on resistance.
- 2) High Power Package (HSMT8).
- 3) Pb-free lead plating ; RoHS compliant.
- 4) Halogen Free.

Application

Switching



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	ТВ
	Marking	E180BN

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

. .	4				
Parameter	Symbol	Value	Unit		
Drain - Source voltage		V _{DSS}	30	V	
Continuous drain surrent	$T_c = 25^{\circ}C$	۱ _D *1	±39	А	
Continuous drain current	T _a = 25°C	Ι _D	±18	А	
Pulsed drain current	I_{DP}^{*2}	±72	А		
Gate - Source voltage	V _{GSS}	±20	V		
Avalanche current, single pulse		ا _{AS} *3	18	А	
Avalanche energy, single pulse		E_{AS}^{*3}	23.3	mJ	
Power dissipation		P _D ^{*1}	20	W	
		P _D ^{*4}	2.0	W	
Junction temperature		Tj	150	°C	
Operating junction and storage te	T _{stg}	-55 to +150	C°		

Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}^{*1}	-	-	6.2	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*4}	-	-	62.5	°C/W

• Electrical characteristics (T_a = 25°C)

Devenuetor	Queek el	Canditiana		Values		1.1
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	21	-	mV/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V	-	-	1	μA
Gate - Source leakage current	I _{GSS}	V _{GS} = ±16V, V _{DS} = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V _{DS} = 10V, I _D = 1mA	1.0	-	2.5	V
$\begin{array}{c} \mbox{Gate threshold voltage} \\ \mbox{temperature coefficient} \end{array} & \begin{tabular}{c} \Delta V_{GS(th)} \\ \hline \Delta T_{j} \end{array}$		I _D = 1mA referenced to 25°C	-	-3	-	mV/°C
Static drain - source	D *5	V _{GS} = 10V, I _D = 18A	-	2.8	3.9	
on - state resistance	${\sf R}_{\sf DS(on)}^{*5}$	V _{GS} = 4.5V, I _D = 18A	-	3.7	5.2	mΩ
Gate resistance	R _G f=1MHz, open drain		-	3.1	-	Ω
Forward Transfer Admittance	$ Y_{fol} ^{\circ} V_{Doc} = 5V _{D} = 184$		20	-	-	S

*1Tc=25°C, Limited only by maximum temperature allowed.

*2 Pw \leq 10µs, Duty cycle \leq 1%

*3 L \simeq 0.1mH, V_{DD} = 15V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2

- *4 Mounted on a Cu board (40×40×0.8mm)
- *5 Pulsed



• Electrical characteristics ($T_a = 25^{\circ}C$)

Deremeter	Symbol Conditions		Values			Unit	
Parameter Symbol		Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	3500	-		
Output capacitance	C _{oss}	V _{DS} = 15V	-	410	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	330	-		
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DD} \simeq 15V, V_{GS} = 10V$	-	14	-		
Rise time	t _r *5	I _D = 9A	-	63	-		
Turn - off delay time	t _{d(off)} *5	R _L ≃ 1.67Ω	-	138	-	ns	
Fall time	t _f *5	R _G = 10Ω	-	79	-		

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Symbol Conditions		0.00	Values			Unit
Parameter	Symbol	Symbol Conditions		Min.	Тур.	Max.	Unit
Total gata charge	O *5		V _{GS} = 10V	-	72	-	
Total gate charge	Q _g *5	Q _g ° V _{DD} ≃ 15V		-	37	-	~
Gate - Source charge	Q _{gs} *5	I _D = 18A	V _{GS} = 4.5V	-	10	-	nC
Gate - Drain charge	Q _{gd} *5			-	16	-	

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ _s	T - 25°0	-	-	1.67	А
Pulse forward current	I_{SP}^{*2}	T _a = 25°C	-	-	72	А
Forward voltage	V_{SD}^{*5}	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V



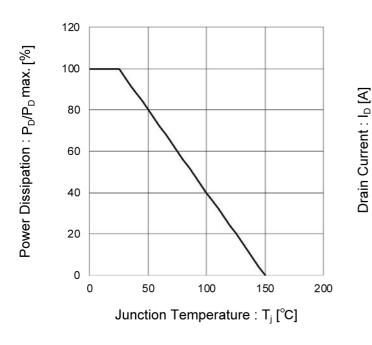


Fig.1 Power Dissipation Derating Curve

Operation in this area is limited by $R_{DS}(on)(V_{GS} = 10V)$ 100 P_W = 100µs 10 P_w = 1ms 1 P_W = 10ms DC Operation 0.1 T_a=25°C Single Pulse Mounted on a Cu board (40mm × 40mm × 0.8mm) 0.01 10 100 0.1 1

Fig.2 Maximum Safe Operating Area

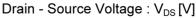
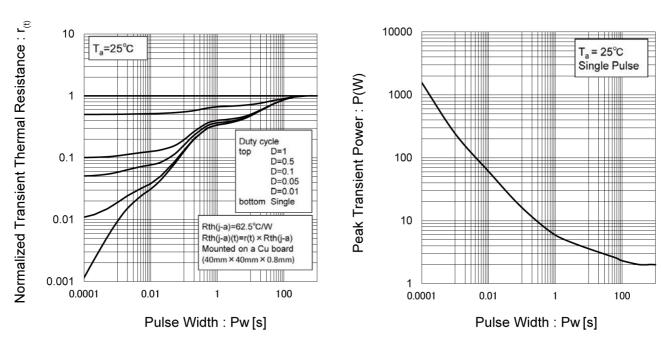


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power dissipation





Electrical characteristic curves

Drain-Source Breakdown Voltage : V_{(BR)DSS} [V]

50

48

46

44

42 40

38

36

34

32

30

-50

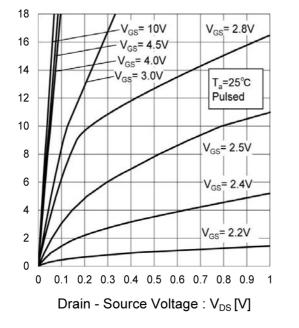


Fig.5 Typical Output Characteristics(I)

Fig.7 Breakdown Voltage vs. Junction Temperature

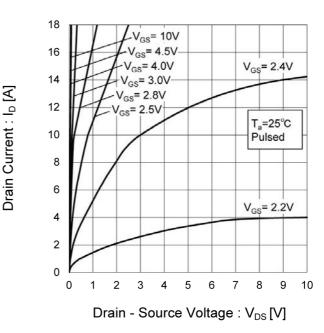
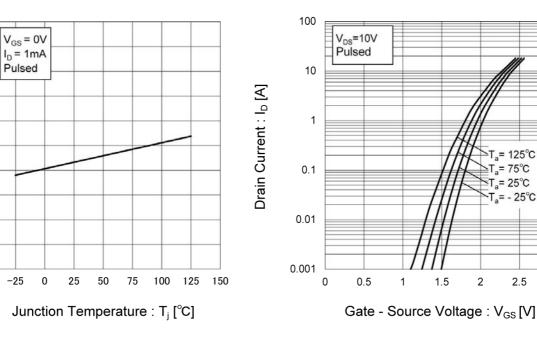


Fig.6 Typical Output Characteristics(II)

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Fig.8 Typical Transfer Characteristics





2.5

3

•Electrical characteristic curves

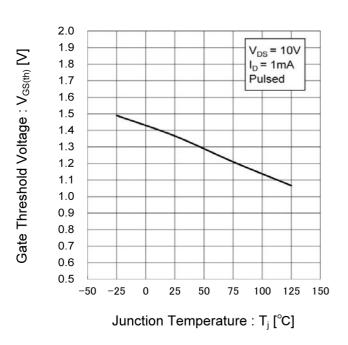
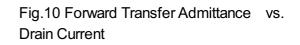


Fig.9 Gate Threshold Voltage vs. Junction Temperature



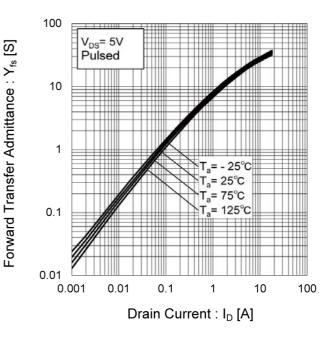
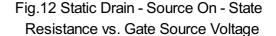
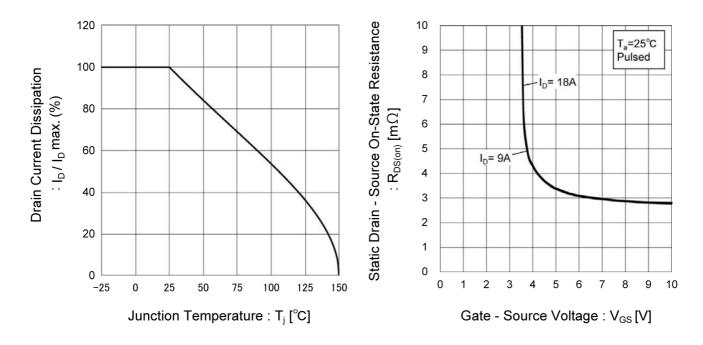


Fig.11 Drain Current Derating Curve







•Electrical characteristic curves

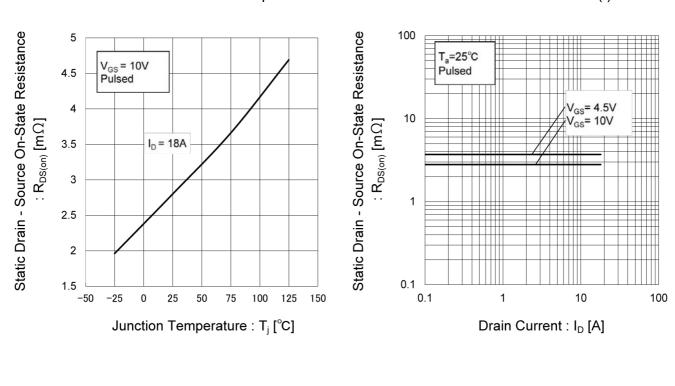
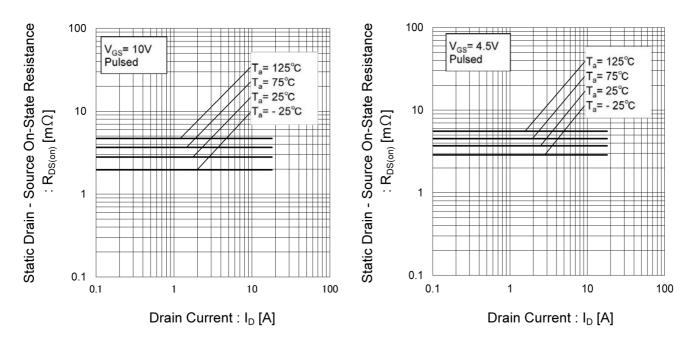


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II) Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)



7/10



Electrical characteristic curves

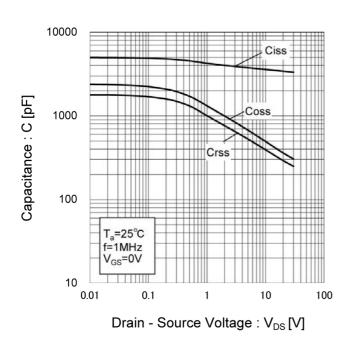


Fig.17 Typical Capacitance vs. Drain -Source Voltage

Fig.18 Switching Characteristics

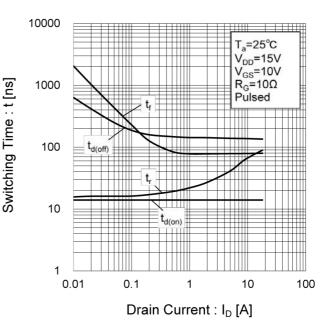


Fig.19 Dynamic Input Characteristics



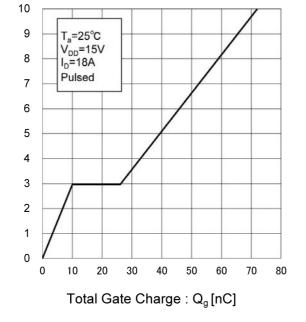
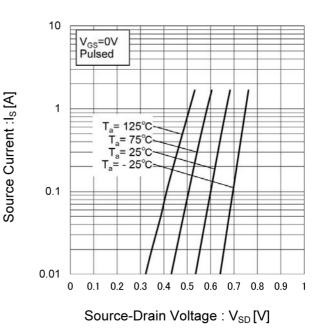


Fig.20 Source Current vs. Source Drain Voltage





Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

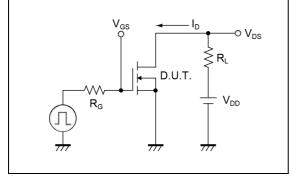


Fig.2-1 Gate Charge Measurement Circuit

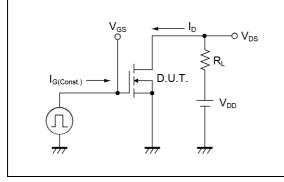


Fig.3-1 Avalanche Measurement Circuit

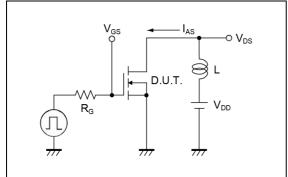


Fig.1-2 Switching Waveforms

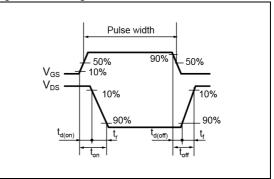


Fig.2-2 Gate Charge Waveform

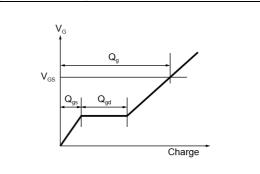
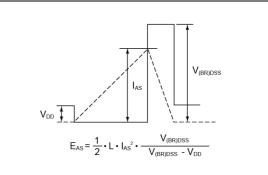


Fig.3-2 Avalanche Waveform

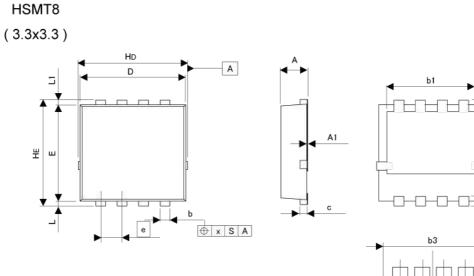


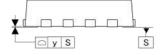
Notice

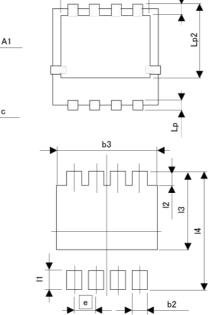
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions







5

Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIME	TERS	INC	HES
	MIN	MAX	MIN	MAX
A	0.70	0.90	0.028	0.035
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
b1	2.50	2.70	0.098	0.106
с	0.10	0.30	0.004	0.012
D	3.10	3.30	0.122	0.130
E	2.90	3.10	0.114	0.122
е	0.	65	0.0	26
HD	3.20	3.40	0.126	0.134
HE	3.20	3.40	0.126	0.134
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.20	0.40	0.008	0.016
Lp1	0.25	0.45	0.010	0.018
Lp2	2.20	2.40	0.087	0.094
x	-	0.10	-	0.004
у		0.10		0.004
	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	8 <u>2</u> 5	0.47		0.019
b3	370	2.70		0.106
11	(#)	0.50	1 H	0.020
12		0.55		0.022
13	2.75	2.40	×	0.094
19137		20.000		1

Dimension in mm/inches

13 14



0.134

3.40

Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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RQ3E180BN - Web Page

Part Number	RQ3E180BN
Package	HSMT8
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes



单击下面可查看定价,库存,交付和生命周期等信息

>>ROHM Semiconductor(罗姆)