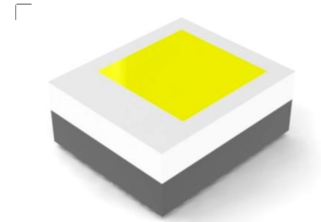
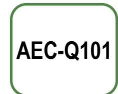
Applicable for automotive exterior light

WICOP-B 1x1 White

SWW0Y1XA





Product Brief

Description

- The Compact LED series is designed for high current operation and high flux output applications.
- It incorporates state of the art SMD design and low thermal resistant material.
- The Compact LED is ideal light sources for automotive applications and mobile flash, general lighting.

Features and Benefits

- Super high Flux output and high Luminance
- Designed for high current operation
- SMT solderable
- Lead Free product
- Compact module design available.

Key Applications

- Automotive Lighting

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Performance Characteristics

Table 1. Characteristics, IF = 350mA, Tj = 25ºC

Parameter	Symbol	Min	Typ	Max	Unit
Forward Voltage [1],[2],[4]	V_F	2.75	3.13	3.5	V
Luminous Flux [1],[3],[4]	Φ_V		125		lm
Luminous Flux [1],[3],[4] @1A (Reference only)	Φ_V		290	-	lm
Chromaticity Coordinates [1],[4],[5]	*CIEx*		0.33		
	CIEy		0.34		
Viewing Angle	$2\theta_{1/2}$		120		deg.
Real Thermal resistance	*Rth JS*		4.5	6.5	K / W
Electrical Thermal resistance	*Rth JS*		3.5	5.5	K / W
Temperature coefficient of V_F -40℃ ≤ T ≤ 135 ℃	TC_v		-2.5		mV/K
Temperature coefficient of color coordinates -40℃ ≤ T ≤ 135 ℃	TC_x		-0.20		10^{-3}/K
	TC_y		-0.40		10^{-3}/K

Notes :

[1] Tolerance : VF :±0.05V, ΦV :±8%, CIEx, CIEy:±0.005
[2] LEDs are to be classified in forward voltage groups if requested by Customer. Minimum and maximum values include all tolerances.
[3] LEDs are to be classified in luminous flux groups. The ratio of the group limits (ΦV,max/ΦV,min) must not
[4] Parameter has to be checked by 100% in the production process.
[5] LEDs are to be classified in colour bins if requested by Customer.
Colour bins and tolerances are program specific and will be agreed by sample evaluation between SSC and the customer. The colour coordinates of the application have to fulfil ECE /SAE legislation, whereby the binning range must not exceed 0.02 typically. Minimum and maximum values include all tolerances.

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Performance Characteristics

Table 2. Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Forward Current (T_a=25℃)	I_F	50 ~ 1,000	mA
Operating Temperature	T_{opr}	-40 ~ +125	℃
Storage Temperature	T_{stg}	-40 ~ +125	℃
Junction Temperature	T_j	150	℃
Soldering Temperature	T_{sld}	Reflow Soldering : 260 ℃ for 10sec. Hand Soldering : 315 ℃ for 4sec.	
ESD (HBM) (R=1.5kΩ, C= 100pF)		Class 3B (JESD22-A114-E)	-

Notes :

[1] LEDs are to be classified in forward voltage groups if requested by Customer. Minimum and maximum values include all tolerances.

[2] LEDs are to be classified in luminous flux groups. The ratio of the group limits (ΦV,max/ΦV,min) must not exceed 1,6 including measurement tolerances. Minimum and maximum values include all tolerances.

[3] LEDs are to be classified in color bins if requested by Customer.
Color bins and tolerances are program specific and will be agreed by sample evaluation between SSC and the customer. The color coordinates of the application have to fulfill ECE /SAE legislation, whereby the binning range must not exceed 0.02 typically. Minimum and maximum values include all tolerances.

[4] Subject to Human Body Model / JESD22-A114.

[5] Parameter has to be checked by 100% in the production process.

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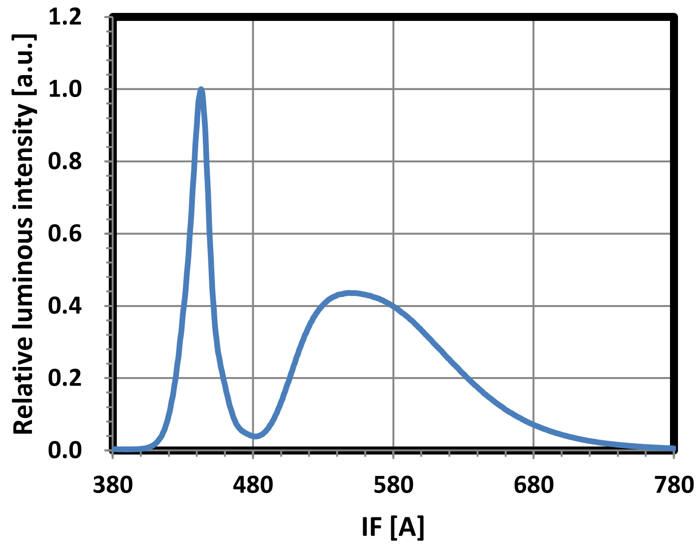
Characteristics Graph

Fig 1. Relative Intensity vs. Wavelength , I_F = 350mA, Tj = 25℃



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Characteristics Graph

Fig 2. Relative Intensity vs. Angle, Tj = 25℃

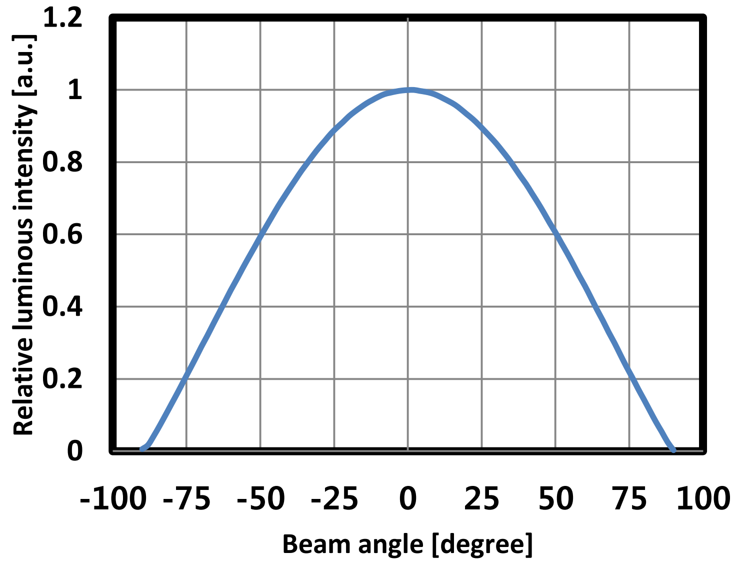
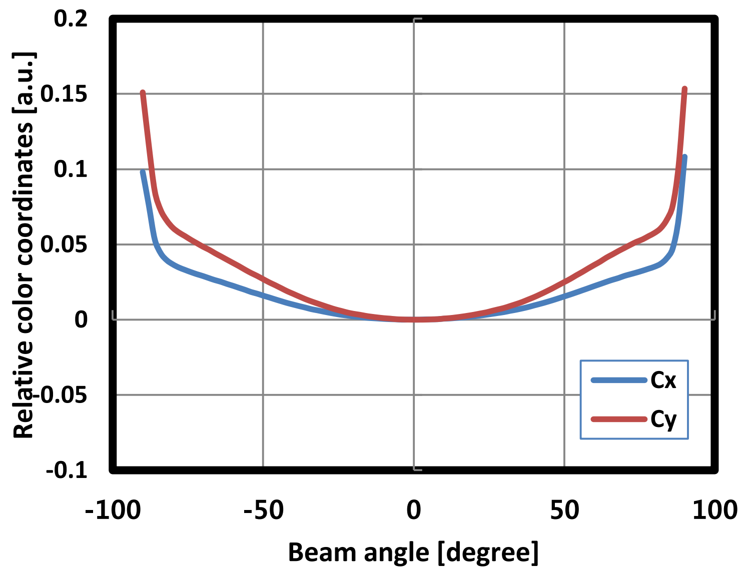


Fig 3. Distribution of color coordinates vs. Radiation Angle , Tj = 25℃



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Characteristics Graph

Fig 4. Forward Current vs. Forward Voltage, Tj=25℃

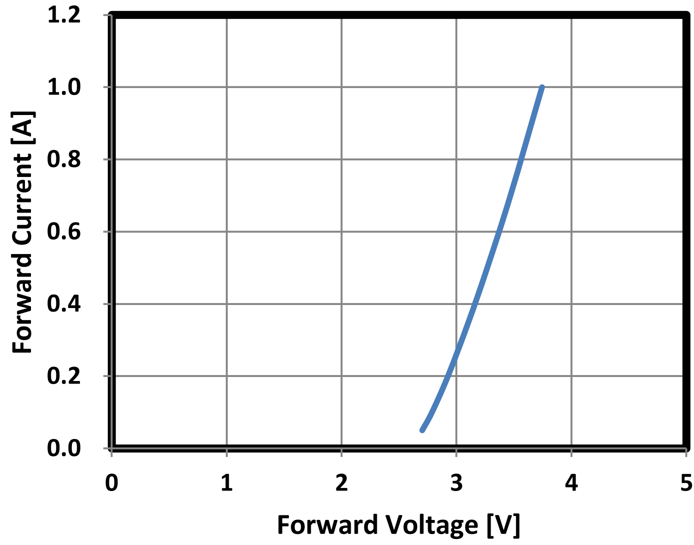
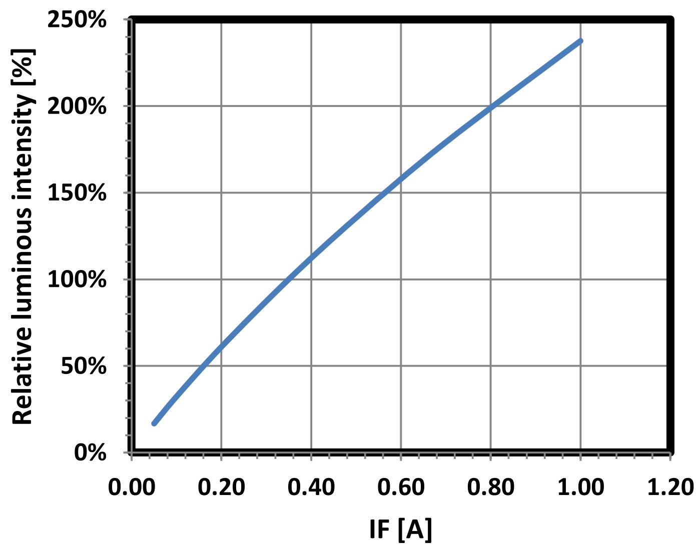


Fig 5. Forward Current vs. Relative Luminous Flux, Tj=25℃



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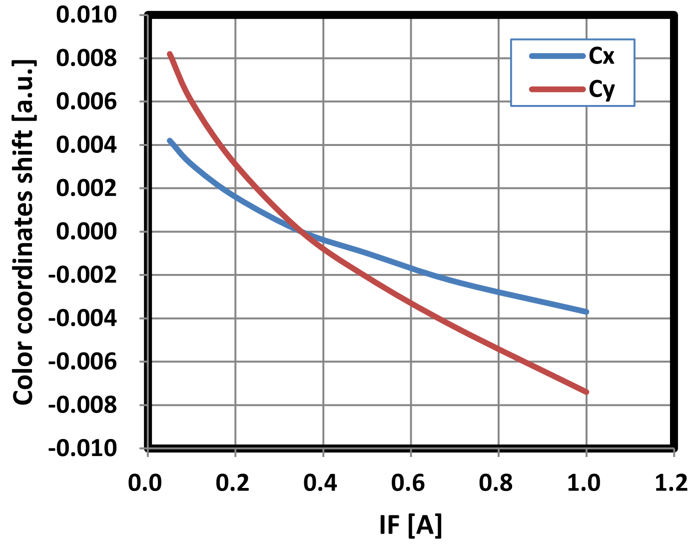
Characteristics Graph

Fig 6. Forward Current vs. CIE X, Y Shift , Tj=25℃



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Characteristics Graph

Fig 7. Junction Temperature vs. Relative Light Output, I_F=350mA

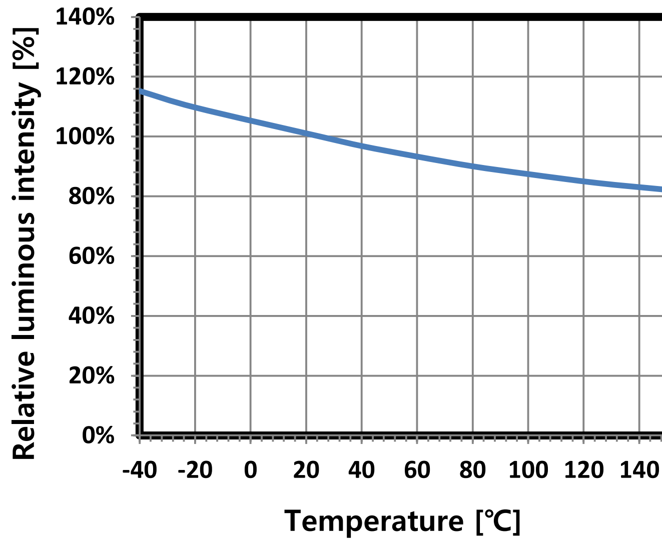
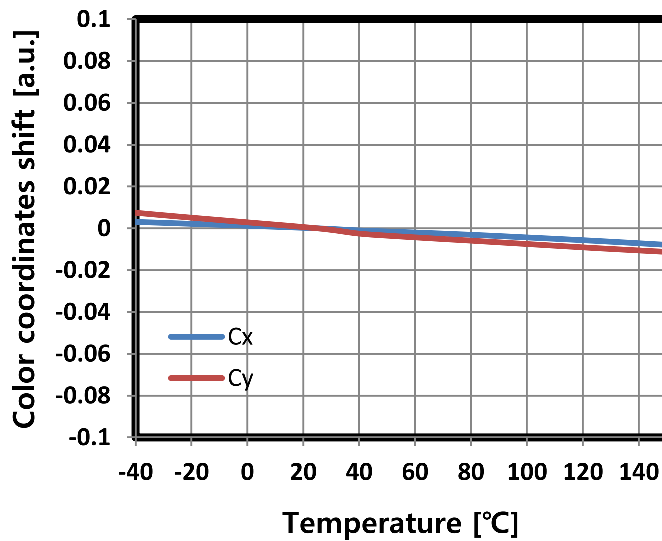


Fig 8. Junction Temperature vs. CIE X, Y Shift , Tj=25℃



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Characteristics Graph

Fig 9. Ambient temperature vs. Forward Voltage, I_F=350mA

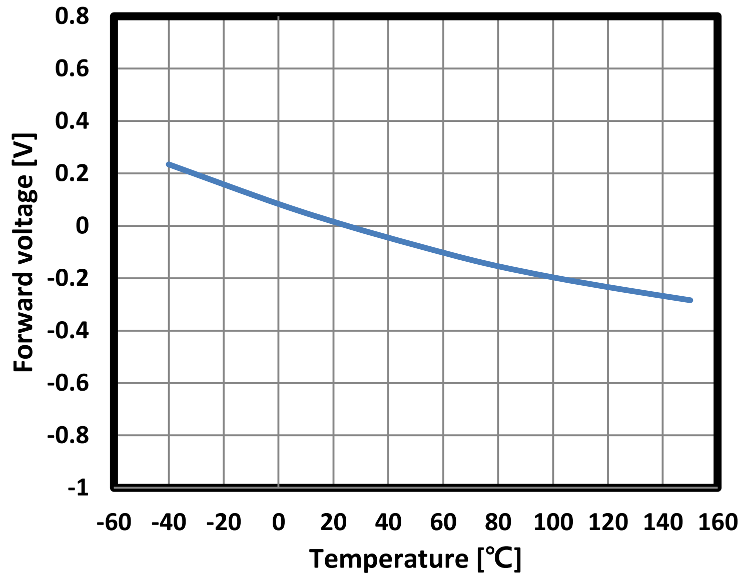
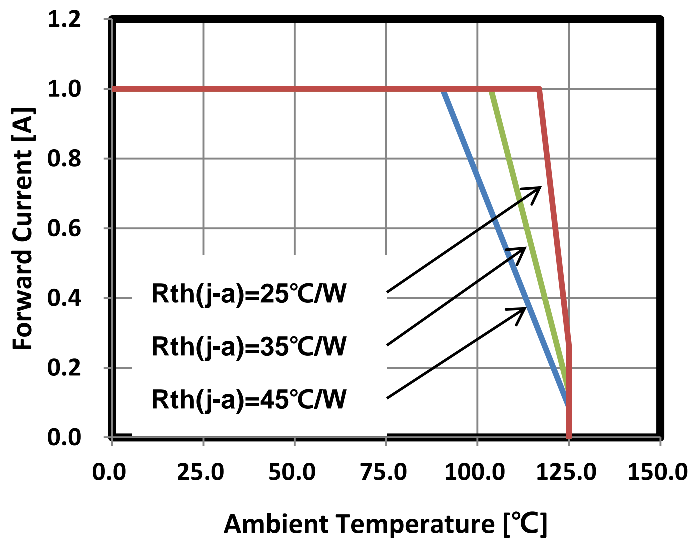


Fig 10. Maximum Forward Current vs. Ambient Temperature, Tj(max.) = 150℃



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Color Bin Structure

Table 3. Bin Code description

Part Number	Luminous Flux (lm) @ I_F = 0.35A			Color Chromaticity Coordinate @ I_F = 0.35A	Forward Voltage (V_F) @ I_F = 0.35A		
	Bin Code	Min.	Max.		Bin Code	Min.	Max.
SWW0Y1XA	U2	100	109	Refer to page.12	G	2.75	3.00
	U3	109	118.5				
	V1	118.5	130		H	3.00	3.25
	V2	130	140		I	3.25	3.50
	V3	140	150				

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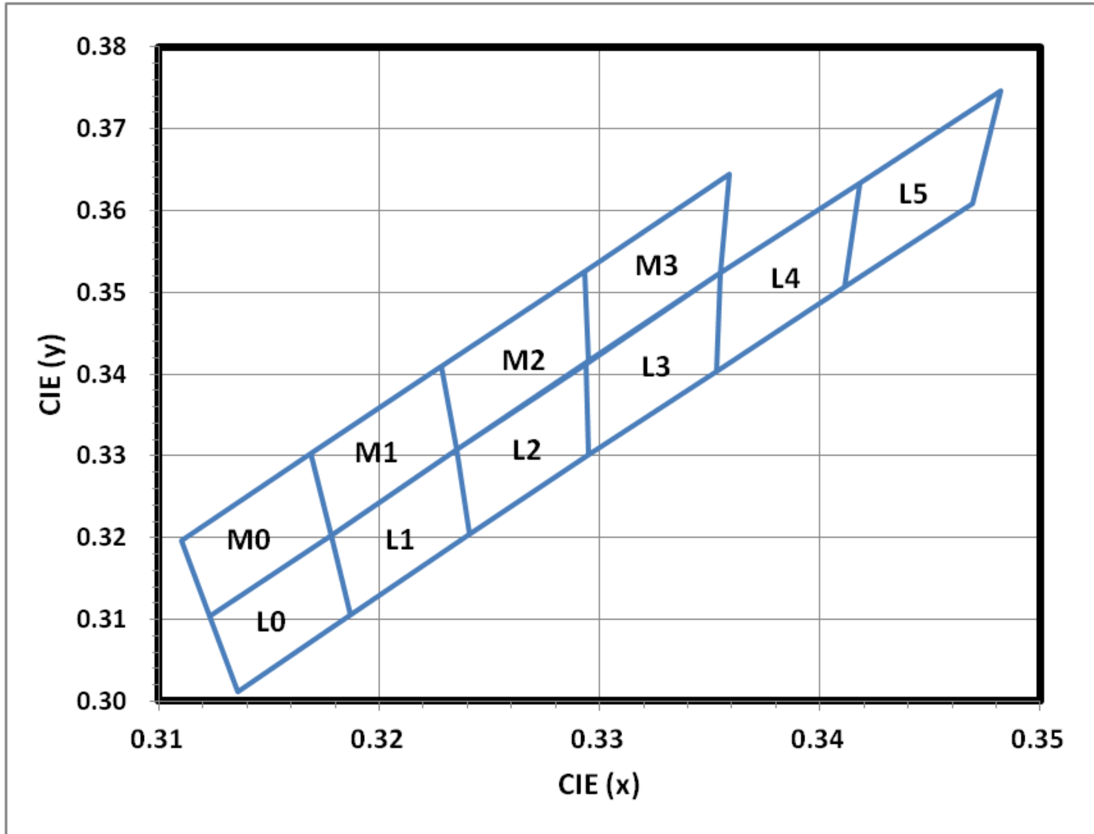
Color Bin Structure

CIE Chromaticity Diagram, Tj=25℃, I_F=350mA



L0		L1		L2		L3		L4	
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y
0.3123	0.3104	0.3178	0.3203	0.3235	0.3307	0.3294	0.3414	0.3355	0.3524
0.3136	0.3012	0.3187	0.3106	0.3241	0.3204	0.3295	0.3302	0.3353	0.3404
0.3187	0.3106	0.3241	0.3204	0.3295	0.3302	0.3353	0.3404	0.3411	0.3506
0.3178	0.3203	0.3235	0.3307	0.3294	0.3414	0.3355	0.3524	0.3418	0.3634
M0		**M1**		**M2**		**M3**		**L5**	
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y
0.3110	0.3196	0.3169	0.3303	0.3228	0.3410	0.3293	0.3526	0.3418	0.3634
0.3123	0.3104	0.3178	0.3203	0.3235	0.3307	0.3294	0.3414	0.3411	0.3506
0.3178	0.3203	0.3235	0.3307	0.3294	0.3414	0.3355	0.3524	0.3469	0.3608
0.3169	0.3303	0.3228	0.3410	0.3293	0.3526	0.3359	0.3644	0.3482	0.3746

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Reliability Test

Test Item	Standard Test Method	Test Condition	Duration / Cycle	Number Of Test
External Visual	JESD22 B-101	Visual inspection	-	77
D.P.A	AEC-Q101-004	Random Sample H3TRB,HAST,TC	-	5
Vibration	JESD22 B-103	0.06 inch displacement, 20 to 100 Hz, 50 g 100 Hz to 2kHz,	4 times	30
ESD	JESD22 A-114	Human-body mode, R=1.5㏀, C = 100pF	3 times Negative/ Positive	30
Physical Dimension	JESD22 B-100	Verify physical dimensions against device mechanical drawing	3 times	30
Mechanical Shock	JESD22 B-104	1500 g's for 0.5 ms, 5 blows, 3 orientations	3 times	30
Parametric Verification	JESD22 A-108	25℃, 1000 hours @350mA	1000hrs	77
Temperature cycling	JESD22 A-104	Tc= -40°~100°C, 30 min. dwell, 5 min transfer, 1000 cycles	1000hrs	77
Power Temperature Cycle	JESD22 A-105	Ta=-40℃~85℃, If =1A, 20 min dwell / 20 min transition (1 hour cycle), 2 min ON / 2 min OFF	1000hrs	77
High Humidity High Temp. Operating Life	JESD22 A-101	85℃/85% RH, @ 350mA	1000hrs	77
High Temperature Operating Life	JESD22 A-108C	Ta= 85°C, If =1A	1000hrs	77
Low Temperature Operating Life	JESD22 A-108C	Ta= -40°C, If = 1A	1000hrs	77
Low Temperature Storage Life	JESD22 A-119	Ta=-40°C, non-operating	1000hrs	77
High Temperature Storage Life	JESD22 A-103B	Ta=125°C, non-operating	1000hrs	77
Thermal Shock	JESD22 A-104	-40°C ~ 100°C, 20 min. dwell, <10 second transfer, 1000 cycles	1000hrs	77

Criteria for Judging the Damage

Item	Symbol	Condition	Criteria for Judgment	
			MIN	MAX
Forward Voltage	V_F	I_F =350mA	-	Initial × 1.2
Luminous Intensity	I_V	I_F =350mA	Initial × 0.8	-

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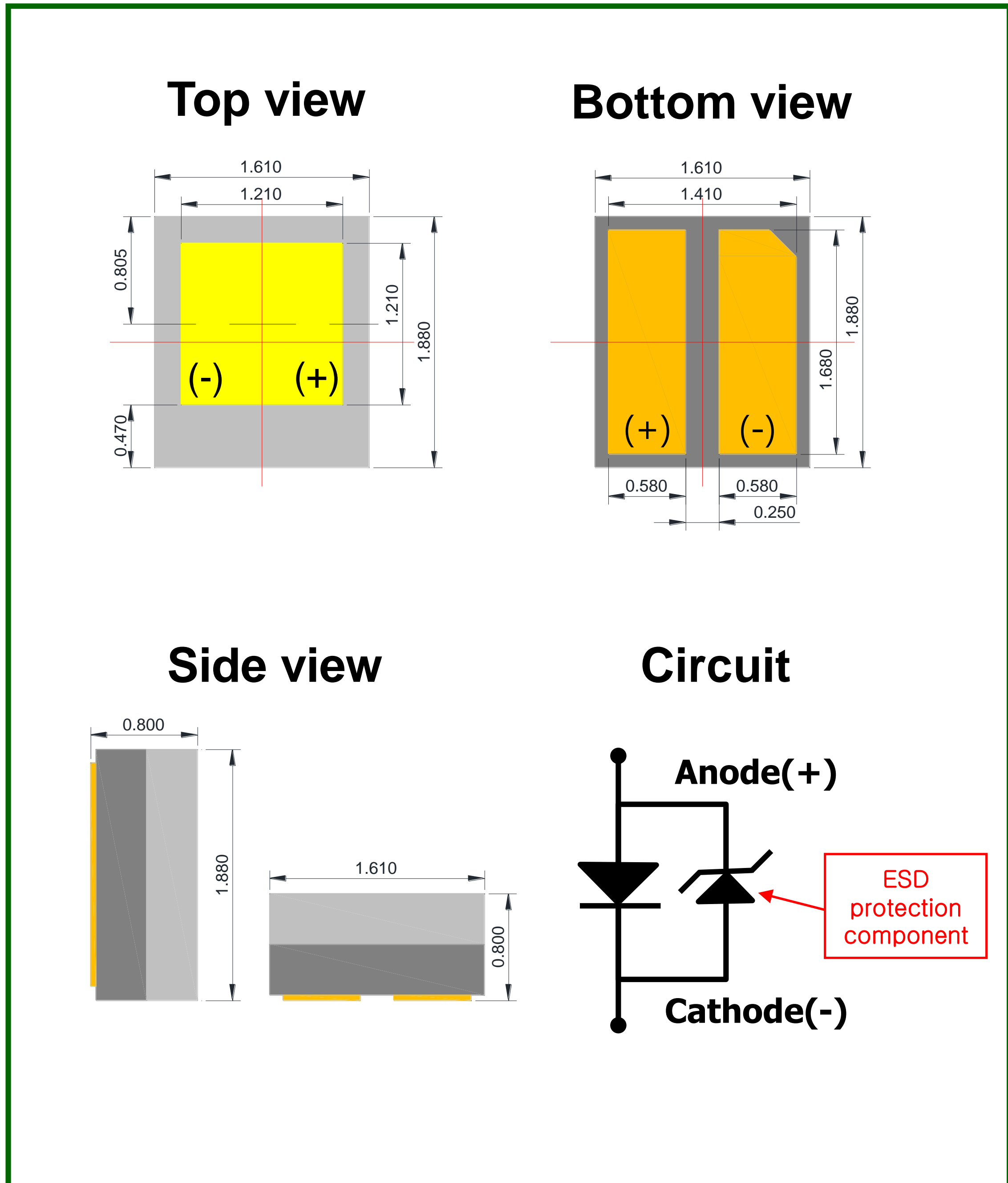
Mechanical Dimensions



Notes :

1. All dimensions are in millimeters.
2. Scale : none
3. Undefined tolerance is ±0.1mm

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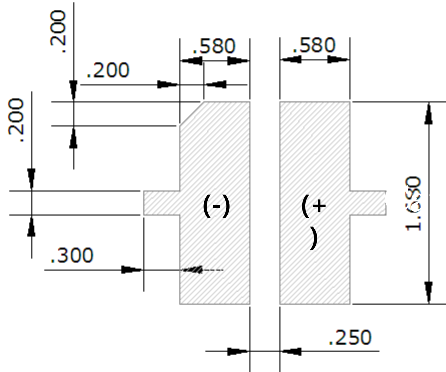
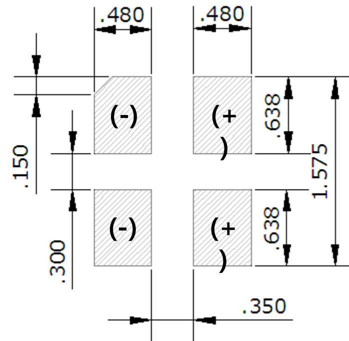
Recommended Solder Pad



<Recommended solder pad>



<Recommended stencil pattern>

It recommended that metal mask is designed to be under 80% of dimension of solder pad.

Notes :

1. All dimensions are in millimeters.
2. Scale : none
3. This drawing without tolerances are for reference only
4. Undefined tolerance is $\pm$0.1mm

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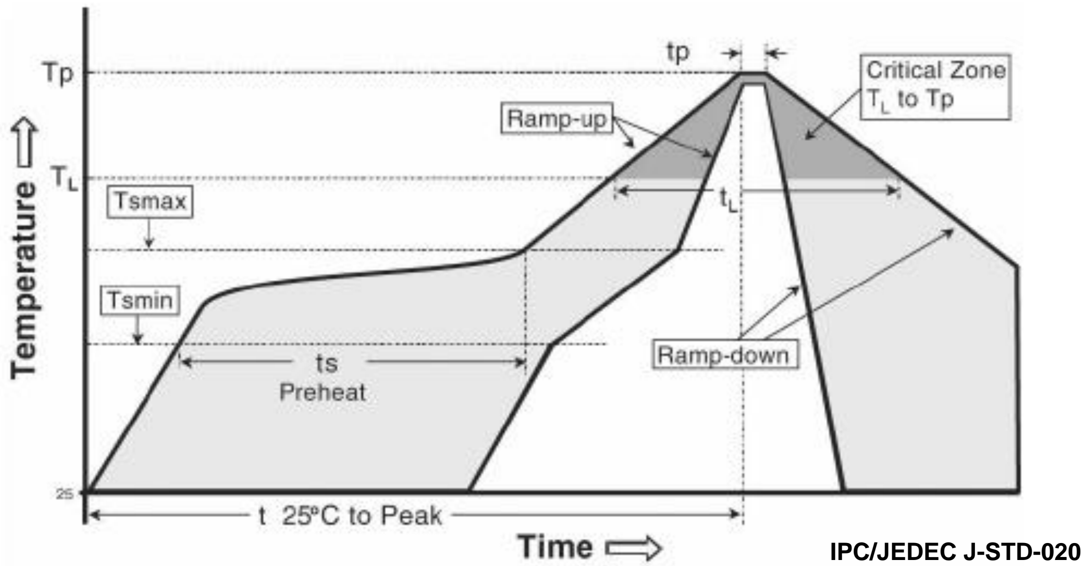
Reflow Soldering Characteristics



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (Tsmax to Tp)	3° C/second max.	3° C/second max.
Preheat - Temperature Min (Tsmin) - Temperature Max (Tsmax) - Time (Tsmin to Tsmax) (ts)	 100 °C 150 °C 60-120 seconds	 150 °C 200 °C 60-180 seconds
Time maintained above: - Temperature (TL) - Time (tL)	 183 °C 60-150 seconds	 217 °C 60-150 seconds
Peak Temperature (Tp)	215℃	260℃
Time within 5°C of actual Peak Temperature (tp)2	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Caution

1. Reflow soldering is recommended not to be done more than two times. In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
2. Repairs should not be done after the LEDs have been soldered. When repair is unavoidable, suitable tools must be used.
3. Die slug is to be soldered.
4. When soldering, do not put stress on the LEDs during heating.
5. After soldering, do not warp the circuit board.

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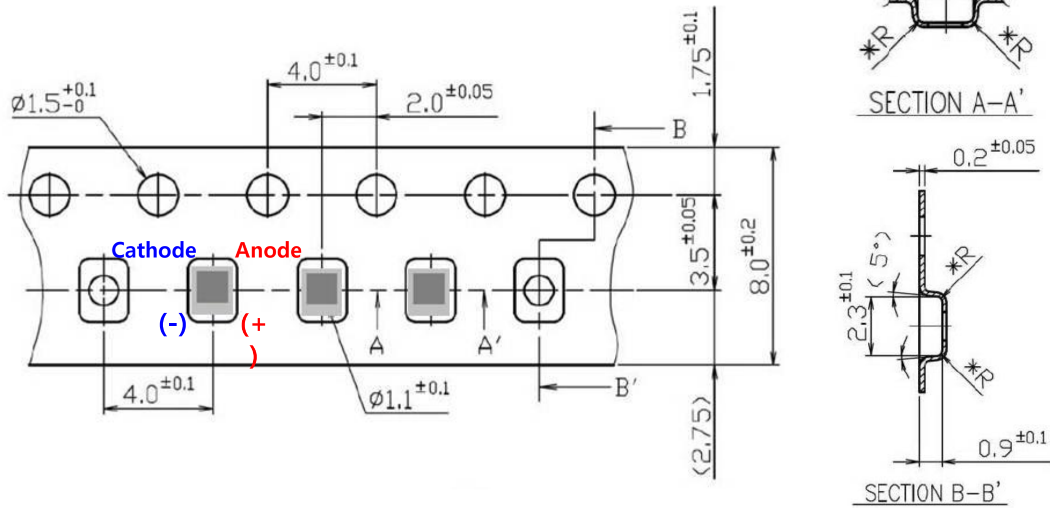
Emitter Tape & Reel Packaging



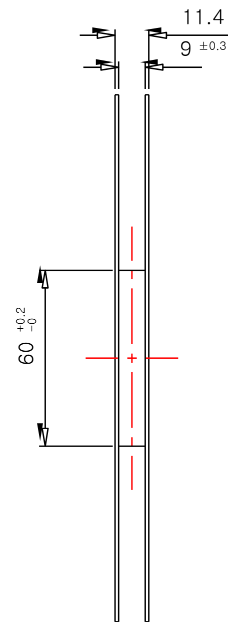
180 +0 -3

2 ±0.2

ø22

ø13 ±0.2

Label



(Tolerance: ±0.2, Unit: mm)

(1) Quantity : 1000pcs/Reel (Can be blank , less than 20)
(2) Cumulative Tolerance : Cumulative Tolerance/10 pitches to be ±0.2mm
(3) Adhesion Strength of Cover Tape : Adhesion strength to be 0.1-0.7N when the cover tape is turned off from the carrier tape at the angle of 10º to the carrier tape
(4) Package : P/N, Manufacturing data Code No. and quantity to be indicated on a damp proof Package

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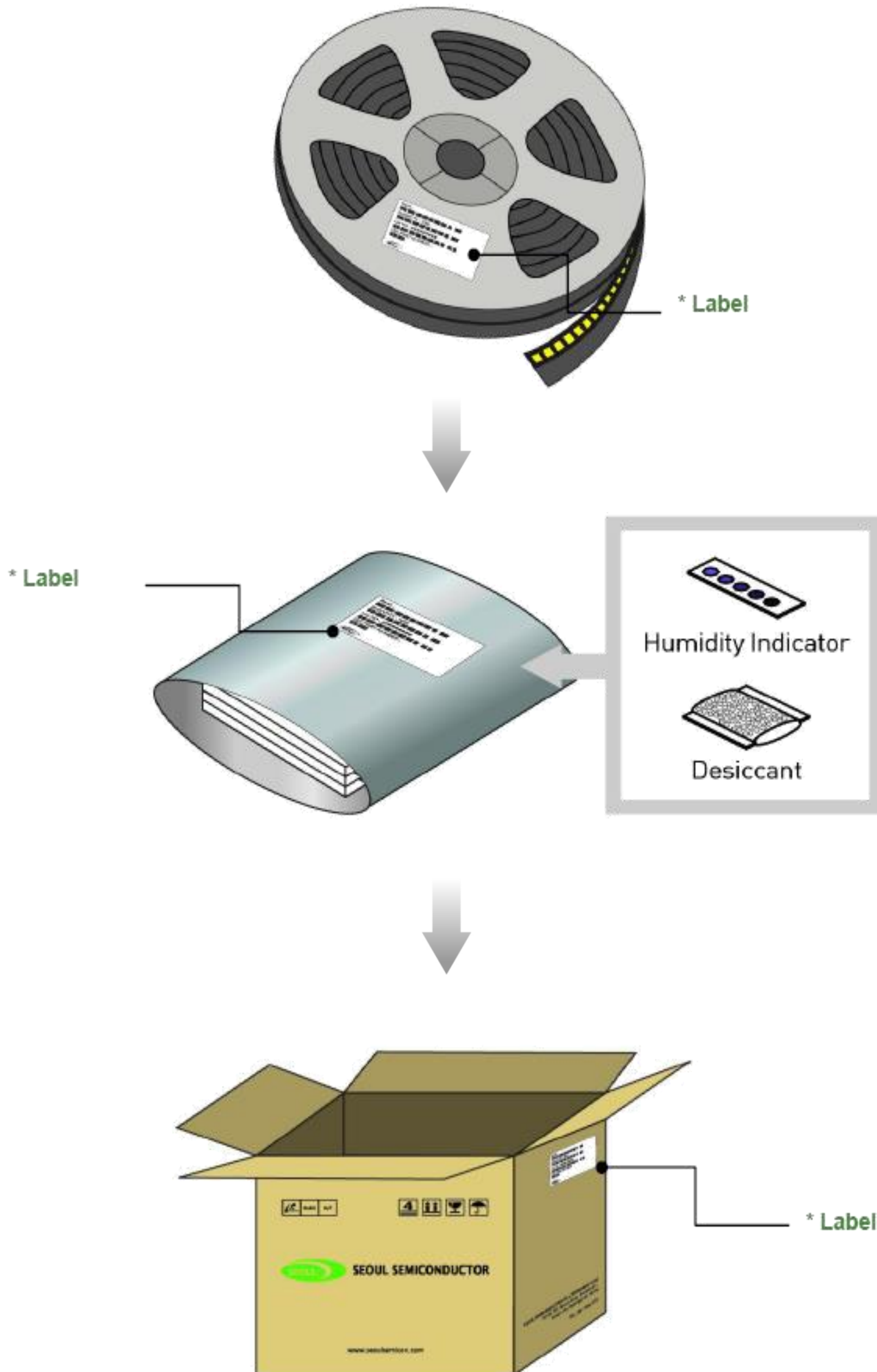
Emitter Tape & Reel Packaging



* Please refer to the next page for the 'Labeling Information' and 'Product Nomenclature'.

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Product Nomenclature

Table 4. Part Numbering System : $X_1X_2X_3X_4X_5X_6X_7X_8$

Part Number Code	Description	Part Number	Value
X_1	Company	S	SSC
X_2	Package Type	W	WICOP
X_3X_4	Color	W0	WHITE
X_5	Chip Type	Y	-
X_6X_7	Chip Number	1X	1CHIP
X_8	Product Revision	A	-

Table 5. Lot Numbering System :$Y_1Y_2Y_3Y_4Y_5Y_6Y_7Y_8Y_9Y_{10}$–$Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$

Lot Number Code	Description	Lot Number	Value
Y_1Y_2	Year		
Y_3	Month		
Y_4Y_5	Day		
Y_6	Top View LED series		
$Y_7Y_8Y_9Y_{10}$	Mass order		
$Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$	Internal Number		

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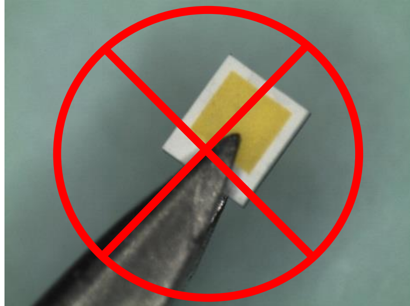
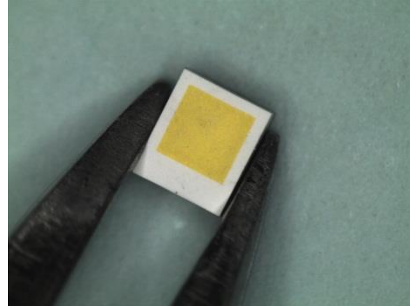
Handling of Silicone Resin for LEDs

1. During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.





2. In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.

3. When populating boards in SMT production, there are basically no restrictions regarding the form of the pick and place nozzle, except that mechanical pressure on the surface of the resin must be prevented. This is assured by choosing a pick and place nozzle which is larger than the LED's reflector area.

4. Silicone differs from materials conventionally used for the manufacturing of LEDs. These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust. As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components.

5. Seoul Semiconductor suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.

6. Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this product with acid or sulfur material in sealed space.

7. Avoid leaving fingerprints on silicone resin parts.

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Precaution for Use

(1) Storage

To avoid the moisture penetration, we recommend store in a dry box with a desiccant.

The recommended storage temperature range is 5℃ to 30℃ and a maximum humidity of RH50%.

(2) Use Precaution after Opening the Packaging

Use proper SMT techniques when the LED is to be soldered dipped as separation of the lens may affect the light output efficiency.

Pay attention to the following:

a. Recommend conditions after opening the package

- Sealing
- Temperature : 5 ~ 40℃ Humidity : less than RH30%

b. If the package has been opened more than 1 year (MSL_2) or the color of the desiccant changes, components should be dried for 10-12hr at 60±5℃

(3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering.

(4) Do not rapidly cool device after soldering.

(5) Components should not be mounted on warped (non coplanar) portion of PCB.

(6) Radioactive exposure is not considered for the products listed here in.

(7) Gallium arsenide is used in some of the products listed in this publication.

These products are dangerous if they are burned or shredded in the process of disposal.

It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.

(8) This device should not be used in any type of fluid such as water, oil, organic solvent and etc.

When washing is required, IPA (Isopropyl Alcohol) should be used.

(9) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.

(10) LEDs must be stored properly to maintain the device. If the LEDs are stored for 3 months or more after being shipped from SSC, a sealed container with a nitrogen atmosphere should be used for storage.

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Precaution for Use

(11) The appearance and specifications of the product may be modified for improvement without notice.

(12) Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.

(13) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.

(14) The slug is electrically isolated.

(15) Attaching LEDs, do not use adhesives that outgas organic vapor.

(16) The driving circuit must be designed to allow forward voltage only when it is ON or OFF.
If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

(17) Similar to most Solid state devices;
LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS).
Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.

a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event.
One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:
- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

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Precaution for Use

Environmental controls:
- Humidity control (ESD gets worse in a dry environment)

b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device.
The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
  (If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package
  (shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires
- This damage usually appears due to the thermal stress produced during the EOS event

c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:

- A surge protection circuit
- An appropriately rated over voltage protection device
- A current limiting device

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Company Information

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Company Information

Seoul Semiconductor (SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", deep UV LEDs, "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs. The company's broad product portfolio includes a wide array of package and device choices such as Acrich, high-brightness LEDs, mid-power LEDs, side-view LEDs, through-hole type LED lamps, custom displays, and sensors. The company is vertically integrated from epitaxial growth and chip manufacture in it's fully owned subsidiary, Seoul Viosys, through packaged LEDs and LED modules in three Seoul Semiconductor manufacturing facilities. Seoul Viosys also manufactures a wide range of unique deep-UV wavelength devices.

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