



SGM3715

0.8 Ω , High Voltage, Rail-to-Rail Negative Signal Passing, Dual, SPDT Analog Switch

GENERAL DESCRIPTION

The SGM3715 is a dual SPDT (single-pole/double-throw) analog switch. It operates from a 2.7V to 12V single power supply and allows negative signal passing with low distortion.

The SGM3715 features low on-resistance, high voltage and fast switching times. The high performances make it very suitable for multiple applications. In addition, the SGM3715 can be used as a dual 2-to-1 multiplexer because it has two normally open and close switches, respectively.

The SGM3715 is available in Green WLCSP-1.27 \times 2.13-15B package. It operates over an ambient temperature range of -40 $^{\circ}$ C to +85 $^{\circ}$ C.

FEATURES

- **Single Supply Voltage Range: 2.7V to 12V**
- **On-Resistance for Switches: 0.8 Ω (TYP)**
- **-V_{CC} to +V_{CC} Rail-to-Rail Low Distortion Positive and Negative Signal Passing**
- **High Off-Isolation**
- **Very Low Crosstalk**
- **1.8V Logic Compatible Control Pin**
- **Break-Before-Make Switching**
- **-40 $^{\circ}$ C to +85 $^{\circ}$ C Operating Temperature Range**
- **Available in Green WLCSP-1.27 \times 2.13-15B Package**

APPLICATIONS

Portable Equipment
Battery-Powered Systems

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM3715	WLCSP-1.27×2.13-15B	-40°C to +85°C	SGM3715YG/TR	XXXXX 3715	Tape and Reel, 3000

NOTE: XXXXX = Date Code and Vendor Code.

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND	0V to 13.2V
IN1, IN2, EN to GND	0V to 6V
Analog Voltage Range ⁽¹⁾	(-V _{CC} - 0.3V) to (V _{CC} + 0.3V)
Continuous Current from NO to COM	±350mA
Continuous Current from NC to COM	±350mA
Peak Current from NO to COM	±400mA
Peak Current from NC to COM	±400mA
I/O Clamp Current (V _I < 0)	-30mA
Junction Temperature	+150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	8000V
MM	400V
CDM	1000V

NOTE:

1. Internal diodes will clamp voltages at NC, NO, or COM that exceed V_{CC} or GND. Limit the current through the forward diode to the maximum ratings.

RECOMMENDED OPERATING CONDITIONS

Supply Voltage Range	2.7V to 12V
Operating Temperature Range	-40°C to +85°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

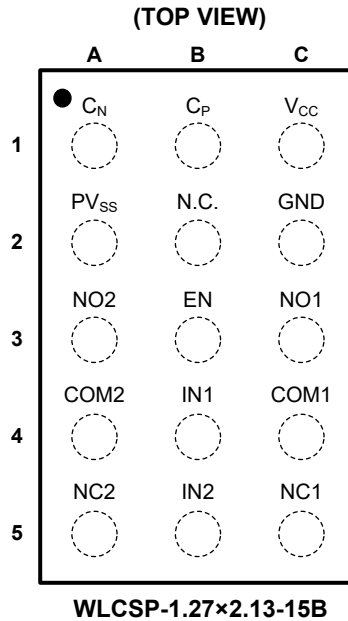
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION



PIN DESCRIPTION

PIN	NAME	FUNCTION
A1	C _N	Charge Pump Flying Capacitor Negative Pin.
B1	C _P	Charge Pump Flying Capacitor Positive Pin.
C1	V _{CC}	Power Supply Pin.
A2	PV _{SS}	Negative Supply Voltage Output Pin. Connect one 0.1μF ceramic capacitor from PV _{SS} to GND.
B2	N.C.	No Connection.
C2	GND	Ground.
A3	NO2	Normally-Open Pin.
B3	EN	Enable Control Pin. When EN = "Low", both NC and NO are disconnected with COM, negative charge pump does not work and the SGM3715 is in shutdown state. When EN = "High", negative charge pump works, the SGM3715 is in working state, and NC or NO is connected with COM depending on the logical state of IN.
C3	NO1	Normally-Open Pin.
A4	COM2	Common Pin.
B4	IN1	Digital Control Pin. It is used to connect COM1 to NO1 or NC1.
C4	COM1	Common Pin.
A5	NC2	Normally-Closed Pin.
B5	IN2	Digital Control Pin. It is used to connect COM2 to NO2 or NC2.
C5	NC1	Normally-Closed Pin.

NOTE: NO, NC and COM pins may be an input or output.

FUNCTION TABLE

Table 1. Function Table of Switch 1:

EN	IN1	COM1	NEGATIVE CHARGE PUMP
0	X	COM1 is disconnected with NO1 and NC1	Turn off
1	0	COM1 = NC1	Turn on
1	1	COM1 = NO1	Turn on

Table 2. Function Table of Switch 2:

EN	IN2	COM2	NEGATIVE CHARGE PUMP
0	X	COM2 is disconnected with NO2 and NC2	Turn off
1	0	COM2 = NC2	Turn on
1	1	COM2 = NO2	Turn on

ELECTRICAL CHARACTERISTICS

(V_{CC} = 3.3V, Full = -40°C to +85°C. Typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Analog Switch							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		Full	-V _{CC}		+V _{CC}	V
On-Resistance	R _{ON}	-V _{CC} ≤ V _{NO} , V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.8	1.1	Ω
			Full			1.7	
On-Resistance Match between Channels	ΔR _{ON}	-V _{CC} ≤ V _{NO} or V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.01	0.15	Ω
			Full			0.25	
On-Resistance Flatness	R _{FLAT(ON)}	-V _{CC} ≤ V _{NO} or V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.05	0.15	Ω
			Full			0.2	
Source Off Leakage Current	I _{NC(OFF)} , I _{NO(OFF)}	V _{NO} or V _{NC} = -2.8V, 2.8V, V _{COM} = 2.8V, -2.8V	+25°C	-0.5	0.01	0.5	μA
			Full			1	
Channel On Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V _{NO} or V _{NC} = -2.8V, 2.8V, V _{COM} = floating, or V _{NO} or V _{NC} = floating, V _{COM} = -2.8V, 2.8V	+25°C	-0.5	0.01	0.5	μA
			Full			1	
Digital Inputs							
Input High Voltage	V _{INH}	V _{CC} = 2.7V to 12V	Full	1.5		5.5	V
Input Low Voltage	V _{INL}	V _{CC} = 2.7V to 12V	Full	0		0.5	V
Pull Down Resistor	R _{PULL DOWN}		+25°C		600		kΩ
Dynamic Characteristics							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 2	+25°C		820		μs
Turn-Off Time	t _{OFF}	V _{NO} or V _{NC} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 2	+25°C		180		μs
Break-Before-Make Delay Time	t _D	V _{NO1} or V _{NC1} = V _{NO2} or V _{NC2} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 3	+25°C		680		μs
Off-Isolation	O _{ISO}	f = 1kHz, R _L = 32Ω, Signal = 0dBm, Test Circuit 4	+25°C		-130		dB
				f = 1MHz, R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 4	-50		
Channel-to-Channel Crosstalk	X _{TALK}	f = 1kHz, R _L = 32Ω, Signal = 0dBm, Test Circuit 5	+25°C		-120		dB
				f = 1MHz, R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 5	-60		
-3dB Bandwidth	BW	R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 6	+25°C		100		MHz
Channel On Capacitance	C _{ON}		+25°C		60		pF
Charge Injection	Q	V _G = GND, R _G = 0Ω, C _L = 1nF, Test Circuit 7	+25°C		1000		pC
Total Harmonic Distortion	THD	A-Weighting, Test Circuit 8	+25°C	V _{NO} , V _{NC} = 2V _{RMS} , R _L = 600Ω	-113		dB
				V _{NO} , V _{NC} = 2V _{PP} , R _L = 600Ω	-115		
				V _{NO} , V _{NC} = 2V _{PP} , R _L = 32Ω	-113		
				V _{NO} , V _{NC} = 1V _{PP} , R _L = 600Ω	-112		
				V _{NO} , V _{NC} = 1V _{PP} , R _L = 32Ω	-110		
				V _{NO} , V _{NC} = 0.5V _{PP} , R _L = 600Ω	-108		
				V _{NO} , V _{NC} = 0.5V _{PP} , R _L = 32Ω	-104		
Start-Up Time	t _{START}	Switch V _{EN} = 0V to V _{EN} = 1.5V	+25°C		0.5		ms

ELECTRICAL CHARACTERISTICS (continued)

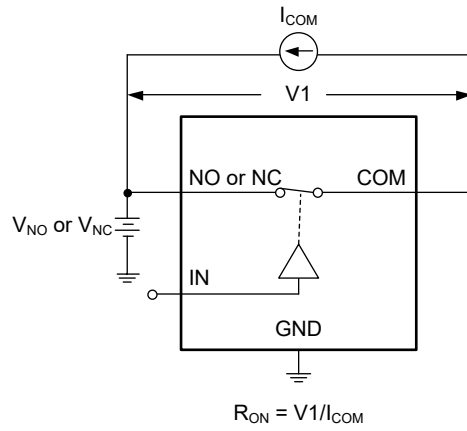
(V_{CC} = 5V, Full = -40°C to +85°C. Typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Analog Switch							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		Full	-V _{CC}		+V _{CC}	V
On-Resistance	R _{ON}	-V _{CC} ≤ V _{NO} , V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.8	1.1	Ω
			Full			1.7	
On-Resistance Match between Channels	ΔR _{ON}	-V _{CC} ≤ V _{NO} or V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.01	0.15	Ω
			Full			0.25	
On-Resistance Flatness	R _{FLAT(ON)}	-V _{CC} ≤ V _{NO} or V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.01	0.15	Ω
			Full			0.2	
Source Off Leakage Current	I _{NC(OFF)} , I _{NO(OFF)}	V _{NO} or V _{NC} = -4.5V, 4.5V, V _{COM} = 4.5V, -4.5V	+25°C	-0.5	0.01	0.5	μA
			Full			1	
Channel On Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V _{NO} or V _{NC} = -4.5V, 4.5V, V _{COM} = floating, or V _{NO} or V _{NC} = floating, V _{COM} = -4.5V, 4.5V	+25°C	-0.5	0.01	0.5	μA
			Full			1	
Dynamic Characteristics							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 2	+25°C		880		μs
Turn-Off Time	t _{OFF}	V _{NO} or V _{NC} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 2	+25°C		190		μs
Break-Before-Make Delay Time	t _D	V _{NO1} or V _{NC1} = V _{NO2} or V _{NC2} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 3	+25°C		720		μs
Off-Isolation	O _{ISO}	f = 1kHz, R _L = 32Ω, Signal = 0dBm, Test Circuit 4	+25°C		-130		dB
		f = 1MHz, R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 4			-50		
Channel-to-Channel Crosstalk	X _{TALK}	f = 1kHz, R _L = 32Ω, Signal = 0dBm, Test Circuit 5	+25°C		-120		dB
		f = 1MHz, R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 5			-60		
-3dB Bandwidth	BW	R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 6	+25°C		100		MHz
Channel On Capacitance	C _{ON}		+25°C		60		pF
Charge Injection	Q	V _G = GND, R _G = 0Ω, C _L = 1nF, Test Circuit 7	+25°C		1000		pC
Total Harmonic Distortion	THD	A-Weighting, Test Circuit 8	+25°C	V _{NO} , V _{NC} = 2V _{RMS} , R _L = 600Ω		-117	dB
				V _{NO} , V _{NC} = 2V _{PP} , R _L = 600Ω		-115	
				V _{NO} , V _{NC} = 2V _{PP} , R _L = 32Ω		-113	
				V _{NO} , V _{NC} = 1V _{PP} , R _L = 600Ω		-112	
				V _{NO} , V _{NC} = 1V _{PP} , R _L = 32Ω		-110	
				V _{NO} , V _{NC} = 0.5V _{PP} , R _L = 600Ω		-108	
				V _{NO} , V _{NC} = 0.5V _{PP} , R _L = 32Ω		-104	
Start-Up Time	t _{START}	Switch V _{EN} = 0V to V _{EN} = 1.5V	+25°C		0.5		ms
Power Requirements							
Power Supply Current	I _{CC}	V _{IN} = 0V or 1.5V, V _{EN} = 1.5V	+25°C		520	650	μA
			Full			680	
Power Supply Current in Shutdown State	I _{CC}	V _{IN} = 0V or 1.5V, V _{EN} = 0V	+25°C		0.4	1	μA
			Full			1.5	

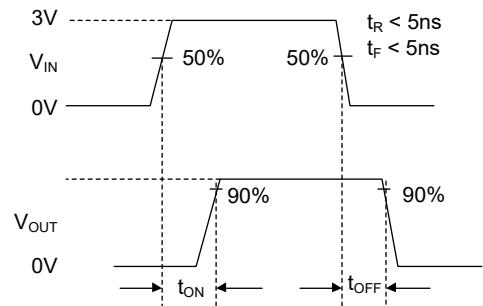
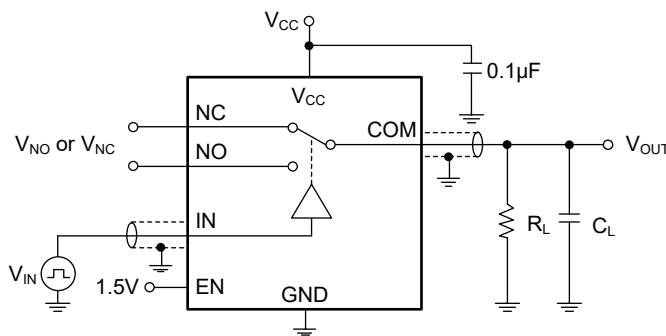
ELECTRICAL CHARACTERISTICS (continued)(V_{CC} = 12V, Full = -40°C to +85°C. Typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Analog Switch							
Analog Signal Range	V _{NO} , V _{NC} , V _{COM}		Full	-V _{CC}		+V _{CC}	V
On-Resistance	R _{ON}	-V _{CC} ≤ V _{NO} , V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.8	1.1	Ω
			Full			1.7	
On-Resistance Match between Channels	ΔR _{ON}	-V _{CC} ≤ V _{NO} or V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.01	0.15	Ω
			Full			0.25	
On-Resistance Flatness	R _{FLAT(ON)}	-V _{CC} ≤ V _{NO} or V _{NC} ≤ V _{CC} , I _{COM} = -50mA, Test Circuit 1	+25°C		0.01	0.15	Ω
			Full			0.2	
Source Off Leakage Current	I _{NC(OFF)} , I _{NO(OFF)}	V _{NO} or V _{NC} = -11.5V, 11.5V, V _{COM} = 11.5V, -11.5V	+25°C	-1.5	0.05	1.5	μA
			Full			9	
Channel On Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V _{NO} or V _{NC} = -11.5V, 11.5V, V _{COM} = floating, or V _{NO} or V _{NC} = floating, V _{COM} = -11.5V, 11.5V	+25°C	-1.5	0.05	1.5	μA
			Full			9	
Dynamic Characteristics							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 2	+25°C		1100		μs
Turn-Off Time	t _{OFF}	V _{NO} or V _{NC} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 2	+25°C		200		μs
Break-Before-Make Delay Time	t _D	V _{NO1} or V _{NC1} = V _{NO2} or V _{NC2} = 1V, R _L = 50Ω, C _L = 35pF, Test Circuit 3	+25°C		950		μs
Off-Isolation	O _{ISO}	f = 1kHz, R _L = 32Ω, Signal = 0dBm, Test Circuit 4 f = 1MHz, R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 4	+25°C		-130		dB
					-50		
Channel-to-Channel Crosstalk	X _{TALK}	f = 1kHz, R _L = 32Ω, Signal = 0dBm, Test Circuit 5 f = 1MHz, R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 5	+25°C		-120		dB
					-60		
-3dB Bandwidth	BW	R _L = 50Ω, C _L = 5pF, Signal = 0dBm, Test Circuit 6	+25°C		100		MHz
Channel On Capacitance	C _{ON}		+25°C		60		pF
Charge Injection	Q	V _G = GND, R _G = 0Ω, C _L = 1nF, Test Circuit 7	+25°C		1100		pC
Total Harmonic Distortion	THD	A-Weighting, Test Circuit 8	+25°C	V _{NO} , V _{NC} = 2V _{RMS} , R _L = 600Ω	-117		dB
				V _{NO} , V _{NC} = 2V _{PP} , R _L = 600Ω	-115		
				V _{NO} , V _{NC} = 2V _{PP} , R _L = 32Ω	-113		
				V _{NO} , V _{NC} = 1V _{PP} , R _L = 600Ω	-112		
				V _{NO} , V _{NC} = 1V _{PP} , R _L = 32Ω	-110		
				V _{NO} , V _{NC} = 0.5V _{PP} , R _L = 600Ω	-108		
				V _{NO} , V _{NC} = 0.5V _{PP} , R _L = 32Ω	-104		
Start Up Time	t _{START}	Switch V _{EN} = 0V to V _{EN} = 1.5V	+25°C		0.5		ms
Power Requirements							
Power Supply Current	I _{CC}	V _{IN} = 0V or 1.5V, V _{EN} = 1.5V	+25°C		620	780	μA
			Full			800	
Power Supply Current in Shutdown State	I _{CC}	V _{IN} = 0V or 1.5V, V _{EN} = 0V	+25°C		0.5	1.5	μA
			Full			2	

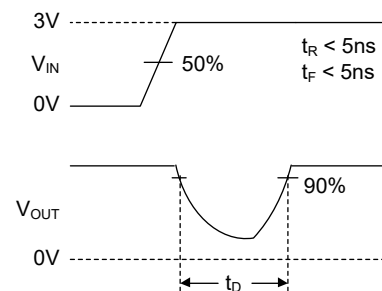
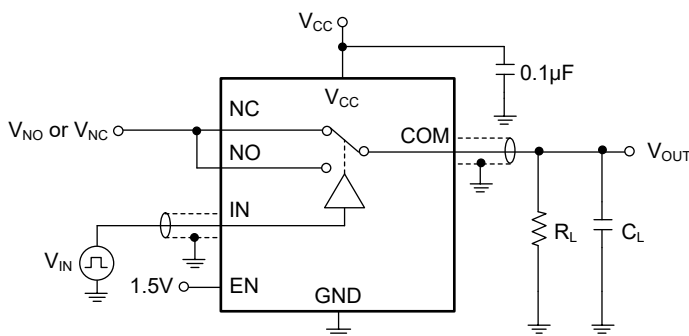
TEST CIRCUITS



Test Circuit 1. On-Resistance

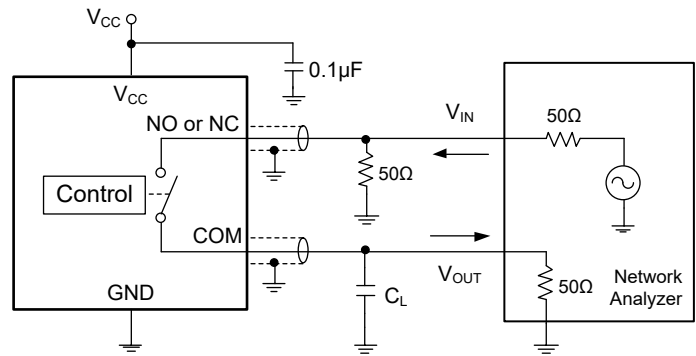


Test Circuit 2. Switching Times (t_{ON} , t_{OFF})

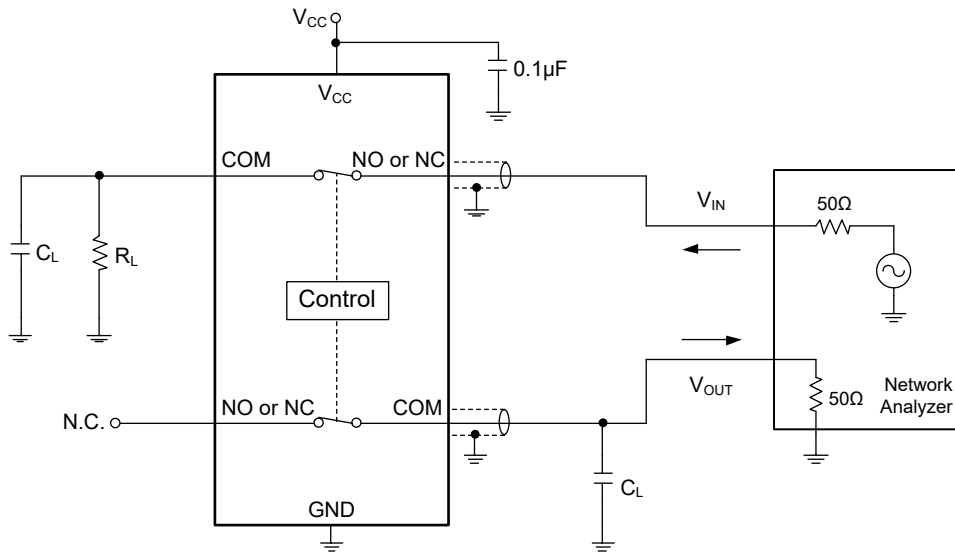


Test Circuit 3. Break-Before-Make Delay Time (t_D)

TEST CIRCUITS (continued)

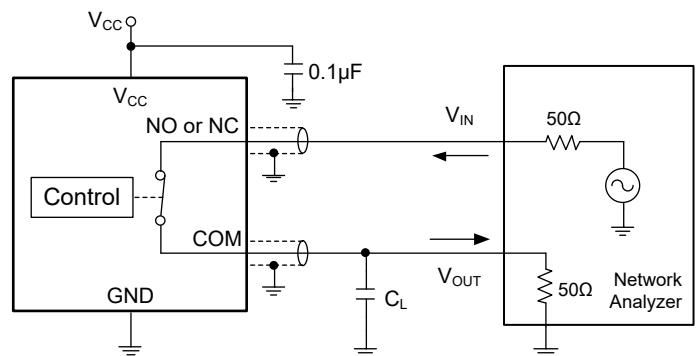


Test Circuit 4. Off-Isolation



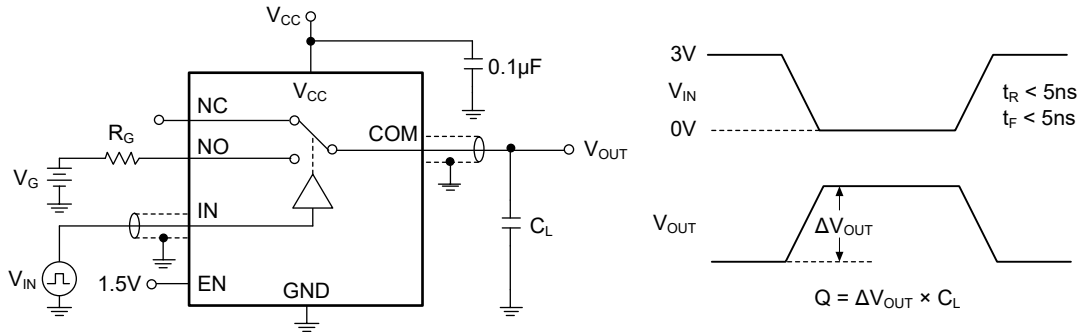
$$\text{Channel-to-Channel Crosstalk} = -20\log(V_{IN}/V_{OUT})$$

Test Circuit 5. Channel-to-Channel Crosstalk

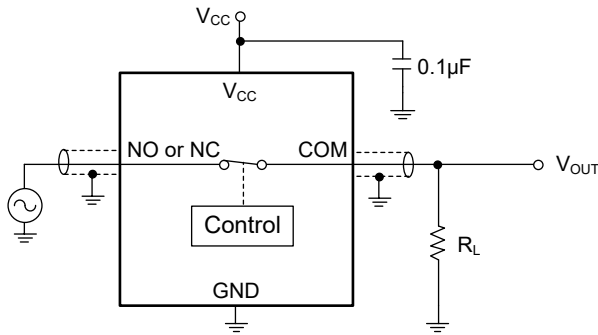


Test Circuit 6. -3dB Bandwidth

TEST CIRCUITS (continued)



Test Circuit 7. Charge Injection (Q)



Test Circuit 8. Total Harmonic Distortion (THD)

APPLICATION INFORMATION

The combination of Speaker and Receiver is always used in portable devices, and high voltage class D speaker driver is used to drive speaker in order to provide high audio volume. But the high output voltage of class D speaker driver will damage the receiver driver. The SGM3715 provides the safe isolation between receiver driver and high voltage class D speaker driver. The SGM3715 provides low R_{ON} channels to pass the positive and negative signals from capless receiver driver. The circuit is shown in Figure 1.

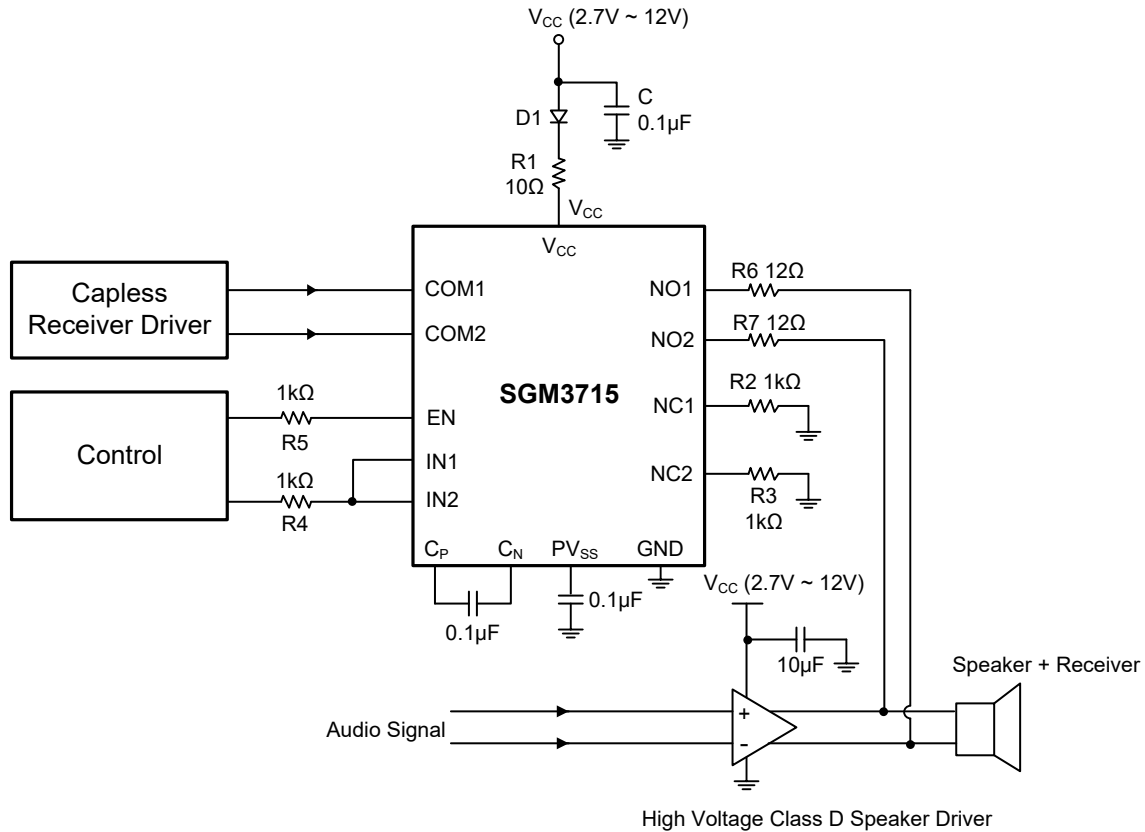


Figure 1. Typical Application Circuit for Speaker + Receiver

APPLICATION INFORMATION (continued)

In order to improve audio performance of portable equipment, external speaker power amplifier is always selected to replace the internal integrated speaker power amplifier. Because the audio signal quality of audio line out or headset driver is better than that of the integrated speaker power amplifier, the audio signal of line out or headset driver is selected as the high performance audio signal source for external speaker power amplifier. High performance SGM3715 is used as the 1-to-2 HiFi signal switch in this application. The circuit is shown in Figure 2, and a stable 3.3V power supply is required in this circuit.

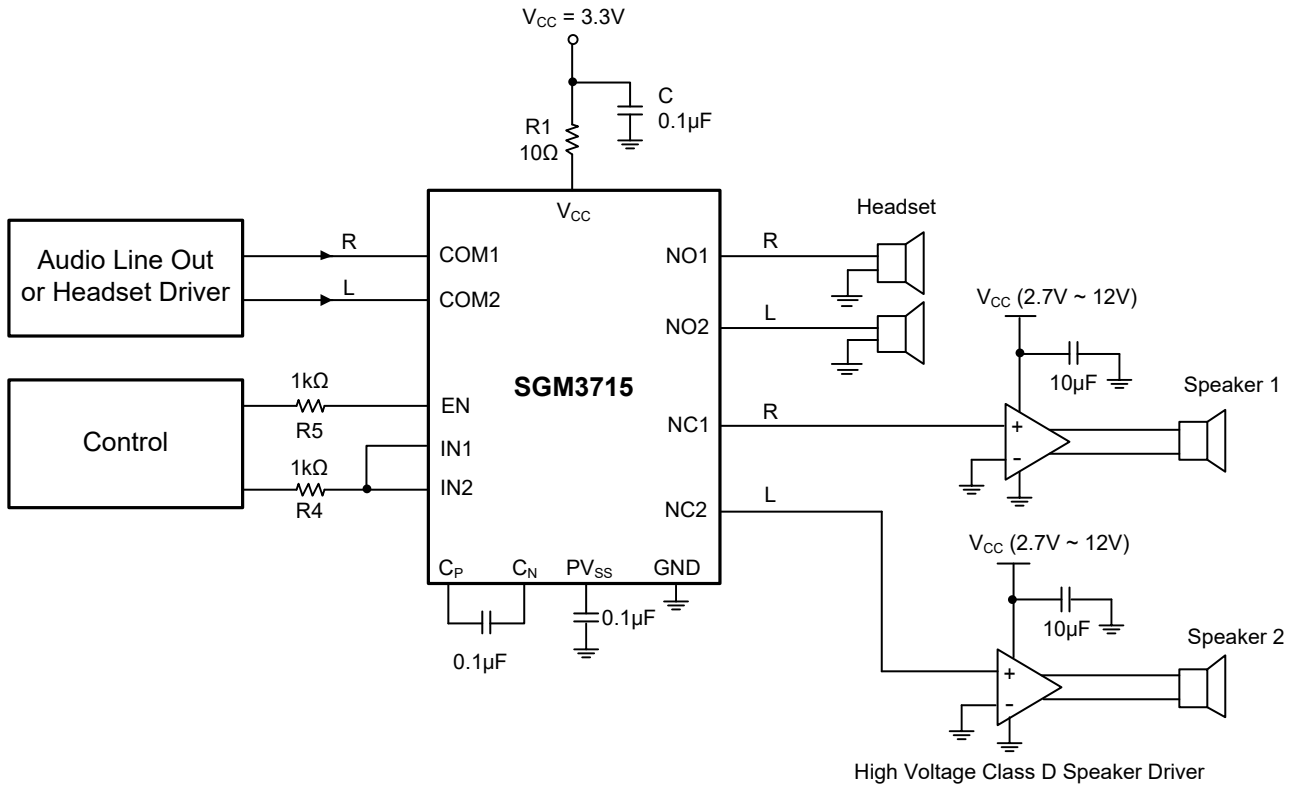


Figure 2. Typical Application Circuit for 1-to-2 HiFi Audio Signal Switch

REVISION HISTORY

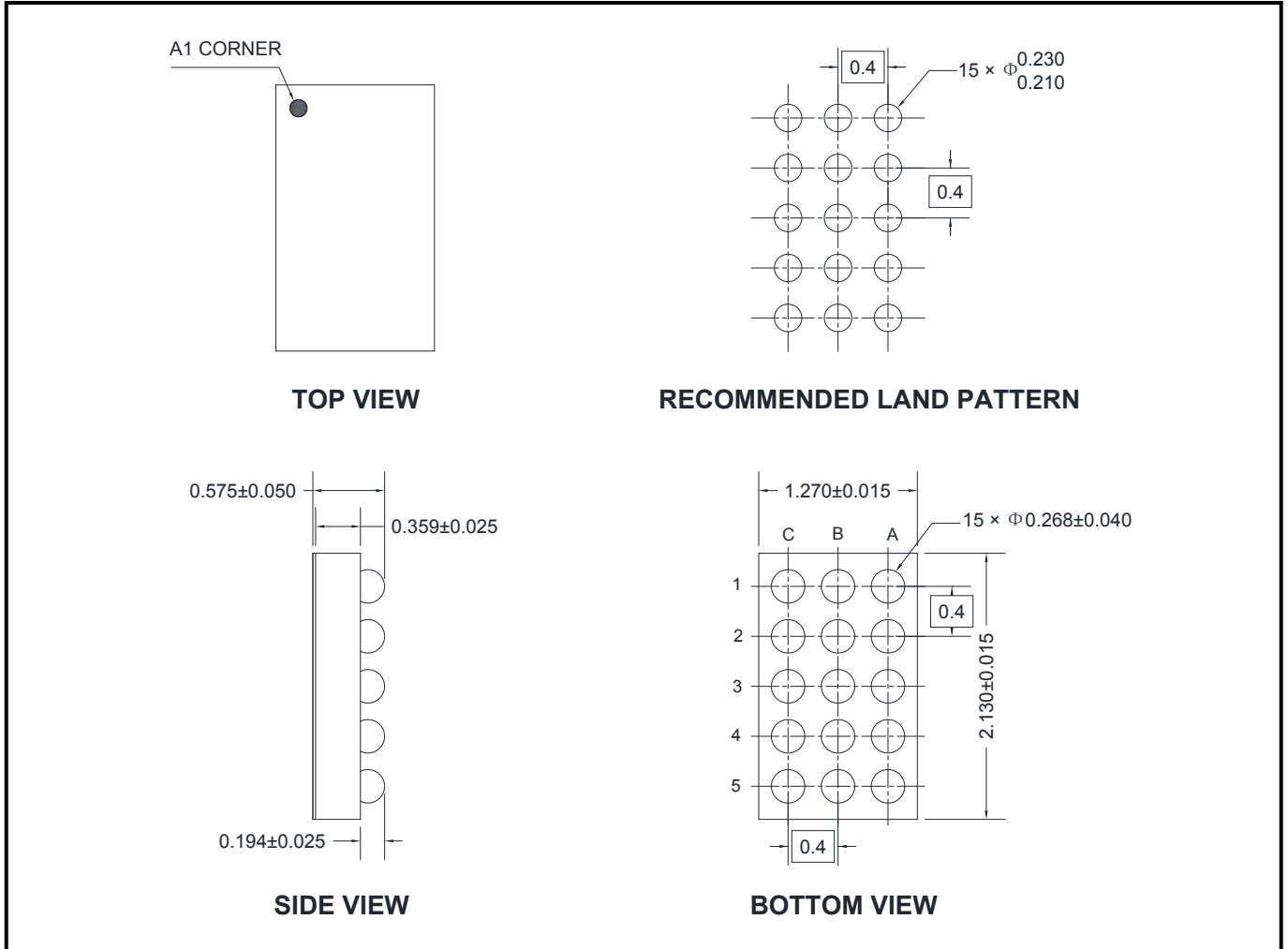
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Original (NOVEMBER 2017) to REV.A

Changed from product preview to production data.....	All
--	-----

PACKAGE OUTLINE DIMENSIONS

WLCSP-1.27×2.13-15B

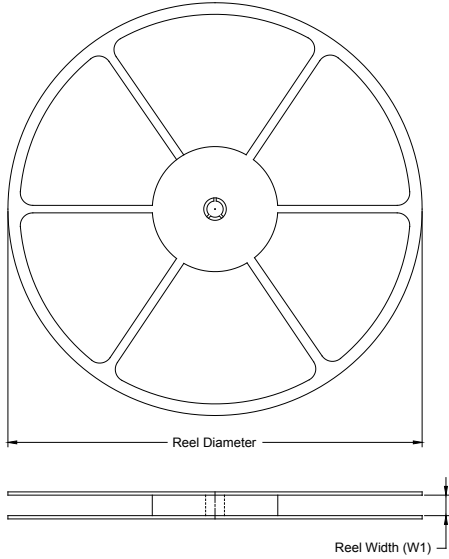


NOTE: All linear dimensions are in millimeters.

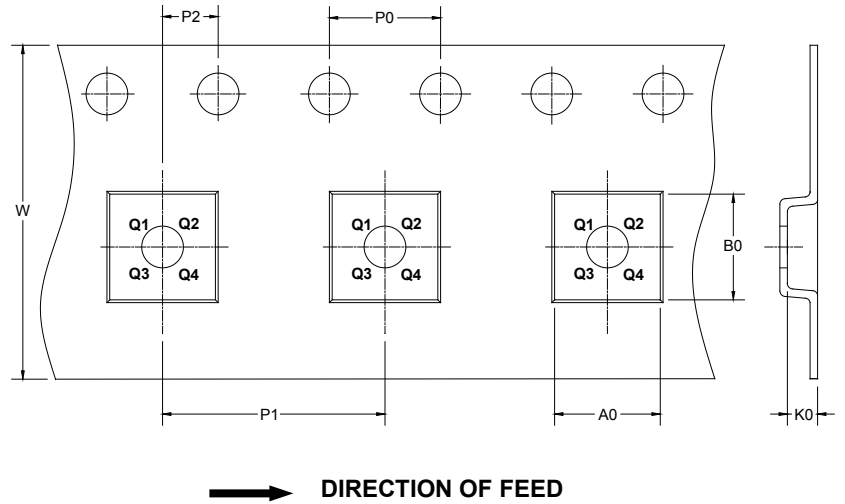
PACKAGE INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
WLCSP-1.27×2.13-15B	7"	9.5	1.47	2.37	0.78	4.0	4.0	2.0	8.0	Q1

000001

PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18

DD0002

单击下面可查看定价，库存，交付和生命周期等信息

[>>SGMICRO\(圣邦微电子\)](#)