

GENERAL DESCRIPTION

The SGM2567 is a single load switch with reverse current protection function. The device can operate from 1V to 5.5V single supply and has the ability to drive up to 4A continuous current.

The device contains a 15mΩ low R_{ON} N-MOSFET controlled by the ON pin. When the power supply is turned on for the first time, a smart pull-down resistor is used to float the ON pin until the system is stable. Once the ON pin reaches a high voltage (> V_{IH}), the pull-down resistor is disconnected, then the standby current is very low and power loss can be reduced. The small package and low R_{ON} make the device very suitable for space limited, battery powered applications.

The device supports a wide input voltage range, which is suitable for many different voltage rails. The rise time is used to avoid inrush current. The SGM2567 offers the quick output discharge function in disable status.

The SGM2567 is available in a Green WLCSP-1.45×0.95-6B package.

FEATURES

- **Input Voltage Range: 1V to 5.5V**
- **Maximum Continuous Current: 4A**
- **Low On-Resistance**
 - ◆ R_{ON} = 15mΩ at V_{IN} = 5V
 - ◆ R_{ON} = 15mΩ at V_{IN} = 3.3V
- **Low Shutdown Current: 170nA (TYP)**
- **Reverse Current Protection When Disabled**
- **Low Threshold 1.8V GPIO Control Input**
- **Bidirectional Power Supply for Power Zone Application**
- **Controlled Slew Rate to Avoid Inrush Current**
- **Over-Temperature Protection**
- **Quick Output Discharge**
- **Available in a Green WLCSP-1.45×0.95-6B Package**

APPLICATIONS

- Smartphone
- Notebook Computer and Ultrabook
- Optical Module
- Solid State Drive (SSD)
- DTV/IP Set Top Box
- POS Terminal and Media Gateway

TYPICAL APPLICATION

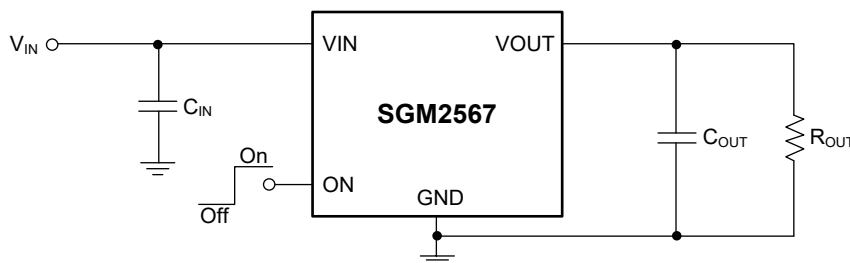


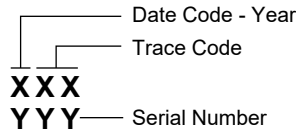
Figure 1. Typical Application Circuit

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM2567	WLCSP-1.45×0.95-6B	-40°C to +125°C	SGM2567XG/TR	XXX CK3	Tape and Reel, 3000

MARKING INFORMATION

NOTE: XXX = Date Code and Trace Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

- Input Voltage Range, V_{IN} -0.3V to 6V
- Output Voltage Range, V_{OUT} -0.3V to 6V
- ON Pin Voltage Range, V_{ON} -0.3V to 6V
- Maximum Continuous Switch Current, I_{MAX} 4A
- Package Thermal Resistance
WLCSP-1.45×0.95-6B, θ_{JA} 159°C/W
- Junction Temperature +150°C
- Storage Temperature Range -65°C to +150°C
- Lead Temperature (Soldering, 10s) +260°C
- ESD Susceptibility
- HBM 4000V
- CDM 1000V

RECOMMENDED OPERATING CONDITIONS

- Input Voltage Range, V_{IN} 1V to 5.5V
- Output Voltage Range, V_{OUT} 0V to 5.5V
- High-Level ON Pin Voltage, V_{IH} 1.2V to 5.5V
- Low-Level ON Pin Voltage, V_{IL} 0V to 0.4V
- Input Capacitance, C_{IN} 1µF
- Operating Junction Temperature Range -40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

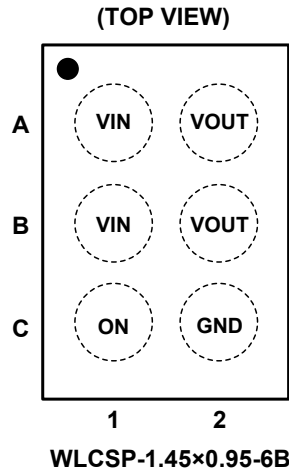
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

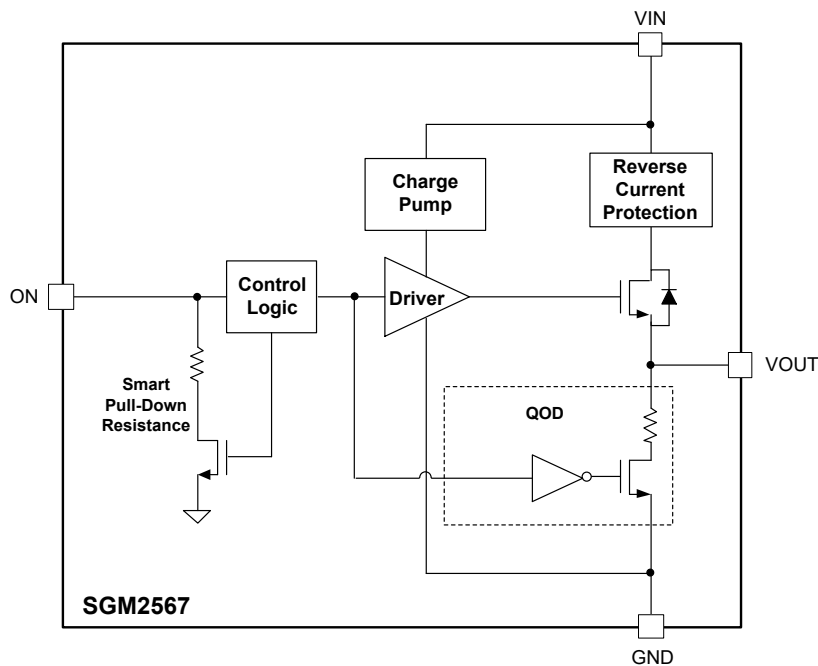
PIN CONFIGURATION



PIN DESCRIPTION

PIN	NAME	FUNCTION
A1, B1	VIN	Switch Input. A bypass capacitor (ceramic) to ground is recommend.
A2, B2	VOUT	Switch Output.
C1	ON	Switch Control Input. Logic high turns on the power switch.
C2	GND	Ground.

FUNCTIONAL BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS

(V_{IN} = 1V to 5.5V, C_{IN} = 1μF, C_{OUT} = 0.1μF, typical values are at T_J = +25°C, unless otherwise noted.)

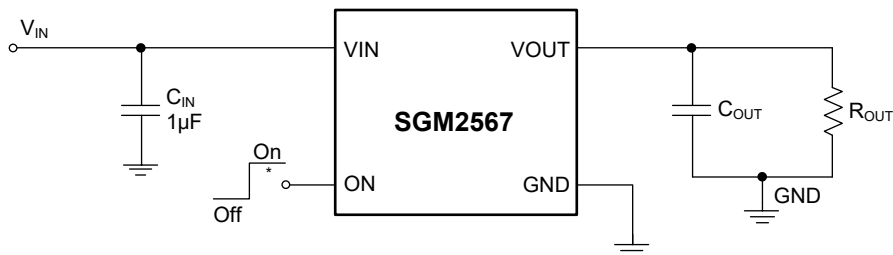
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Input Voltage Range	V _{IN}	T _J = -40°C to +125°C	1		5.5	V	
Quiescent Current	I _Q	V _{IN} = 5.5V, V _{ON} = 1.2V, I _{OUT} = 0A, T _J = -40°C to +125°C		680	1700	nA	
		V _{IN} = 3.3V, V _{ON} = 1.2V, I _{OUT} = 0A, T _J = -40°C to +125°C		530	1400		
		V _{IN} = 1V, V _{ON} = 1.2V, I _{OUT} = 0A, T _J = -40°C to +125°C		400	950		
Shutdown Current	I _{SD}	V _{IN} = 5.5V, V _{ON} = 0V, T _J = -40°C to +85°C		0.17	1.2	μA	
Supply Leakage Current in Shutdown Mode	I _{LEAKAGE}	V _{IN} = 5.5V, V _{ON} = 0V, V _{OUT} = 0V, T _J = -40°C to +85°C			1.3	μA	
On-Resistance	R _{ON}	V _{IN} = 5V, V _{ON} = 1.2V, I _{OUT} = -200mA	T _J = -40°C to +85°C		15	34	mΩ
			T _J = -40°C to +125°C				37
		V _{IN} = 3.3V, V _{ON} = 1.2V, I _{OUT} = -200mA	T _J = -40°C to +85°C		15	34	mΩ
			T _J = -40°C to +125°C				38
ON Pin Hysteresis	V _{HYS}	V _{IN} = 5.5V		56		mV	
		V _{IN} = 3.3V		47			
ON Pin Leakage Current	I _{ON}	V _{ON} = 5.5V, T _J = -40°C to +125°C			1.4	μA	
Reverse Current When Disabled	I _{RC}	V _{IN} = V _{ON} = 0V, V _{OUT} = 5.5V, T _J = -40°C to +85°C			1.2	μA	
Output Pull-Down Resistance	R _{PD}	V _{ON} = 0V, I _{OUT} = 2mA, T _J = -40°C to +125°C		280	440	Ω	
Smart Pull-Down Resistance	R _{PD_ON}	Disabled		800		kΩ	
ON Pin Input Low Voltage	V _{IL}	T _J = -40°C to +125°C			0.4	V	
ON Pin Input High Voltage	V _{IH}	T _J = -40°C to +125°C	1.2				
Over-Temperature Shutdown Threshold	T _{SD}			170		°C	
Over-Temperature Shutdown Hysteresis	T _{HYS}			25		°C	

SWITCHING CHARACTERISTICS

(C_{IN} = 1μF, R_{OUT} = 10Ω, C_{OUT} = 0.1μF, typical values are at T_J = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
V_{IN} = 5.0V, T_J = +25°C, unless otherwise noted.						
Turn-On Time	t _{ON}	Figure 2 and Figure 3		2600		μs
Turn-Off Time	t _{OFF}			8		
V _{OUT} Rise Time	t _R			3900		
V _{OUT} Fall Time	t _F			2		
Delay Time	t _D			1600		
V_{IN} = 3.3V, T_J = +25°C, unless otherwise noted.						
Turn-On Time	t _{ON}	Figure 2 and Figure 3		3000		μs
Turn-Off Time	t _{OFF}			7		
V _{OUT} Rise Time	t _R			3600		
V _{OUT} Fall Time	t _F			2		
Delay Time	t _D			1700		

PARAMETER MEASUREMENT INFORMATION



*: Rise and fall times of the control signal are 100ns.

Figure 2. Test Circuit

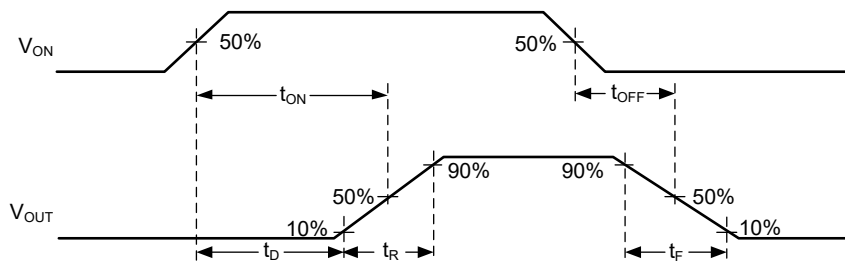
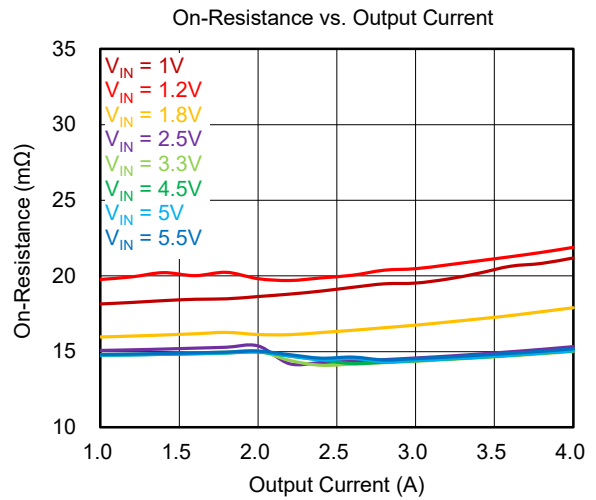
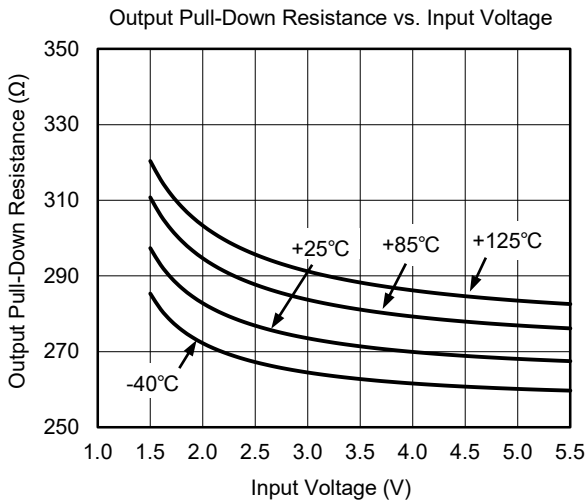
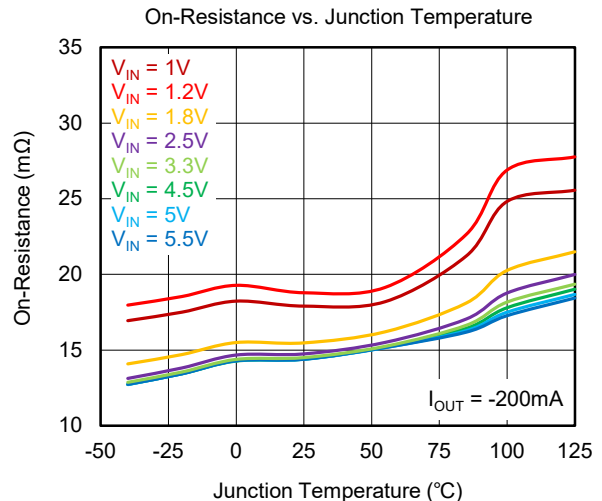
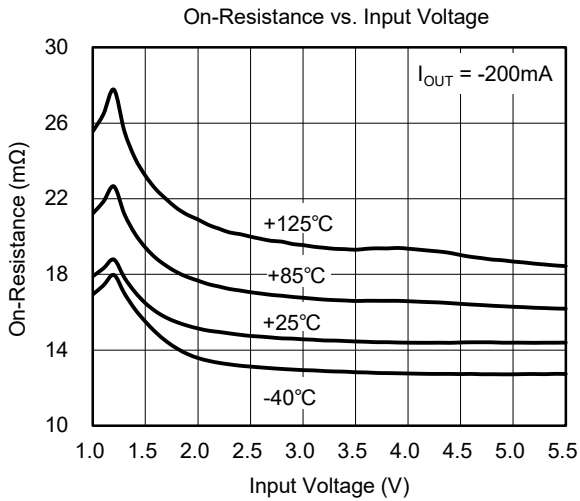
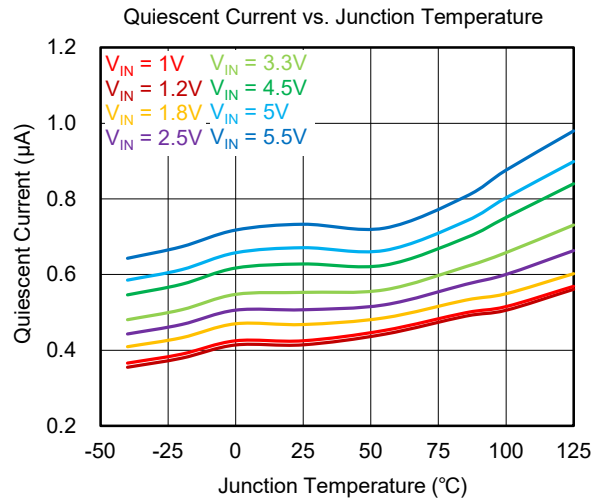
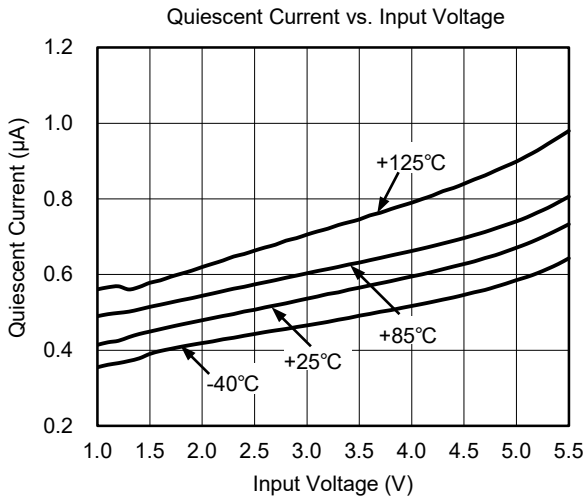


Figure 3. Timing Waveforms

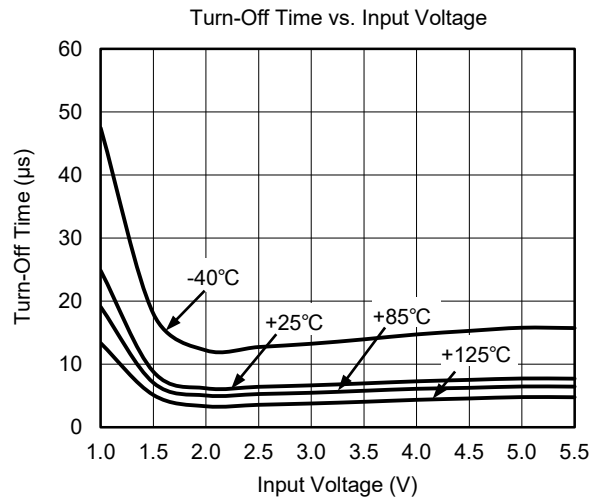
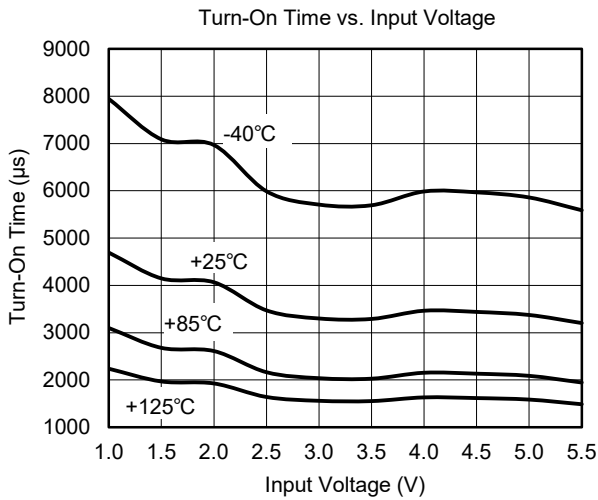
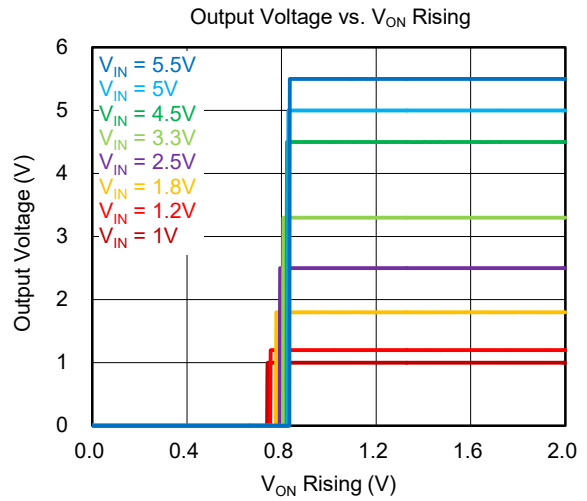
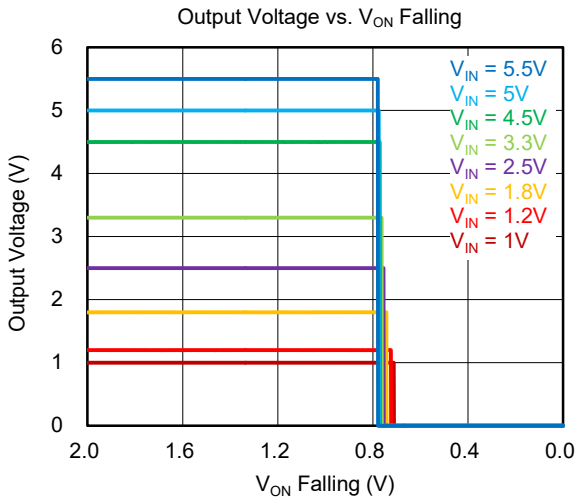
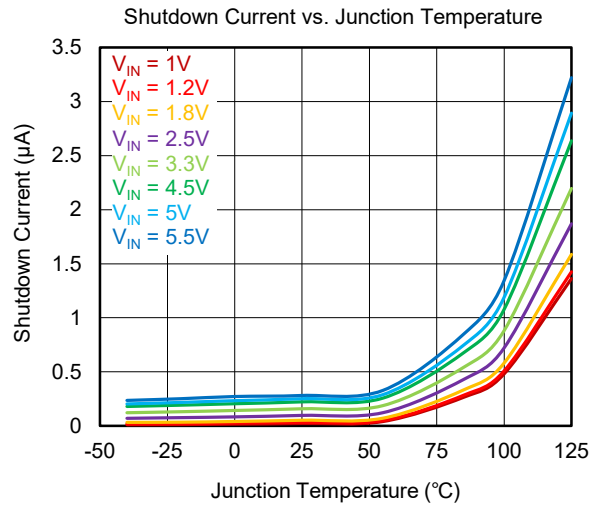
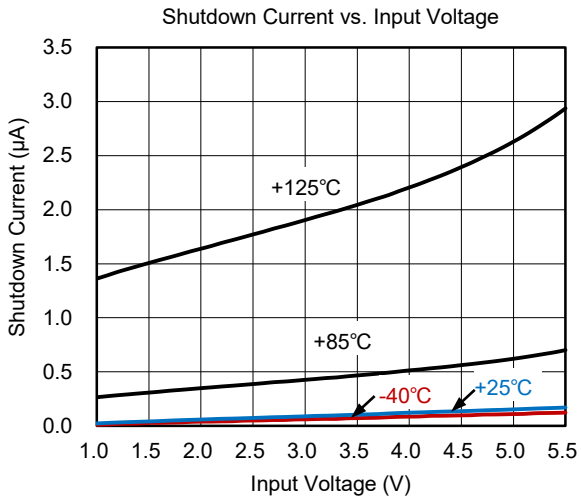
TYPICAL PERFORMANCE CHARACTERISTICS

T_J = +25°C, C_{IN} = 1μF, C_{OUT} = 0.1μF, R_{OUT} = 10Ω, V_{IH} = 1.2V, V_{IL} = 0V, unless otherwise noted.



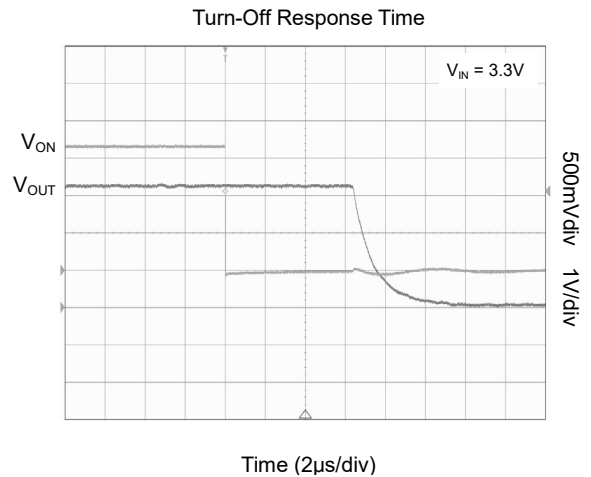
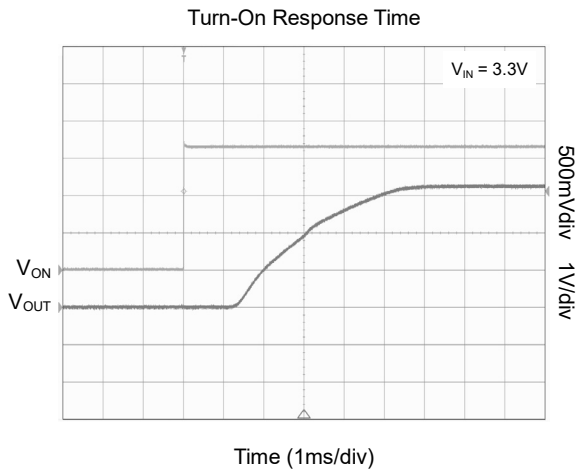
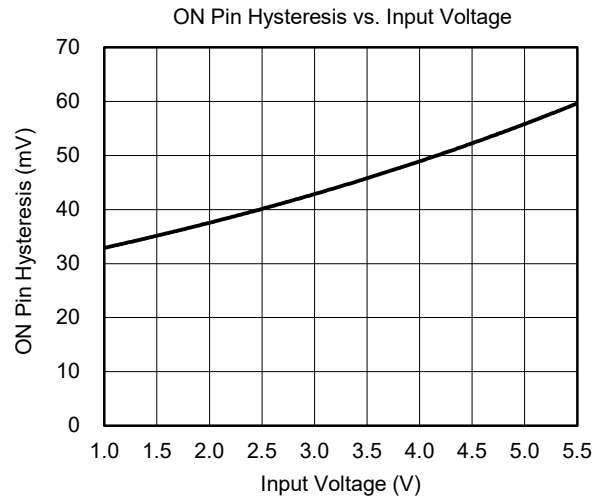
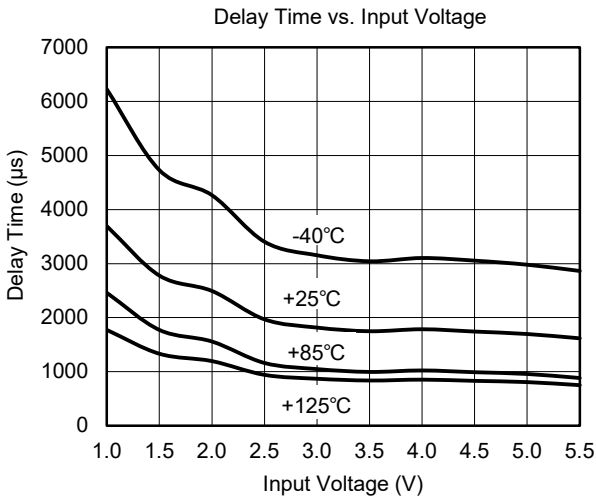
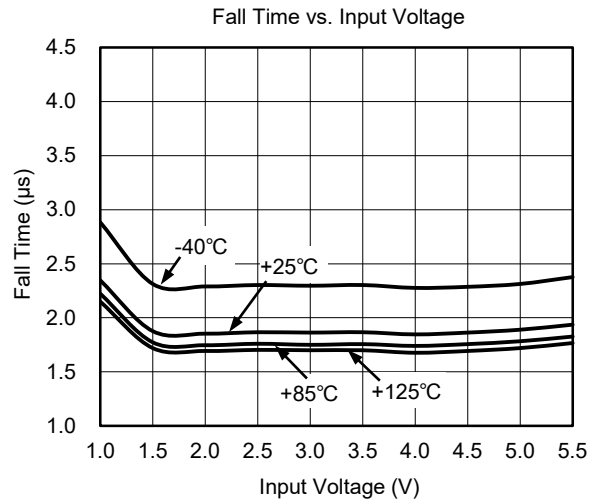
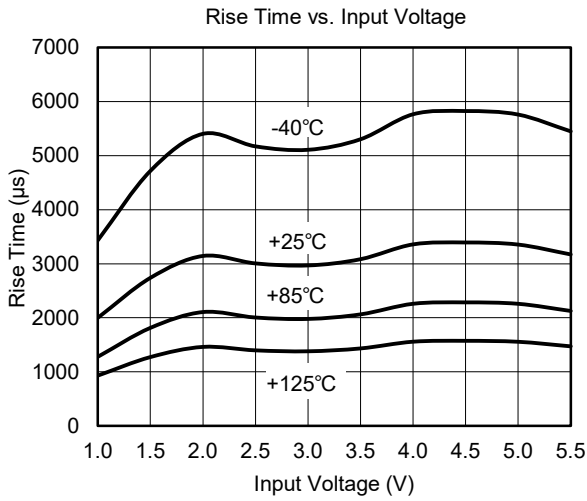
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

T_J = +25°C, C_{IN} = 1μF, C_{OUT} = 0.1μF, R_{OUT} = 10Ω, V_{IH} = 1.2V, V_{IL} = 0V, unless otherwise noted.



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

T_J = +25°C, C_{IN} = 1μF, C_{OUT} = 0.1μF, R_{OUT} = 10Ω, V_{IH} = 1.2V, V_{IL} = 0V, unless otherwise noted.



DETAILED DESCRIPTION

The SGM2567 is a small, 6-ball, 4A load switch. A low on-resistance N-MOSFET is integrated, which makes a low voltage drop across the device. To choose suitable rise time is always used to avoid inrush current.

Control Pin

The ON pin can control the device. Pulling the ON pin high enables the device. Logic high of V_{IH} on the ON pin will enable the device and V_{IL} will turn off it. It has the ability to interface with low-voltage GPIO. It can support with 1.8V GPIOs.

Quick Output Discharge

The quick output discharge (QOD) feature is available for SGM2567. If the ON pin is pulled low, a discharge resistor of 280 (TYP) is connected between V_{OUT} and GND pins to prevent the output from floating when the switch is disabled.

Table 1. V_{OUT} Connection

ON	V _{IN} to V _{OUT}	Output Discharge
L	Off	Active
H	On	Disabled

APPLICATION INFORMATION

SGM2567 is a single channel, up to 4A current capability load switch with low on-resistance. The device has a wide input range, which can be used in different end equipment to set power sequence, reduce inrush current and maintain low standby leakage current. The typical application circuit of SGM2567 is shown in Figure 4.

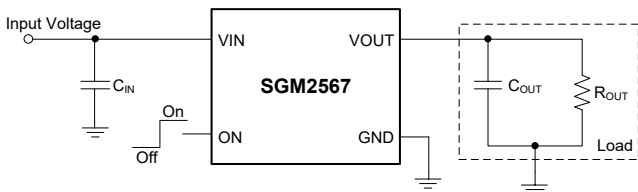


Figure 4. Typical Application Circuit

Design Requirements

Design Parameter	Value
Input Voltage (V _{IN})	3.3V
Load Capacitance (C _{OUT})	4.7μF
Maximum Acceptable Inrush Current (I _{INRUSH})	30mA

Inrush Current

When the switch is enabled, V_{OUT} begins to soft-start from 0V linearly. Inrush current can be calculated by the following formula.

$$I_{INRUSH} = C_{OUT} \times \frac{dV_{OUT}}{dt} \quad (1)$$

From the Equation 1, we can also calculate the soft-start time.

$$dt = C_{OUT} \times V_{OUT} / I_{INRUSH} \quad (2)$$

In this example: C_{OUT} = 4.7μF, V_{OUT} = V_{IN} = 3.3V, I_{INRUSH} = 30mA.

So,

$$dt = 4.7\mu F \times 3.3V / 30mA \approx 517\mu s \quad (3)$$

To ensure an inrush current is less than 30mA, the soft-start time cannot be less than 517μs. The SGM2567 has a typical rise time of 3600μs at 3.3V which meets the above design requirements.

Input Capacitor

A 1μF input capacitor (C_{IN}) is recommended to use between VIN and GND close to the device pins. It can limit the voltage drop on the input supply. Larger C_{IN} can reduce voltage dip in high current applications.

Output Capacitor

A 0.1μF output capacitor (C_{OUT}) should be placed between V_{OUT} and GND close to the device pins. This capacitor will prevent parasitic board inductances from forcing V_{OUT} below GND when the switch is turned off. To improve the V_{IN} dropping when the device is turned on, it is recommended that C_{IN} is placed greater than C_{OUT}, due to the C_{IN} is charge for C_{OUT}.

APPLICATION INFORMATION (continued)

Standby Power Reduction

In battery-powered equipment, the strict power budget must be met under different operating modes. In standby or sleep mode, leakage current of some modules such as LCD displays, Wi-Fi, power amplifiers and GPS may be up to several mA or more. The large consumption is far from meeting the application requirements. Using load switches ahead of these modules can reduce this leakage current to μA/nA level, which can save the standby power consumption greatly. The configuration is illustrated in Figure 5.

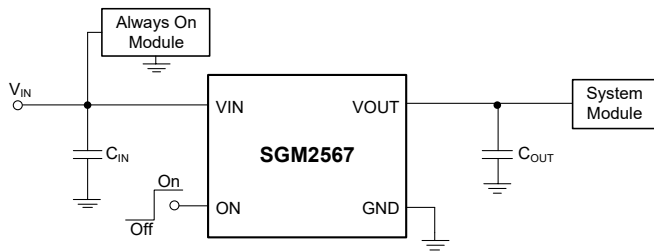
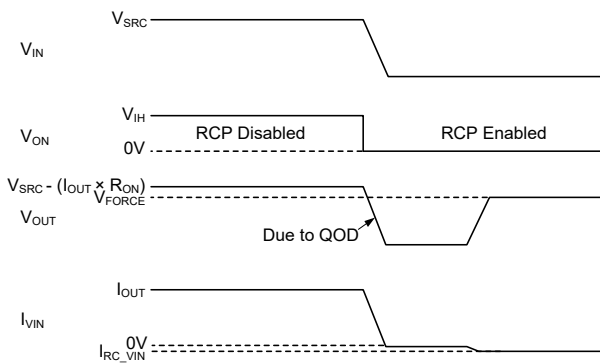


Figure 5. Standby Power Reduction

Reverse Current Protection

If the ON pin is pulled low, the device is disabled, while V_{IN} > 1V or V_{OUT} > 1V is met, the reverse current protection function is activated. This function prevents the current flowing from V_{OUT} to V_{IN}, and is very useful when SGM2567 is disabled and the output needs to be driven by another voltage source.



NOTES: V_{SRC} is the input power supply to the equipment. V_{FORCE} is the external power source forced at V_{OUT} pin. I_{VIN} is the current of VIN pin. I_{OUT} is output load current.

Figure 6. Reverse Current Protection

Figure 6 shows how the reverse current protection circuit is activated in SGM2567. Pulling the ON pin down, the device is shut down and an external voltage (V_{FORCE}) is forced to V_{OUT} pin, the reverse current is tested very small given by I_{RC_VIN}. This will prevent any large extra current reverse from the V_{FORCE} (added on V_{OUT}) to V_{IN}.

Power Supply Recommendations

The SGM2567 is designed for a wide operate input voltage range of 1V to 5.5V. Place a 1μF input bypass capacitor close to the device terminal is recommended.

Power Supply Sequencing without a GPIO Input

In many terminal devices, each module needs to be powered up in a pre-determined manner. SGM2567 can set a power sequence by the t_{DELAY} without extra GPIO, and may reduce inrush current. Figure 7 shows the sequence that the ON pin of first load switch is tied to the VIN, and the second load switch ON pin is tied to the V_{OUT} of first load switch. The second load switch is powered up when the first load switch is turned on, this is the fixed sequence and the delay time set by default t_{DELAY}

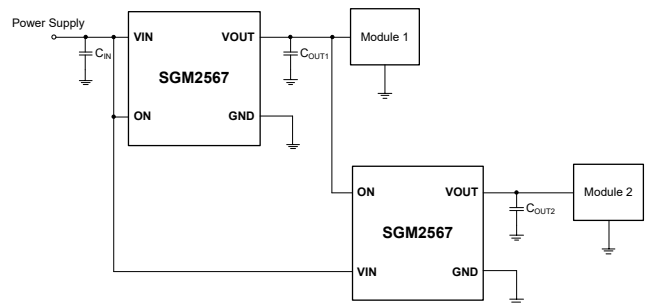


Figure 7. Power Supply Sequencing without a GPIO Input

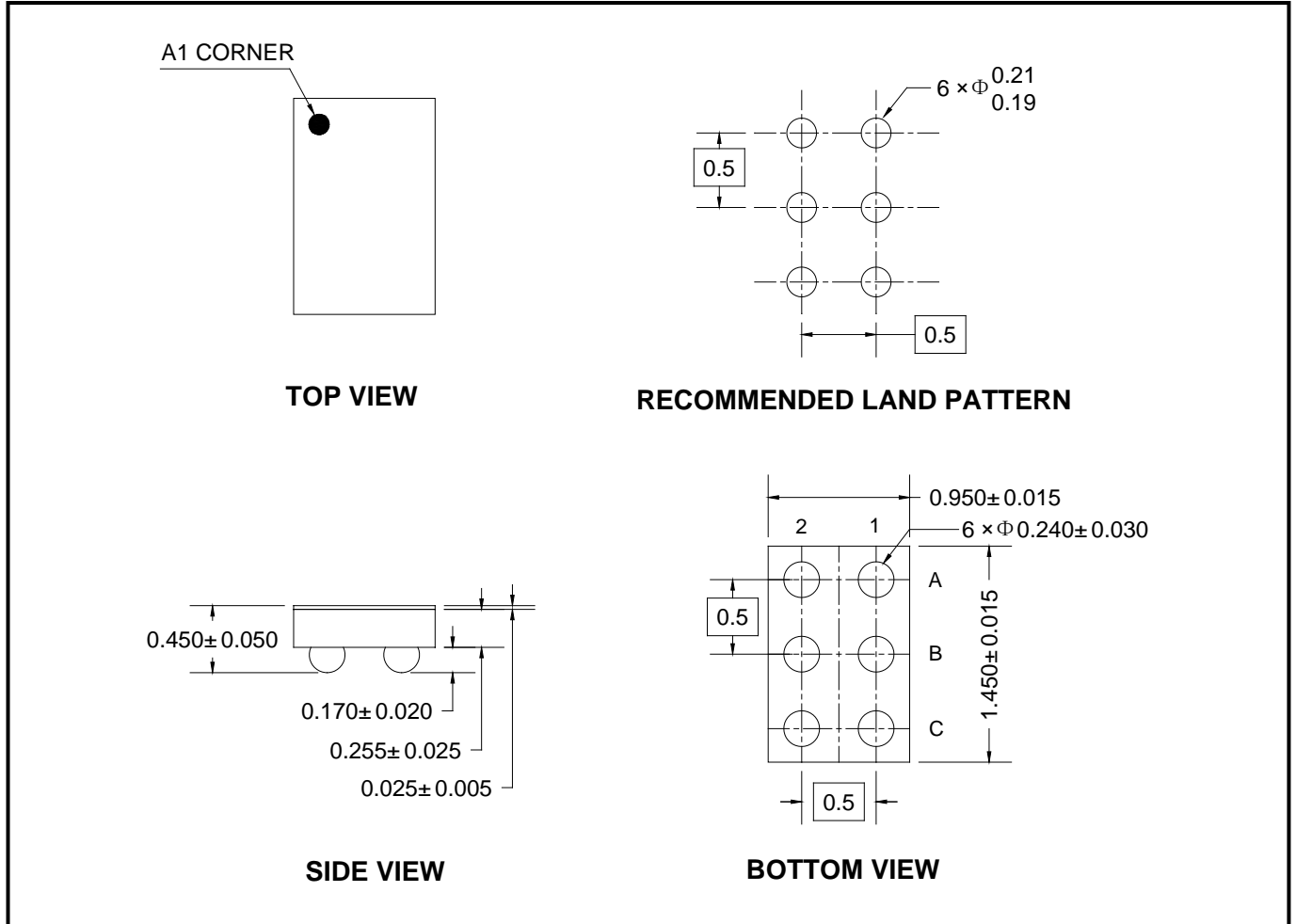
REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

MAY 2022 – REV.A.1 to REV.A.2		Page
Updated General Description and Features sections		1
Updated Detailed Description and Application Information sections.....		9, 10
OCTOMBER 2020 – REV.A to REV.A.1		Page
Updated Absolute Maximum Ratings section.....		2
Changes from Original (JUNE 2020) to REV.A		Page
Changed from product preview to production data.....		All

PACKAGE OUTLINE DIMENSIONS

WLCSP-1.45x0.95-6B



NOTE: All linear dimensions are in millimeters.

PACKAGE INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
WLCSP-1.45×0.95-6B	7"	9.0	1.12	1.57	0.62	4.0	4.0	2.0	8.0	Q1

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PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18

DD0002

单击下面可查看定价，库存，交付和生命周期等信息

[>>SGMICRO\(圣邦微电子\)](#)