

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

## Features

- -30V,-6.8A,  $RDS(ON) = 30m\Omega @ VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications
- Marking : IB

## Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

## Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

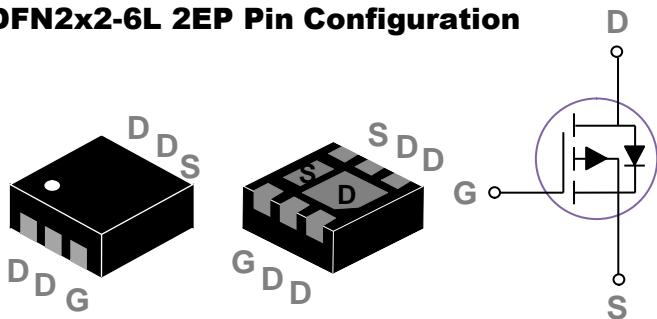
Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ C$ )	-6.8	A
	Drain Current – Continuous ( $T_A=70^\circ C$ )	-4.8	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-24.4	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	39.2	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	28	A
$P_D$	Power Dissipation ( $T_A=25^\circ C$ )	2.01	W
	Power Dissipation – Derate above $25^\circ C$	0.032	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$

BVDSS	RDS(on)	ID
-30V	30m $\Omega$	-6.8A

## DFN2x2-6L 2EP Pin Configuration



**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=-250\mu\text{A}$	-30	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_{\text{D}}=-1\text{mA}$	---	-0.03	---	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{\text{DS}}=-24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	-10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$ , $I_{\text{D}}=-4\text{A}$	---	25	30	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_{\text{D}}=-2\text{A}$	---	45	50	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=-250\mu\text{A}$	-1.2	-1.6	-2.5	V
			---	4	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$ , $I_{\text{D}}=-1\text{A}$	---	4	---	S

**Dynamic and switching Characteristics**

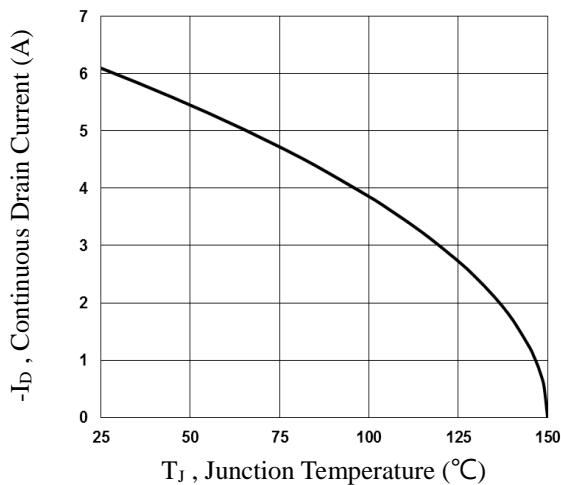
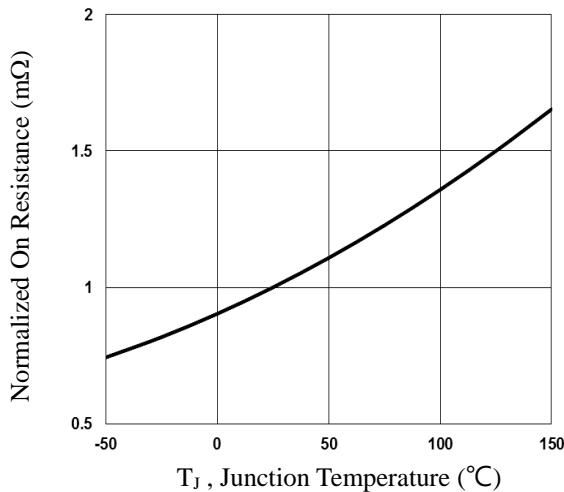
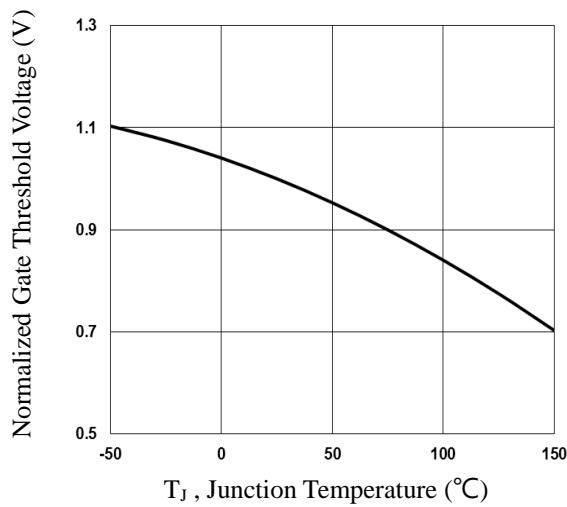
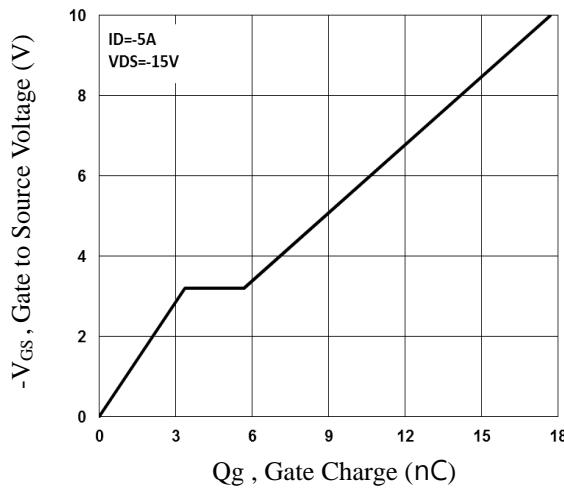
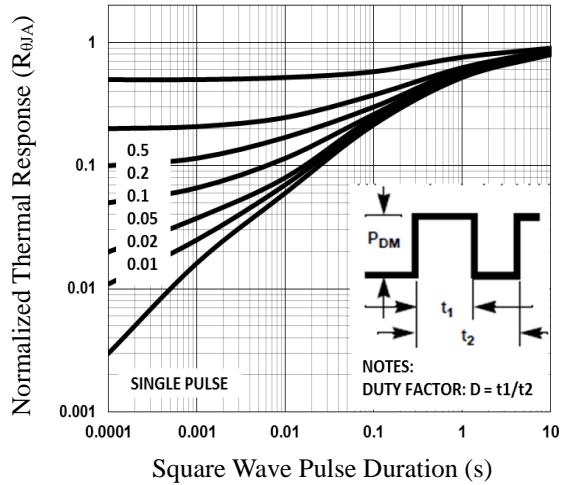
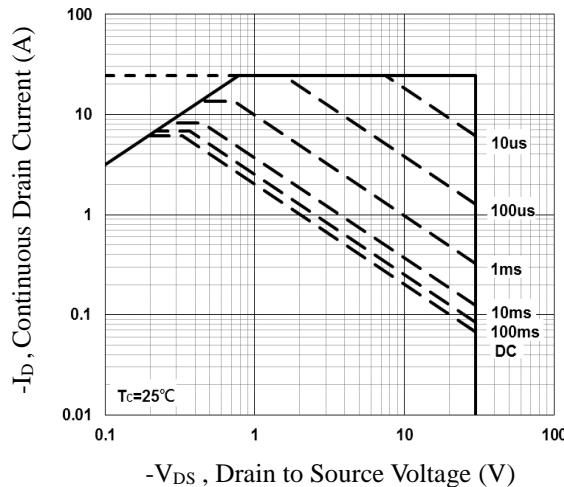
$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{\text{DS}}=-15\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_{\text{D}}=-5\text{A}$	---	8	15	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2,3</sup>		---	3.3	6	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2,3</sup>		---	2.3	5	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2,3</sup>	$V_{\text{DD}}=-15\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_{\text{G}}=6\Omega$ $I_{\text{D}}=-1\text{A}$	---	4.6	9	ns
$T_r$	Rise Time <sup>2,3</sup>		---	14	26	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2,3</sup>		---	34	58	
$T_f$	Fall Time <sup>2,3</sup>		---	18	35	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	757	1280	pF
$C_{\text{oss}}$	Output Capacitance		---	122	210	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	88	175	

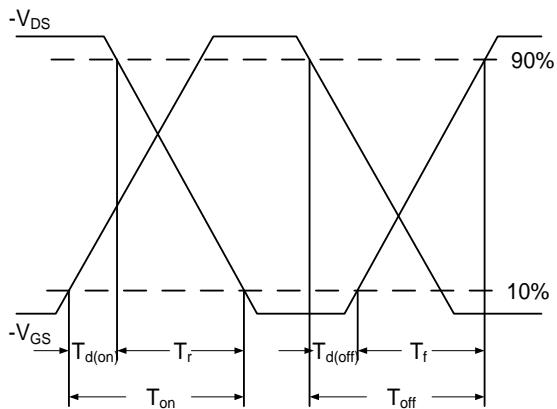
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-6.8	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-12.2	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{S}}=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

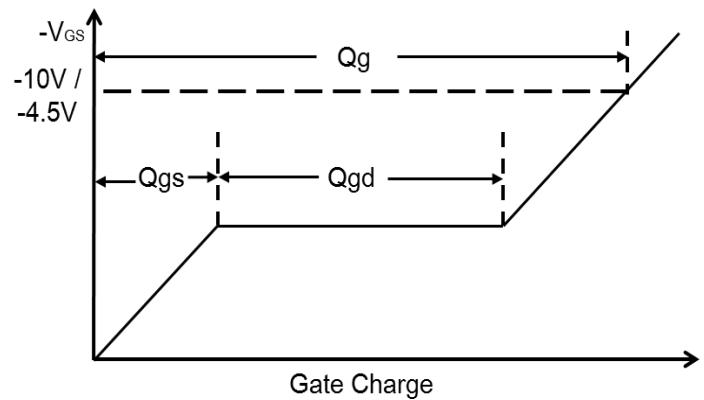
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_J$** 

**Fig.2 Normalized R<sub>DSON</sub> vs.  $T_J$** 

**Fig.3 Normalized  $V_{th}$  vs.  $T_J$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**

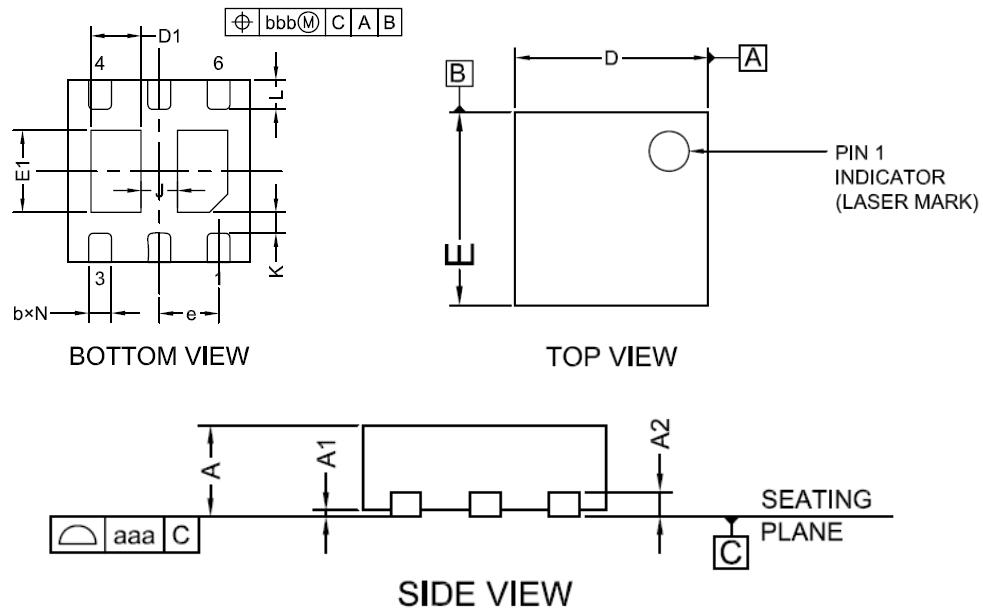


**Fig.7 Switching Time Waveform**



**Fig.8 Gate Charge Waveform**

## DFN2x2-6L 2EP PACKAGE INFORMATION



## COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2			0.203
b	0.20	0.25	0.30
D	1.95	2.00	2.05
D1	0.50	0.55	0.60
E	1.95	2.00	2.05
E1	0.85	0.90	0.95
e	0.65BSC		
L	0.27	0.32	0.37
J	0.40BSC		
K	0.20MIN		
N	6		
aaa	0.08		
bbb	0.10		

单击下面可查看定价，库存，交付和生命周期等信息

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