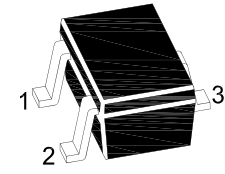
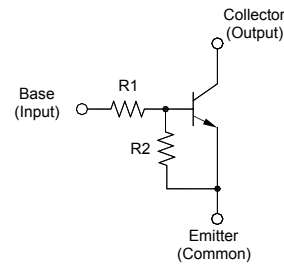


## NPN Silicon Epitaxial Planar Digital Transistor

### Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



1.Base 2.Emitter 3.Collector  
SOT-523 Plastic Package  
Marking Code: E23

### Absolute Maximum Ratings ( $T_a = 25\text{ }^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Collector Emitter Voltage	$V_{CEO}$	50	V
Input Voltage	$V_I$	- 5 to + 30	V
Collector Current	$I_C$	100	mA
Power Dissipation	$P_{tot}$	150	mW
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	- 55 to + 150	$^\circ\text{C}$

### Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $V_{CE} = 5\text{ V}$ , $I_C = 10\text{ mA}$	$h_{FE}$	80	-	-	-
Collector Base Cutoff Current at $V_{CB} = 50\text{ V}$	$I_{CBO}$	-	-	0.5	$\mu\text{A}$
Emitter Base Cutoff Current at $V_{EB} = 5\text{ V}$	$I_{EBO}$	-	-	1.8	mA
Collector Emitter Saturation Voltage at $I_C = 5\text{ mA}$ , $I_B = 0.25\text{ mA}$	$V_{CE(sat)}$	-	-	0.3	V
Input on Voltage at $V_{CE} = 0.3\text{ V}$ , $I_C = 5\text{ mA}$	$V_{I(on)}$	-	-	1.3	V
Input off Voltage at $V_{CE} = 5\text{ V}$ , $I_C = 100\text{ }\mu\text{A}$	$V_{I(off)}$	0.5	-	-	V
Transition frequency at $V_{CE} = 10\text{ V}$ , $-I_E = 5\text{ mA}$ , $f = 100\text{ MHz}$	$f_T$	-	250	-	MHz
Input Resistance	$R_1$	3.29	4.7	6.11	K $\Omega$
Resistance Ratio	$R_2 / R_1$	8	10	12	-

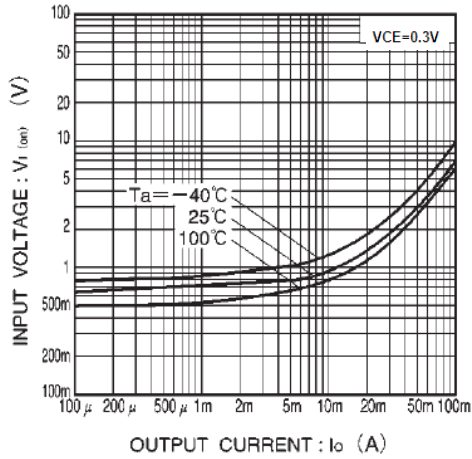


Fig.1 Input voltage vs. output current (ON characteristics)

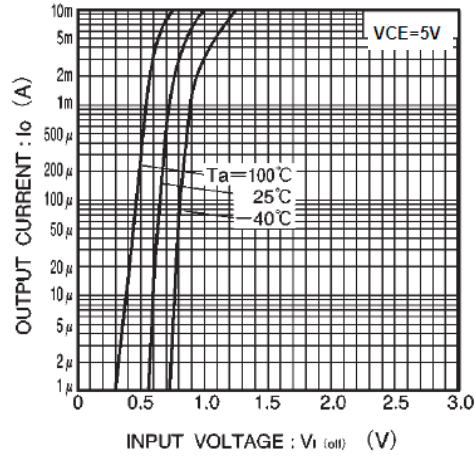


Fig.2 Output current vs. input voltage (OFF characteristics)

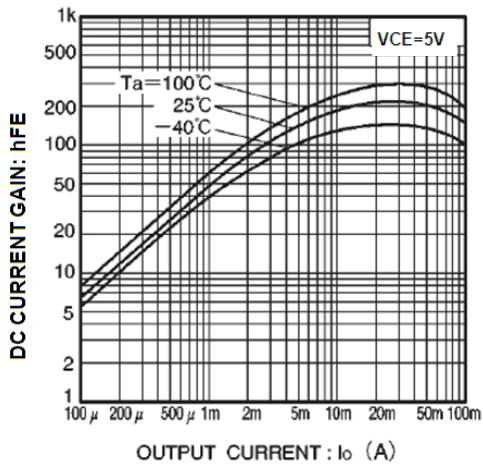


Fig.3 DC current gain vs. output current

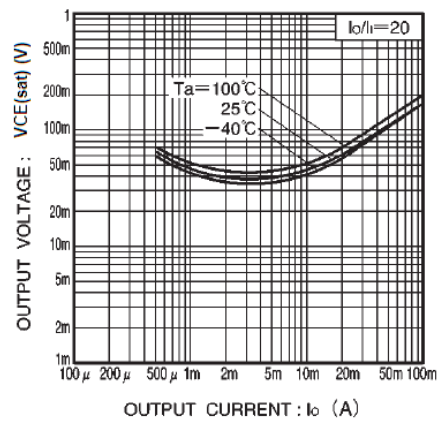
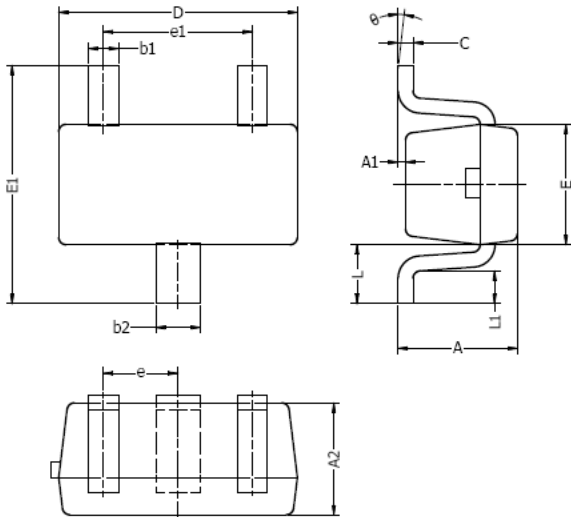


Fig.4 Output voltage vs. output current

## SOT-523 Package Outline

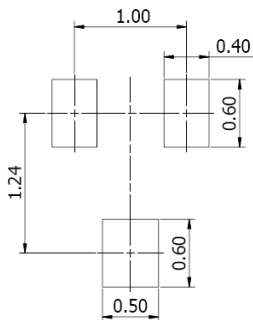


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.90	0.028	0.035
A1	0.00	0.10	0.000	0.004
A2	0.70	0.80	0.028	0.031
b1	0.15	0.25	0.006	0.010
b2	0.25	0.35	0.010	0.014
c	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
E1	1.45	1.75	0.057	0.069
e	0.50 TYP.		0.020 TYP.	
e1	0.90	1.10	0.035	0.043
L	0.40 REF.		0.016 REF.	
L1	0.10	0.30	0.004	0.012
$\theta$	0°	8°	0°	8°

**NOTES:**

1. Above package outline conforms to JEITA EAIJ ED-7500A SC-75A.
2. Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.

**Typical Soldering Pattern:**



单击下面可查看定价，库存，交付和生命周期等信息

[>>SHIKUES\(时科\)](#)