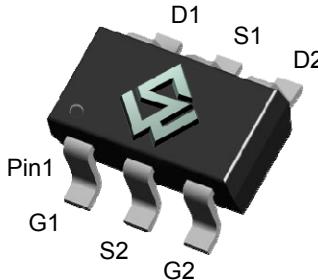


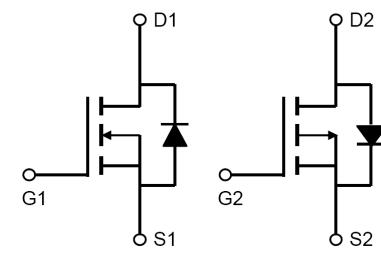
## 20V Complementary Enhancement-Mode MOSFET

<b>General Description</b>	<b>Product Summary</b>	
<ul style="list-style-type: none"> <li>• Low gate charge.</li> <li>• Use as a load switch.</li> <li>• Use in PWM applications</li> </ul>	N-Channel <ul style="list-style-type: none"> <li>• <math>BV_{DSS} = 20V</math></li> <li>• <math>R_{DS(on)} (@VGS= 10V) &lt; 42m\Omega</math></li> <li>• <math>R_{DS(on)} (@VGS= 4.5V) &lt; 45m\Omega</math></li> </ul>	P-Channel <ul style="list-style-type: none"> <li>• <math>BV_{DSS} = -20V</math></li> <li>• <math>R_{DS(on)} (@VGS= -10V) &lt; 42m\Omega</math></li> <li>• <math>R_{DS(on)} (@VGS= -4.5V) &lt; 45m\Omega</math></li> </ul>

**SOT23-6L**





N-Channel      P-Channel

<b>Absolute Maximum Ratings (<math>T_A = 25^\circ C</math> unless otherwise noted)</b>				
<b>Parameter</b>	<b>Symbol</b>	<b>Maximum</b>		<b>Units</b>
		<b>N-Channel</b>	<b>P-Channel</b>	
Drain-Source Voltage	$V_{DS}$	20	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	$\pm 12$	V
Drain Current ( $T_A=25^\circ C$ )	$I_D$	3.8	-4.2	A
Drain Current ( $T_A=75^\circ C$ )		2.3	-2.5	A
Pulsed Drain Current <sup>a</sup>	$I_{DM}$	10	-12	A
Power Dissipation <sup>b</sup> ( $T_A=25^\circ C$ )	$P_D$	1.25	1.25	W
Power Dissipation <sup>b</sup> ( $T_A=75^\circ C$ )		0.75	0.75	W
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 ~ +150	-55 ~ +150	°C

<b>Thermal Characteristics</b>				
<b>Parameter</b>	<b>Symbol</b>	<b>Maximum</b>		<b>Units</b>
		<b>N-Channel</b>	<b>P-Channel</b>	
Junction-to-Ambient <sup>a</sup> ( $t \leq 10s$ )	$R_{\theta JA}$	100	100	°C/W
Junction-to-Ambient <sup>a,d</sup> (Steady-State)		130	130	°C/W
Junction-to-Lead (Steady-State)	$R_{\theta JL}$	90	90	°C/W

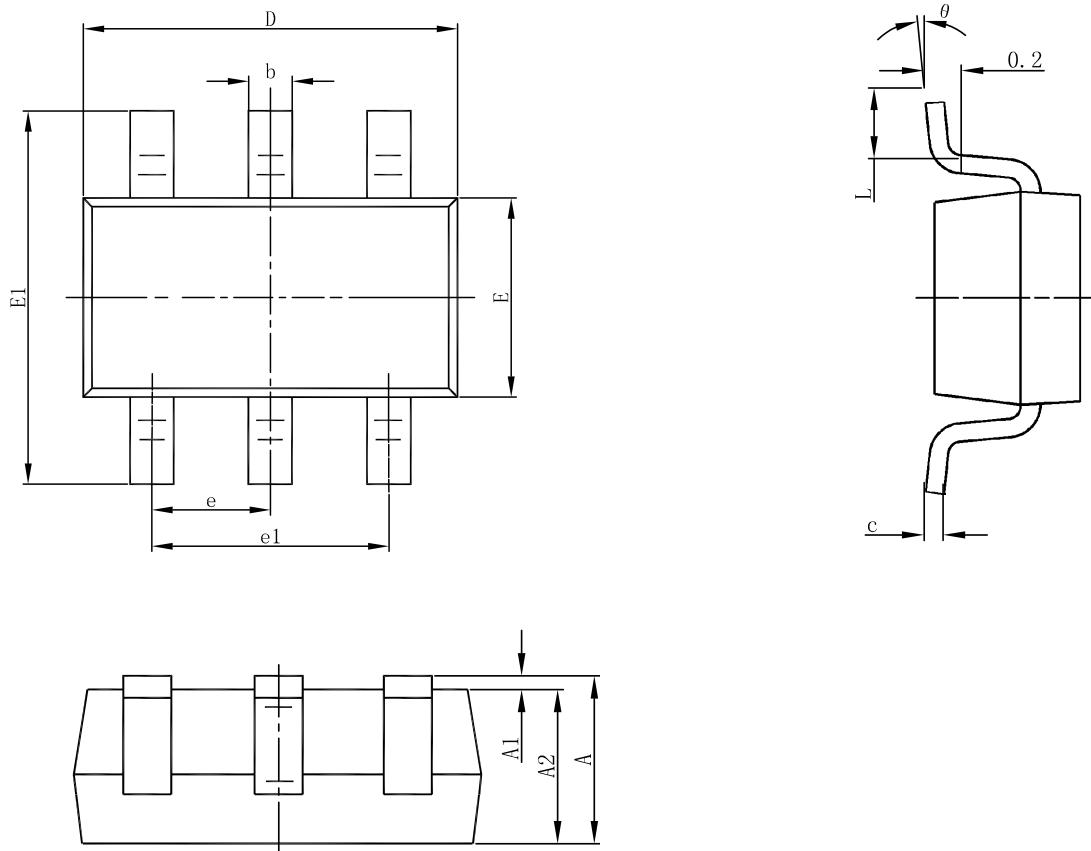
<b>N-Channel Electrical Characteristics (<math>T_A = 25^\circ\text{C}</math> unless otherwise noted)</b>						
<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Units</b>
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}$ , $I_D = 250\mu\text{A}$	20			V
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 20\text{V}$ , $V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}} = \pm 12\text{V}$ , $V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics</b>						
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = 250\mu\text{A}$	0.4		1.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}} = 10\text{V}$ , $I_D = 3.0\text{A}$		32	42	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}$ , $I_D = 2.5\text{A}$		35	45	$\text{m}\Omega$
$\text{g}_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 10\text{V}$ , $I_D = 3.0\text{A}$		15		S
<b>Drain-Source Diode Characteristics</b>						
$\text{V}_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}} = 0\text{V}$ , $I_S = 1.0\text{A}$			1.2	V
$I_S$	Maximum Body-Diode Continuous Current				2.0	A
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = 10\text{V}$ , $V_{\text{GS}} = 0\text{V}$ $f = 1.0\text{MHz}$		182		pF
$C_{\text{oss}}$	Output Capacitance			38		pF
$C_{\text{rss}}$	Reverse Transfer Capacitance			35		pF
<b>Switching Characteristics</b>						
$Q_g$	Total Gate Charge	$V_{\text{DS}} = 10\text{V}$ , $I_D = 3.0\text{A}$ $V_{\text{GS}} = 5\text{V}$		6.2		nC
$Q_{\text{gs}}$	Gate-Source Charge			6.1		nC
$Q_{\text{gd}}$	Gate-Drain Charge			0.6		nC
$t_{\text{D(ON)}}$	Turn-On Delay Time	$V_{\text{DD}} = 10\text{V}$ , $I_D = 3\text{A}$ $V_{\text{GS}} = 5\text{V}$ $R_{\text{GEN}} = 6 \text{ ohm}$		4.6		ns
$t_r$	Turn-On Rise Time			30		ns
$t_{\text{D(OFF)}}$	Turn-Off Delay Time			12		ns
$t_f$	Turn-Off Fall Time			4.2		ns

- a. Repetitive rating, Pulse width limited by junction temperature  $T_{\text{J(MAX)}}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_{\text{J}}=25^\circ\text{C}$
- b. The power dissipation  $P_D$  is based on  $T_{\text{J(MAX)}}=150^\circ\text{C}$ , using  $\leq 10\text{s}$  junction-to-ambient thermal resistance.
- c. The value of  $R_{\theta_{JA}}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ . The value in any given application depends on the user's specific board design.
- d. The  $R_{\theta_{JA}}$  is the sum of the thermal impedance from junction to lead  $R_{\theta_{JL}}$  and lead to ambient.

<b>P-Channel Electrical Characteristics (<math>T_A = 25^\circ\text{C}</math> unless otherwise noted)</b>						
<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Units</b>
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}$ , $I_D = -250\mu\text{A}$	-20			V
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = -20\text{V}$ , $V_{\text{GS}} = 0\text{V}$			-1	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}} = \pm 12\text{V}$ , $V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics</b>						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = -250\mu\text{A}$	-0.4		-1.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}} = -10\text{V}$ , $I_D = -3.0\text{A}$		30	42	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}$ , $I_D = -2.5\text{A}$		32	45	$\text{m}\Omega$
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = -10\text{V}$ , $I_D = -3.0\text{A}$		24		S
<b>Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}} = 0\text{V}$ , $I_S = -1.0\text{A}$			-1.2	V
$I_S$	Maximum Body-Diode Continuous Current				-2.0	A
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = -10\text{V}$ , $V_{\text{GS}} = 0\text{V}$ $f = 1.0\text{MHz}$		608		pF
$C_{\text{oss}}$	Output Capacitance			112		pF
$C_{\text{rss}}$	Reverse Transfer Capacitance			105		pF
<b>Switching Characteristics</b>						
$Q_g$	Total Gate Charge	$V_{\text{DS}} = -10\text{V}$ , $I_D = -3.8\text{A}$ $V_{\text{GS}} = -4.5\text{V}$		8.52		nC
$Q_{\text{gs}}$	Gate-Source Charge			1.56		nC
$Q_{\text{gd}}$	Gate-Drain Charge			2.64		nC
$t_{\text{D}(\text{ON})}$	Turn-On Delay Time	$V_{\text{DD}} = -10\text{V}$ , $I_D = -3.8\text{A}$ $V_{\text{GS}} = -4.5\text{V}$ $R_{\text{GEN}} = 3 \text{ ohm}$		5.7		ns
$t_r$	Turn-On Rise Time			35.2		ns
$t_{\text{D}(\text{OFF})}$	Turn-Off Delay Time			52.1		ns
$t_f$	Turn-Off Fall Time			53		ns

- a. Repetitive rating, Pulse width limited by junction temperature  $T_{\text{J}(\text{MAX})}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_{\text{J}}=25^\circ\text{C}$
- b. The power dissipation  $P_{\text{D}}$  is based on  $T_{\text{J}(\text{MAX})}=150^\circ\text{C}$ , using  $\leq 10\text{s}$  junction-to-ambient thermal resistance.
- c. The value of  $R_{\theta_{\text{JA}}}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ . The value in any given application depends on the user's specific board design.
- d. The  $R_{\theta_{\text{JA}}}$  is the sum of the thermal impedance from junction to lead  $R_{\theta_{\text{JL}}}$  and lead to ambient.

## SOT23-6L Package Outline



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

单击下面可查看定价，库存，交付和生命周期等信息

>>[SiliconWisdom\(矽睿半导体\)](#)