SWR80N06

60V Single N-Channel Enhancement-Mode MOSFET

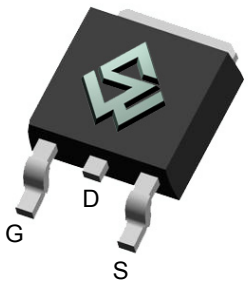
General Description

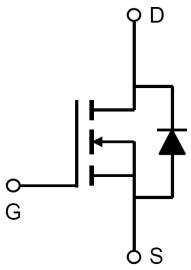
- Low gate charge.
- Use as a load switch.
- Use in PWM applications

Product Summary

- BV_{DSS} 60V
- $R_{DS(on)}$ @VGS = 10V < 7mΩ
- $R_{DS(on)}$ @VGS = 4.5V < 12mΩ

TO-252 D-PAK





Absolute Maximum Ratings (T_A = 25°C unless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current (T_C=25ºC)	I_D	80	A
Drain Current (T_C=75ºC)		52	
Drain Current (T_A=25ºC)		50	
Drain Current (T_A=75ºC)		30	
Pulsed Drain Current [a]	I_{DM}	320	A
Single Pulsed Avalanche Energy[e]	EAS	169	mJ
Power Dissipation [b] (T_C=25ºC)	P_D	105	W
Power Dissipation [b] (T_A=25ºC)		2.5	W
Junction and Storage Temperature Range	T_J, T_{STG}	-55 ~ +150	ºC

Thermal Characteristics

Parameter	Symbol	Maximum	Units
Junction-to-Ambient [a] (t ≤ 10s)	$R_{\theta JA}$	35	ºC/W
Junction-to-Ambient [a,d] (Steady-State)		75	ºC/W
Junction-to-Lead (Steady-State)	$R_{\theta JL}$	7.5	ºC/W

Electrical Characteristics (T_A = 25°C unless otherwise noted)						
Symbol	**Parameter**	**Conditions**	**Min**	**Typ**	**Max**	**Units**
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0V , I_D = 250uA	60			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 60V , V_{GS} = 0V			1	uA
I_{GSS}	Gate-Body Leakage Current	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS} , I_D = 250uA	1		3	V
$R_{DS(ON))}$	Drain-Source On-State Resistance	V_{GS} = 10V , I_D = 30A		5.5	7	mΩ
		V_{GS} = 4.5V , I_D = 25A		8	12	mΩ
g_{FS}	Forward Transconductance	V_{DS} = 10V , I_D = 30A		50		S
Drain-Source Diode Characteristics						
V_{SD}	Diode Forward Voltage	V_{GS} = 0V , I_S = 1.0A			1.3	V
I_S	Maximum Body-Diode Continuous Current				80	A
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 30V , V_{GS} = 0V f = 1.0MHz		4226		pF
C_{oss}	Output Capacitance			280		pF
C_{rss}	Reverse Transfer Capacitance			236		pF
Switching Characteristics						
Q_g	Total Gate Charge	V_{DS} = 30V , I_D = 30A V_{GS} = 10V		90		nC
Q_{gs}	Gate-Source Charge			10		nC
Q_{gd}	Gate-Drain Charge			18		nC
$t_{D(ON)}$	Turn-On Delay Time	V_{DD} = 30V , ID = 30A V_{GS} = 10 V R_{GEN} = 1.8 ohm		9		ns
t_r	Turn-On Rise Time			7.6		ns
$t_{D(OFF)}$	Turn-Off Delay Time			42		ns
t_f	Turn-Off Fall Time			16		ns

a. Repetitive rating, Pulse width limited by junction temperature $T_{J(MAX)}$=150 ºC. Ratings are based on low frequency and duty cycles to keep initial T_J=25 ºC

b. The power dissipation P_D is based on $T_{J(MAX)}$=150 ºC , using≤10s junction-to-ambient thermal resistance.

c. The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A = 25°C. The value in any given application depends on the user's specific board design.

d. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

e. EAS condition: T_J=25 ºC, V_{DD}=30V, V_G=10V, L=0.5mH, Rg=25Ω, I_{AS}=26A

Typical Characteristics

Figure1: Output Characteristics

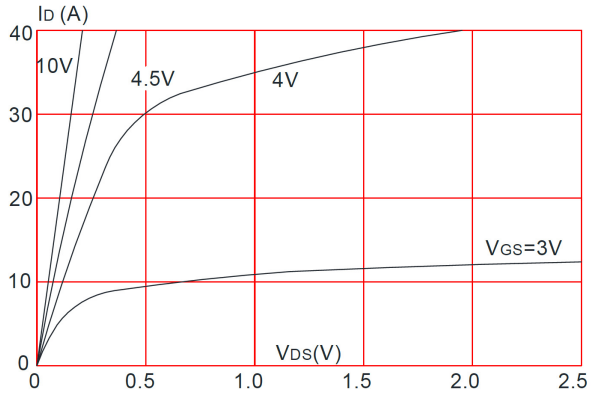


Figure 2: Typical Transfer Characteristics

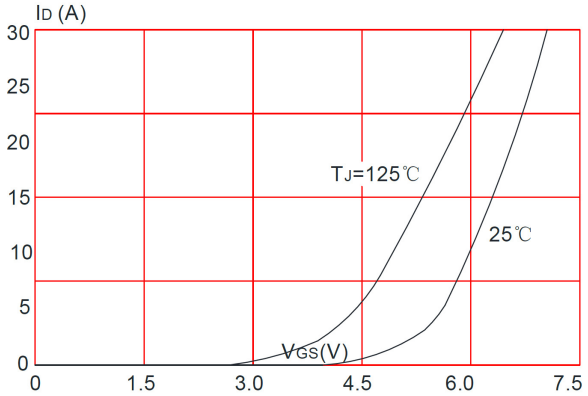


Figure 3: On-resistance vs. Drain Current

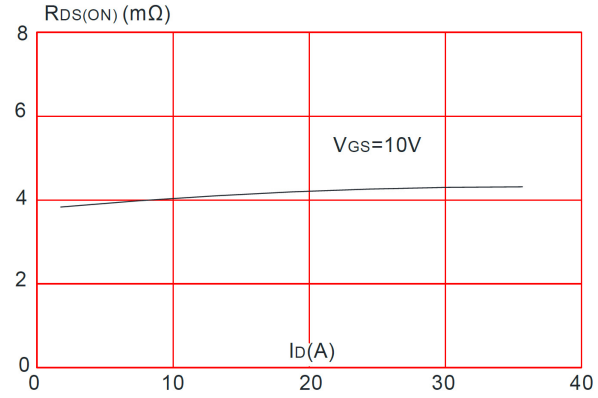


Figure 4: Body Diode Characteristics

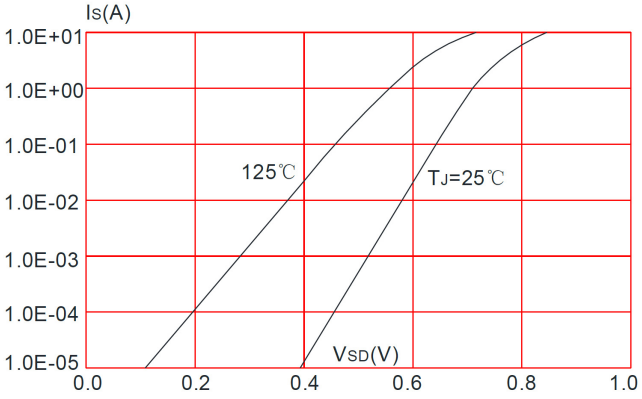


Figure 5: Gate Charge Characteristics

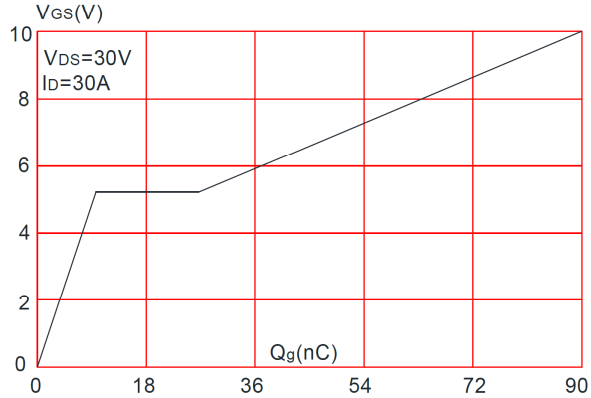
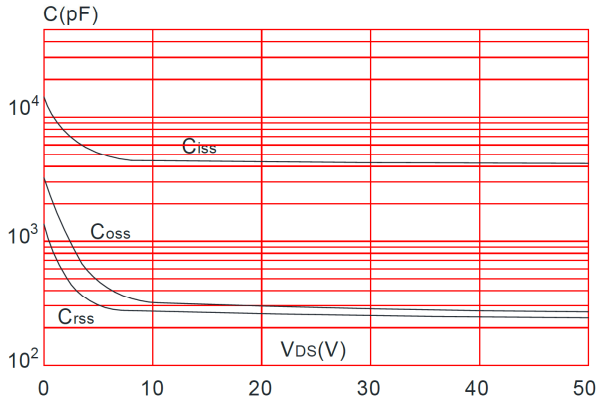


Figure 6: Capacitance Characteristics



Typical Characteristics

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

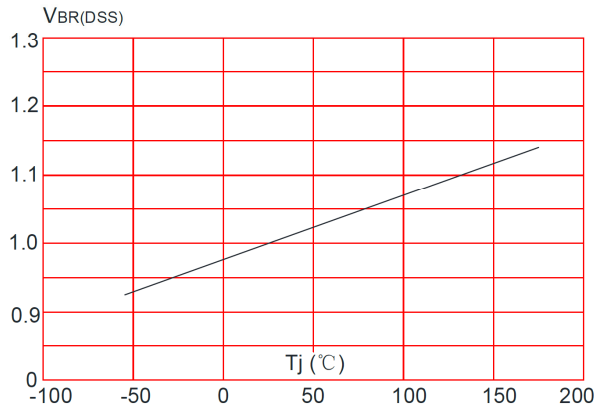


Figure 8: Normalized on Resistance vs. Junction Temperature

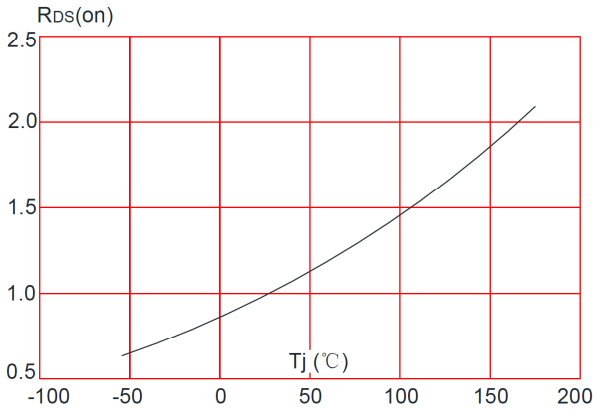


Figure 9: Maximum Safe Operating Area

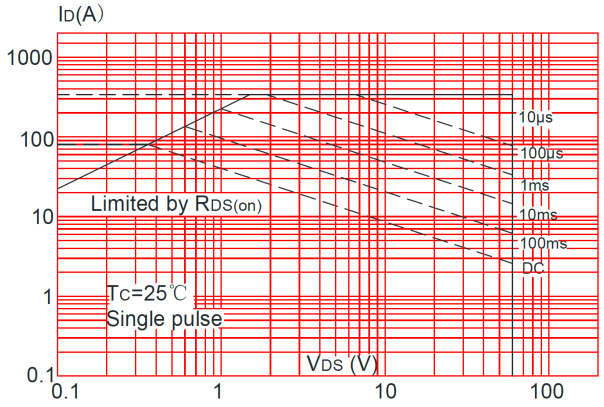


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

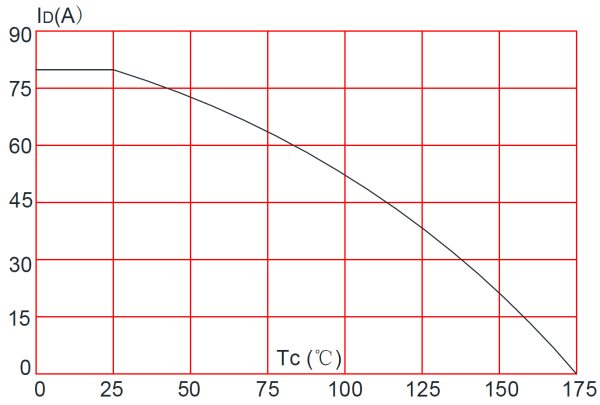
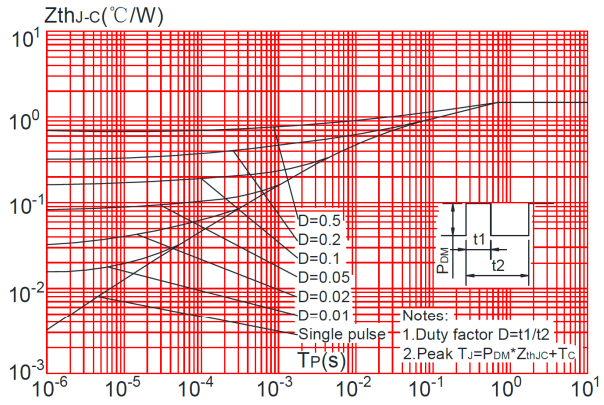
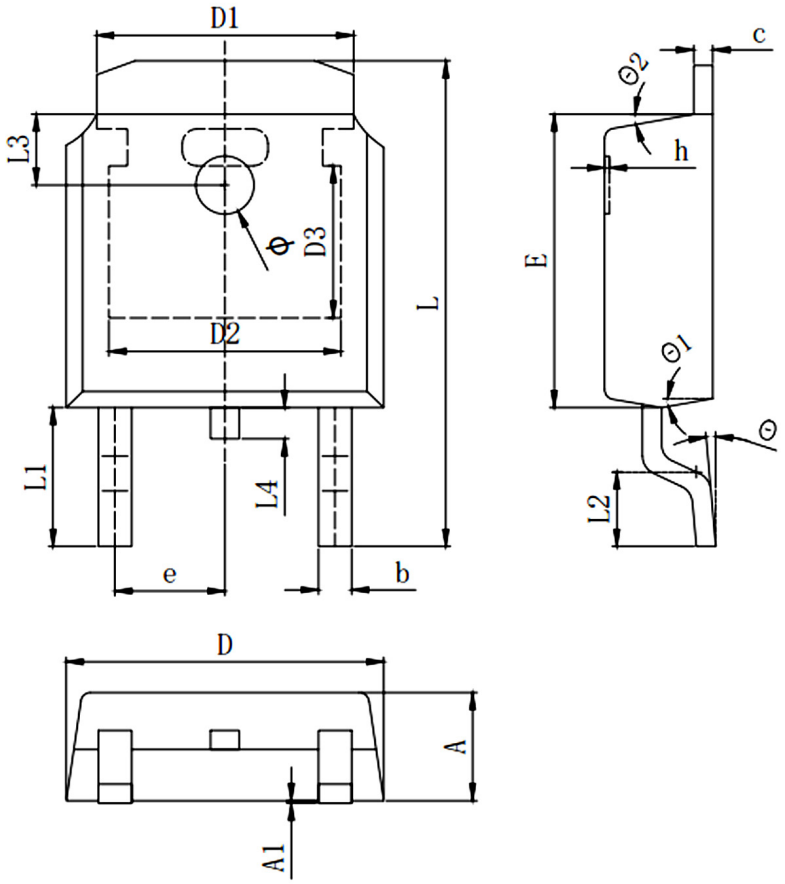


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



TO-252 D-PAK Package



Symbols	Millimeters		
	MIN.	Mom.	MAX.
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c(电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ1	9° TYP		
θ2	9° TYP		

单击下面可查看定价，库存，交付和生命周期等信息

>>SiliconWisdom(矽睿半导体)