

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
V_{DSS}	Drain-Source Voltage	-30	V	
V_{GSS}	Gate-Source Voltage	± 25		
I_D^a	Continuous Drain Current ($V_{GS}=-10V$)	$T_A=25^\circ\text{C}$	-11	A
		$T_A=70^\circ\text{C}$	-8.5	
I_{DM}^a	300 μs Pulsed Drain Current ($V_{GS}=-10V$)	-80		
I_D^d	Continuous Drain Current ($V_{GS}=-10V$)	$T_C=25^\circ\text{C}$	-33	
		$T_C=100^\circ\text{C}$	-21	
I_S^a	Diode Continuous Forward Current	-3		
I_{AS}^b	Avalanche Current, Single pulse ($L=0.1\text{mH}$)	-24		
E_{AS}^b	Avalanche Energy, Single pulse ($L=0.1\text{mH}$)	29	mJ	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150		
P_D^a	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	3.1	W
		$T_A=70^\circ\text{C}$	2	
P_D^d	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	29	
		$T_C=100^\circ\text{C}$	12	
$R_{\theta JA}^{a,c}$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	40	$^\circ\text{C/W}$
		Steady State	75	
$R_{\theta JC}^d$	Thermal Resistance-Junction to Case	Steady State	4.2	

Note a : Surface Mounted on 1in^2 pad area, $t \leq 10\text{sec}$.

Note b : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

Note c : Maximum under Steady State conditions is 75°C/W .

Note d : The power dissipation P_D is based on $T_{J(MAX)} = 150^\circ\text{C}$, and it is useful for reducing junction-to-case thermal resistance ($R_{\theta JC}$) when additional heat sink is used.

Electrical Characteristics (T_A = 25°C Unless Otherwise Noted)

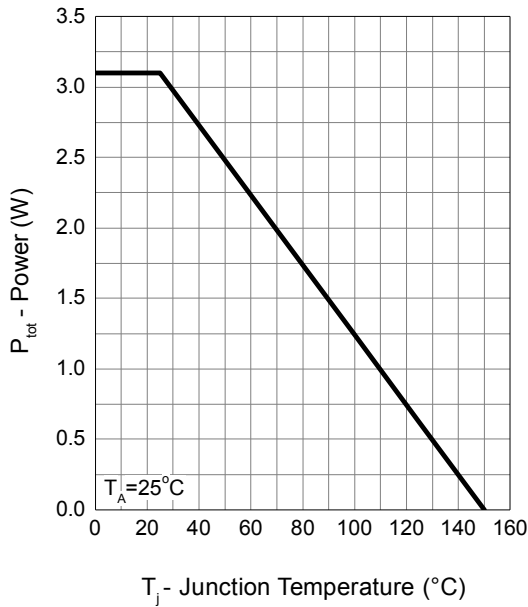
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
		T _J =85°C	-	-	-30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-1.5	-2	-2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±25V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-11A	-	14	18	mΩ
		V _{GS} =-4.5V, I _{DS} =-4A	-	22	30	
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =-1A, V _{GS} =0V	-	-0.7	-1	V
t _{rr} ^f	Reverse Recovery Time	I _{SD} =-11A, di _{SD} /dt=100A/μs	-	19	-	ns
Q _{rr} ^f	Reverse Recovery Charge		-	10	-	nC
Dynamic Characteristics^f						
R _g	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	3	-	Ω
C _{iSS}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, Frequency=1.0MHz	-	1000	-	pF
C _{oSS}	Output Capacitance		-	210	-	
C _{rSS}	Reverse Transfer Capacitance		-	150	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-15V, R _L =15Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω	-	8	-	ns
t _r	Turn-on Rise Time		-	12	-	
t _{d(OFF)}	Turn-off Delay Time		-	32	-	
t _f	Turn-off Fall Time		-	16	-	
Gate Charge Characteristics^f						
Q _g	Total Gate Charge	V _{DS} =-15V, V _{GS} =-10V, I _{DS} =-11A	-	21	-	nC
Q _{gs}	Gate-Source Charge		-	2.6	-	
Q _{gd}	Gate-Drain Charge		-	6.2	-	

Note e : Pulse test ; pulse width≤300μs, duty cycle≤2%.

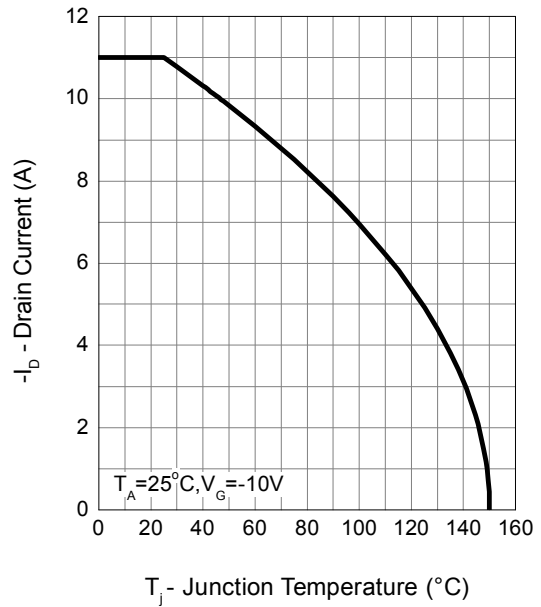
Note f : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

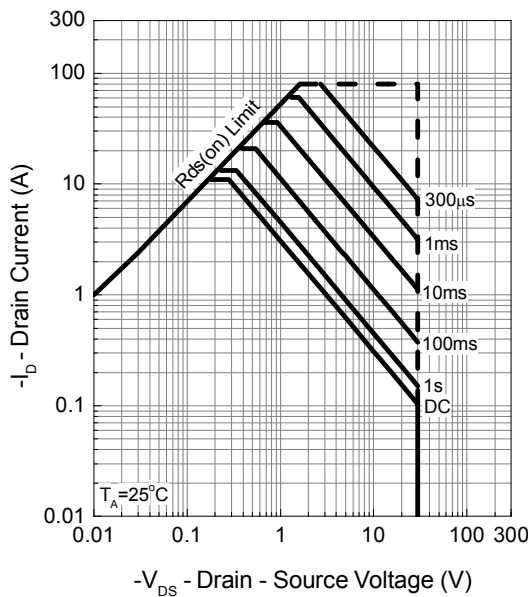
Power Dissipation



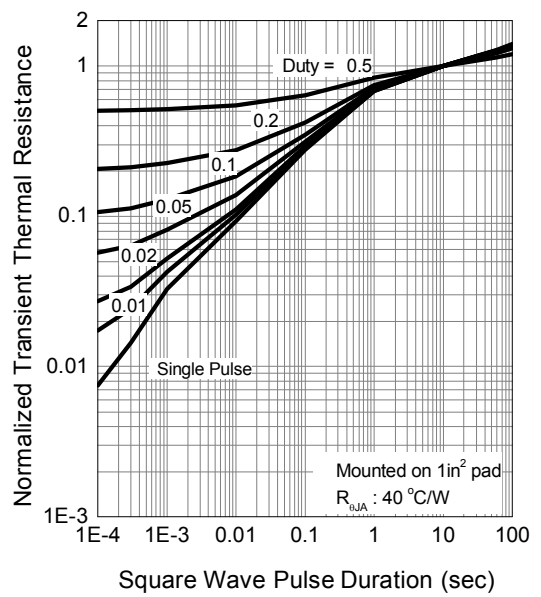
Drain Current



Safe Operation Area

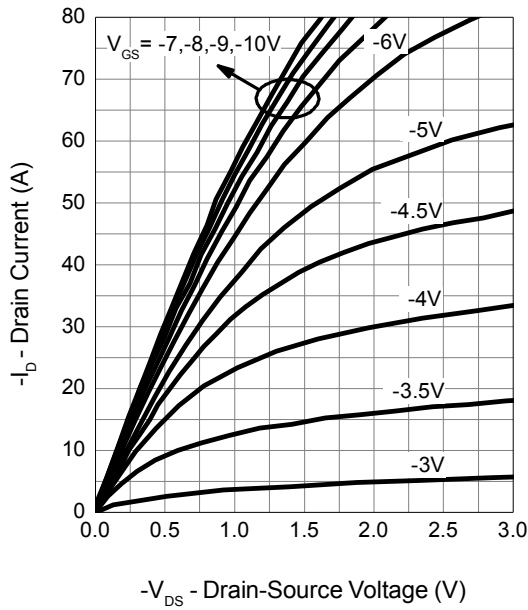


Thermal Transient Impedance

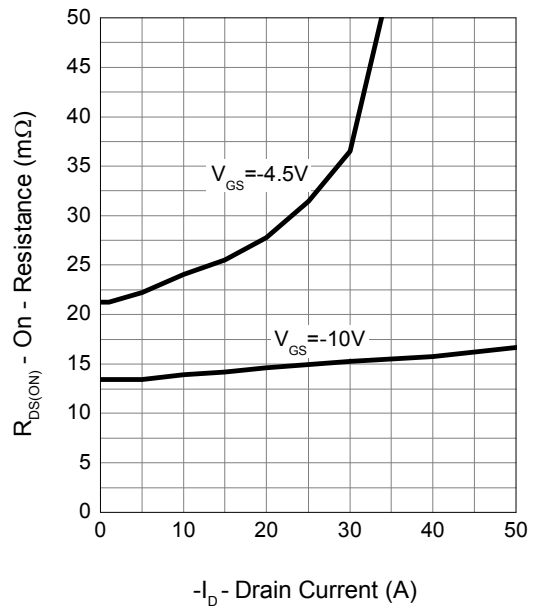


Typical Operating Characteristics (Cont.)

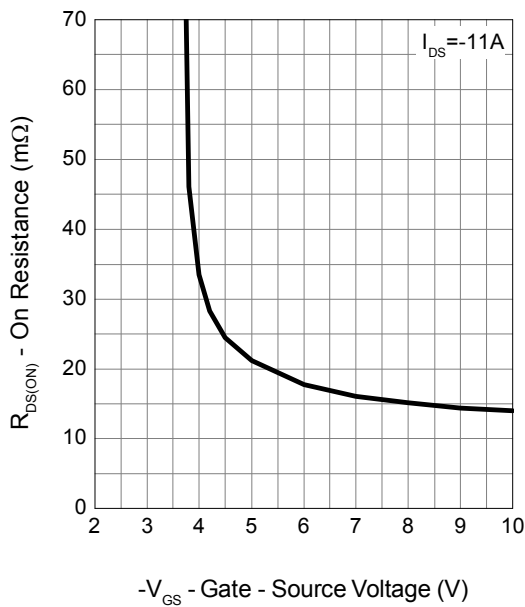
Output Characteristics



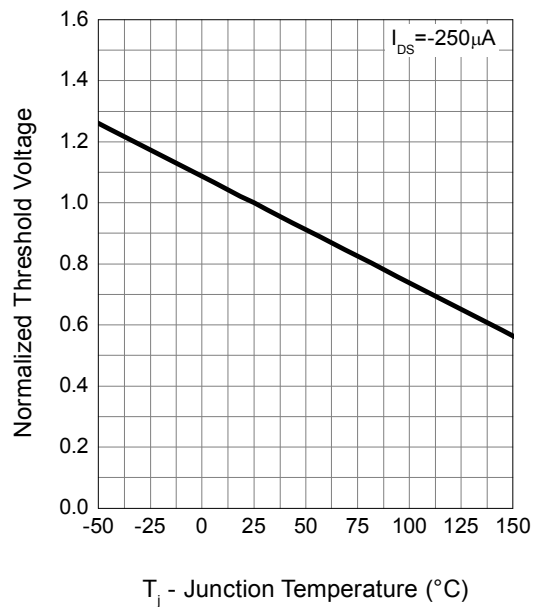
Drain-Source On Resistance



Gate-Source On Resistance

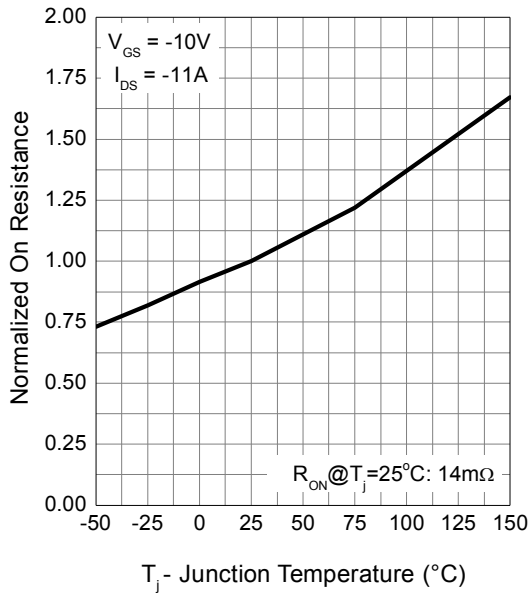


Gate Threshold Voltage

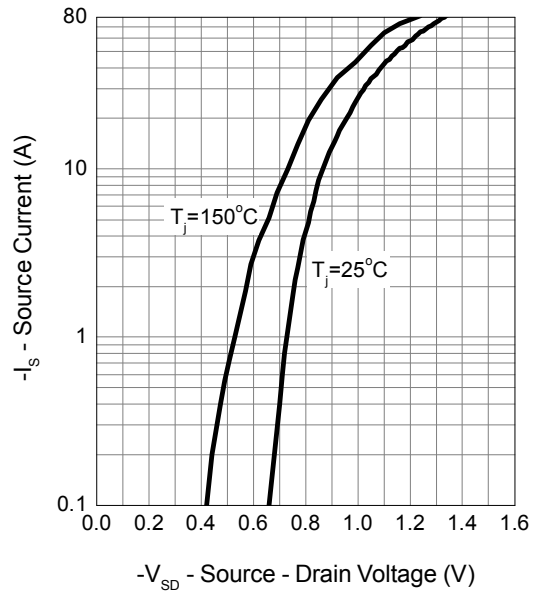


Typical Operating Characteristics (Cont.)

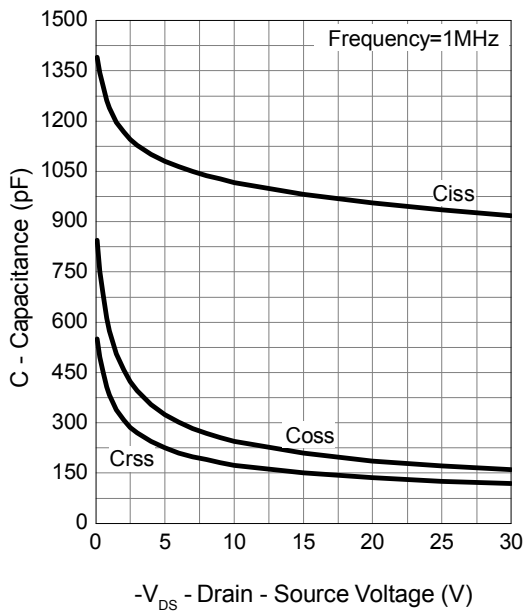
Drain-Source On Resistance



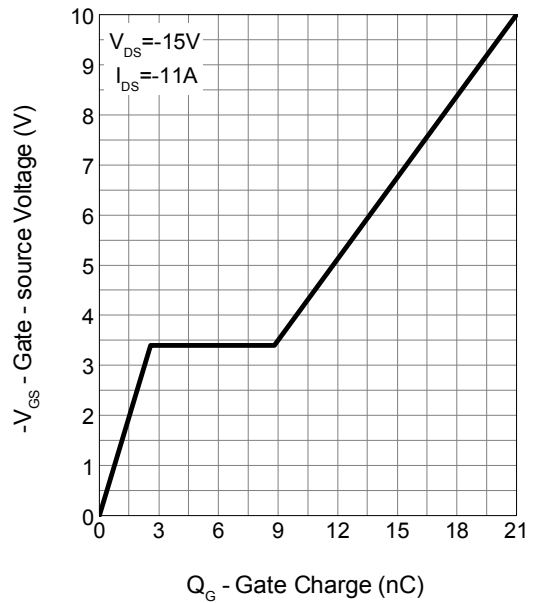
Source-Drain Diode Forward



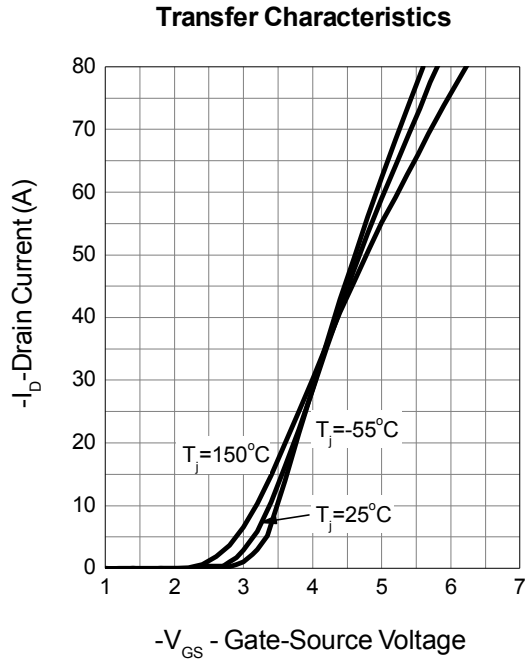
Capacitance



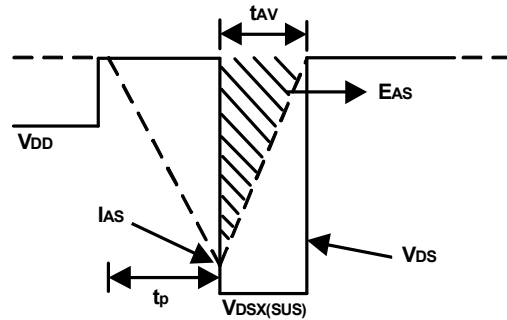
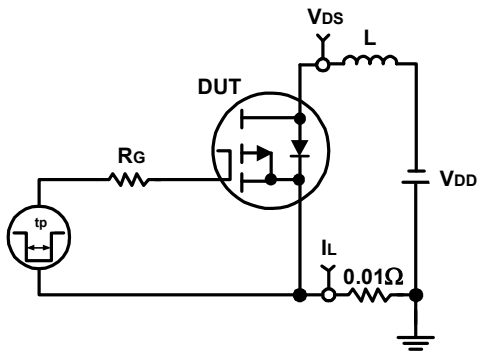
Gate Charge



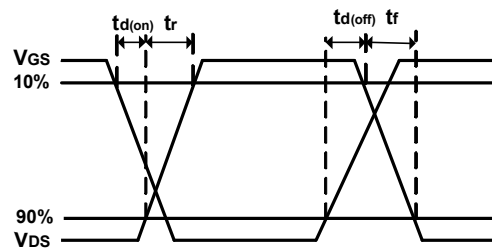
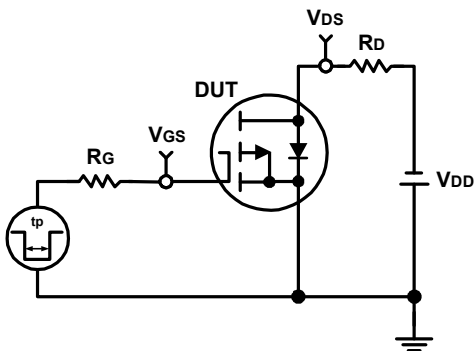
Typical Operating Characteristics (Cont.)



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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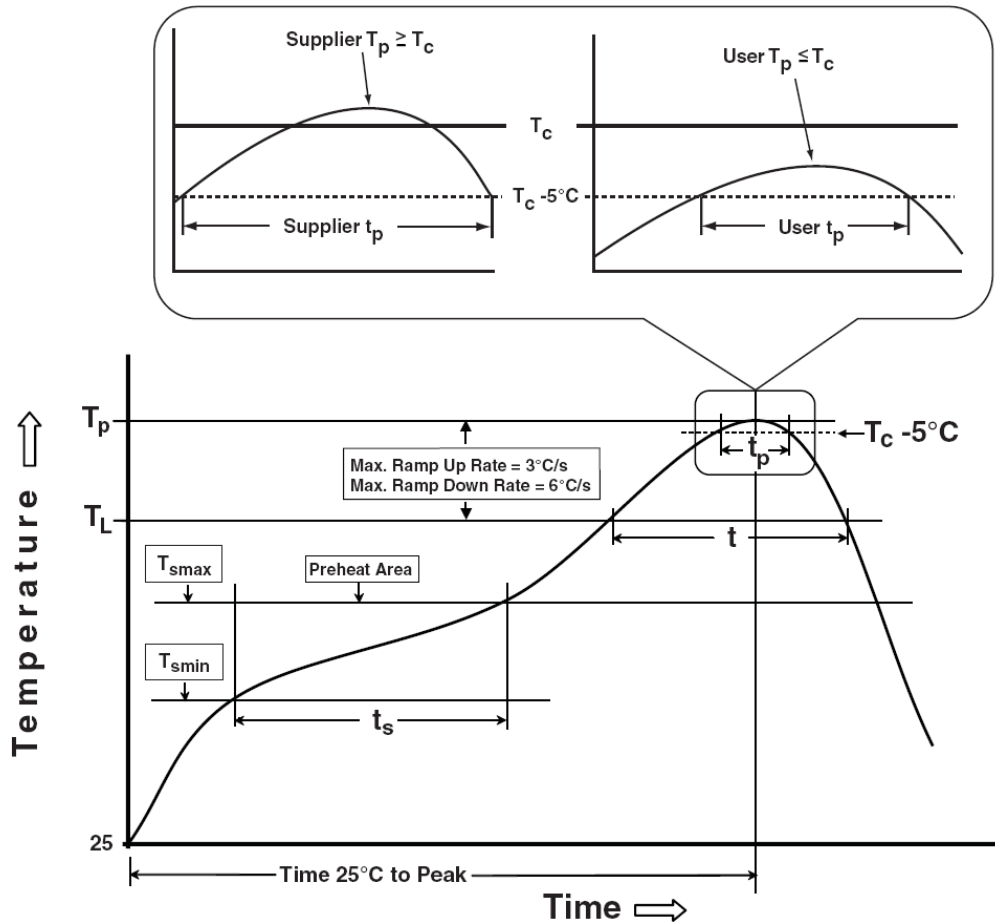
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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