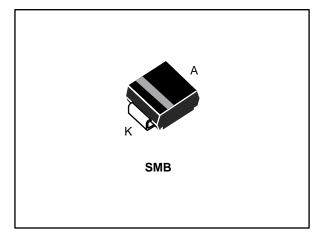


# SMLVT3V3

## Low voltage Transil™

Datasheet - production data



### Features

- Peak pulse power 600 W (10/1000 μs)
- Stand-off voltage 3.3 V
- Unidirectional type
- Low clamping factor
- Fast response time
- JEDEC registered package outline

### Description

This is a Transil diode designed specifically to protect sensitive 3.3 V equipment against transient overvoltages.

Transil diodes provide high overvoltage protection by clamping action. Their instantaneous response to transient overvoltages make them particularly suited to protect voltage sensitive devices such as MOS technology and low voltage supplied ICs.

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TM: Transil is a trademark of STMicroelectronics

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This is information on a product in full production.

### 1 Characteristics

#### Table 1: Absolute maximum ratings (limiting values at T<sub>amb</sub> = 25 °C unless otherwise specified)

Symbol	Parameter	Value	Unit	
P <sub>pp</sub>	Peak pulse power dissipation <sup>(1)</sup>	600	W	
Р	Power dissipation on infinite heatsink	6	W	
IFSM	Non repetitive surge peak forward current for unidirectional types	100	А	
T <sub>stg</sub>	Storage temperature range	-65 to +175	°C	
Tj	Junction temperature range	-55 to +175	°C	
ΤL	Maximum lead temperature for soldering durin	ng 10 s.	260	°C

#### Notes:

 $^{(1)}\mbox{For a surge greater than the maximum values, the diode will fail in short-circuit.$ 

Table 2: Thermal resistances						
Symbol	Parameter	Value	Unit			
Rth(j-l)	Junction to leads	20	°C/W			
R <sub>th</sub> (j-a)	Junction to ambient on printed circuit on recommended pad layout	100	°C/W			

Figure 1: Electrical characteristics (definitions)

#### Symbol Parameter VBR Breakdown voltage Leakage current at V<sub>RM</sub> IRM Stand-off voltage $V_{RM}$ V<sub>BR</sub> V<sub>CL</sub>↓ V Clamping voltage VRM VCL Rd Dynamic resistance RN Peak pulse current lpp Series resistance between Input and Output RI/O Cline Input capacitance per line

<b>Table 3: Electrical characteristics</b>	(T <sub>amb</sub> = 25 °C)
--	----------------------------

Туре	I <sub>RM</sub> at Ma		V <sub>BR</sub> at I <sub>R</sub> <sup>(1)</sup> Min.		V <sub>CL</sub> at I <sub>PP</sub> 10/1000 μs Max.		V <sub>CL</sub> at I <sub>PP</sub> 8/20 µs Max.		αT <sup>(2)</sup> Max.	С <sup>(3)</sup> Тур.
	μA	v	v	mA	v	А	v	А	10⁻⁴/°C	pF
SMLVT3V3	200	3.3	4.1	1	7.3	50	10.3	200	-5.3	5200

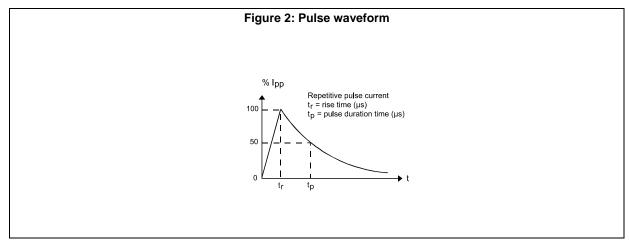
#### Notes:

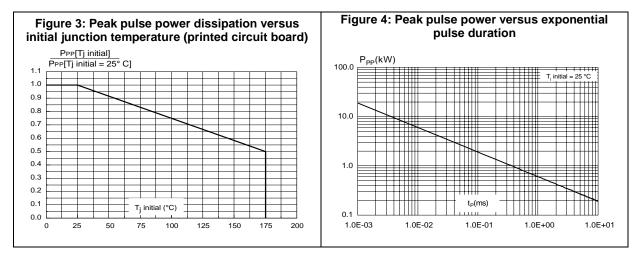
$$\label{eq:VBR} \begin{split} & {}^{(1)}\text{Pulse test : } t_p < 50 \text{ ms} \\ & {}^{(2)}\text{V}_{\text{BR}} = \alpha\text{T x} \ (\text{T}_{\text{amb}} \ \text{-}25) \ x \ \text{V}_{\text{BR}} \ (25 \ \text{°C}) \\ & {}^{(3)}\text{V}_{\text{R}} = 0 \ \text{V}, \ \text{F} = 1 \ \text{MHz} \end{split}$$

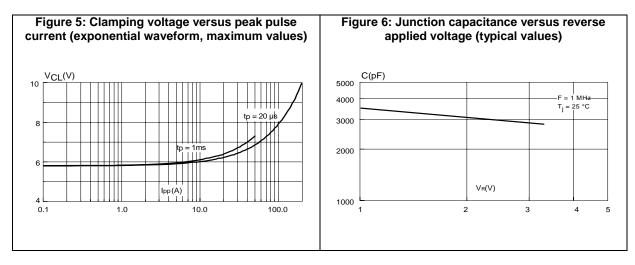
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### 1.1 Characteristics (curves)







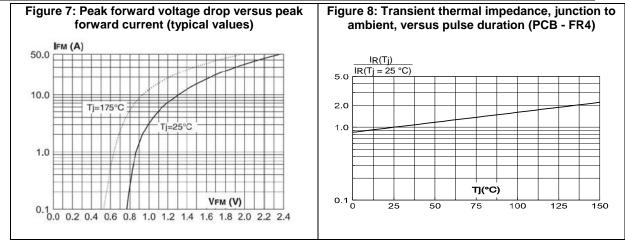
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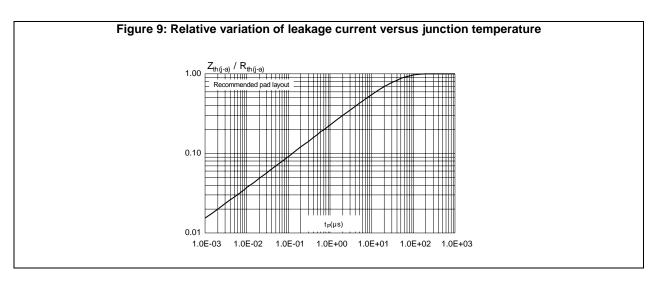
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#### Characteristics

#### SMLVT3V3





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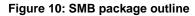


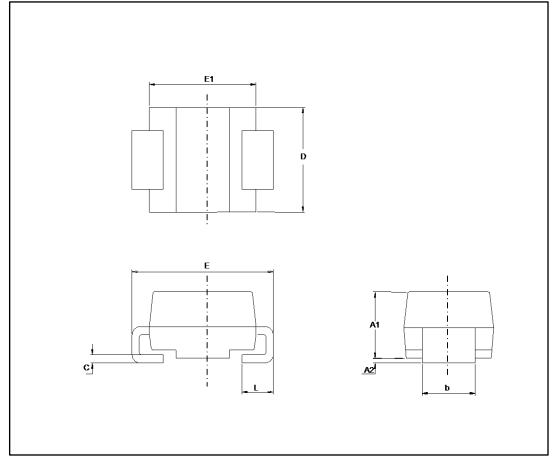
### 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK<sup>®</sup> is an ST trademark.

- Case: JEDEC DO-214AA molded plastic over Planar junction
- Epoxy meets UL94, V0
- RoHS compliant package

### 2.1 SMB package information



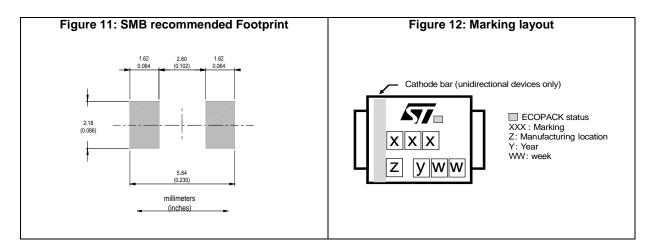




#### Package information

#### SMLVT3V3

	Dimensions					
Ref.	Millin	neters	Inches			
	Min.	Max.	Min.	Max.		
A1	1.90	2.45	0.0748	0.0965		
A2	0.05	0.20	0.0020	0.0079		
b	1.95	2.20	0.0768	0.0867		
С	0.15	0.40	0.0059	0.0157		
D	3.30	3.95	0.1299	0.1556		
E	5.10	5.60	0.2008	0.2205		
E1	4.05	4.60	0.1594	0.1811		
L	0.75	1.50	0.0295	0.0591		





### **3** Ordering information

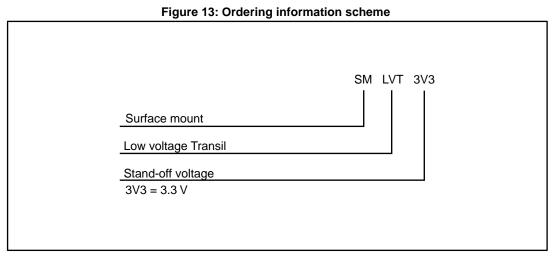


Table 5: Ordering information						
Order code	Order code Marking Package Weight Base qty. Delivery mode					
SMLVT3V3	CD	SMB	0.12 g	2500	Tape and reel	

### 4 Revision history

Table 6: Document revision history

Date	Revision	Changes
Aug-2001	2	Previous issue
25-Apr-2007	3	Reformatted to current standards. Added cathode bar marker in cover page graphics and <i>Figure 11</i> .
14-Sep-2011	4	Updated Junction temperature range in Table 1.
06-Apr-2017	5	Updated Table 1: "Absolute maximum ratings (limiting values at Tamb = 25 °C unless otherwise specified)".



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