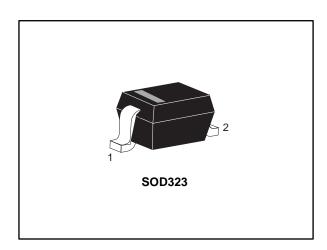


### ESDLIN1524BJ

# Automotive Transil™, transient voltage suppressor (TVS) for LIN bus

Datasheet - production data



#### **Features**

- AEC-Q101 qualified
- Asymmetrical bidirectional device
- Stand-off voltage:
  - 15 V (to comply with reverse battery)
  - + 24 V (to comply with jump start)
- Low leakage current

#### Complies with the following standards

- ISO 10605 (C = 150 pF, R = 330 Ω)
  - 30 kV (air discharge)
  - 30 kV (contact discharge)
- ISO 10605 (C = 330 pF, R = 330  $\Omega$ )
  - 30 kV (air discharge)
  - 30 kV (contact discharge)
- ISO 7637-3
  - Pulse 3a: V<sub>S</sub> = -150 V
  - Pulse 3b: V<sub>S</sub> = 100 V
- HBM MIL STD 833, class 3 (> 4 kV)
- ISO 17987-7 (LIN bus)
- SAE J3076 (CXPI bus)

#### **Description**

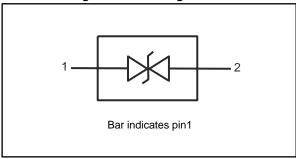
The device is an asymmetrical Transil diode designed specifically for one automotive LIN bus line against electrostatic discharge (ESD) protection. The SOD323 is a very small package that saves space on high density printed circuit board.

Transil diodes provide high overvoltage protection by clamping action and have instantaneous response to transient overvoltages.



TM: Transil is a trademark of STMicroelectronics.

Figure 1: Pin configuration



October 2017 DocID12658 Rev 4 1/12

Characteristics ESDLIN1524BJ

#### 1 Characteristics

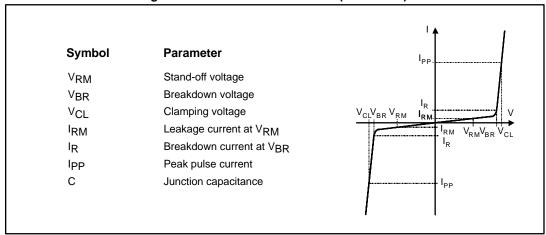
Table 1: Absolute maximum ratings (limiting values) T<sub>amb</sub> = 25° C

Symbol	Parameter			Unit
P <sub>PP</sub>	Peak pulse power dissipation 8/20 μs	160	W	
T <sub>stg</sub>	Storage junction temperature range -65 to			°C
Tj	Maximum operating junction temperature -40 to +150			
TL	Maximum temperature for soldering during 10 s 260			°C

Table 2: ESD maximum ratings

Symbol	Parameter	Conditions	Value	Unit
		ISO 10605 (C = 150 pF, R = 330 Ω)		
	Electrostatic discharge capability	air discharge	30	
		contact discharge	30	
ESD		c discharge capability ISO 10605 (C = 330 pF, R = 330 Ω)		kV
		air discharge		
		contact discharge		
		HBM MIL STD 833	10	

Figure 2: Electrical characteristics (definitions)



47/

ESDLIN1524BJ Characteristics

Table 3: Electrical characteristics (T<sub>amb</sub> = 25 °C)

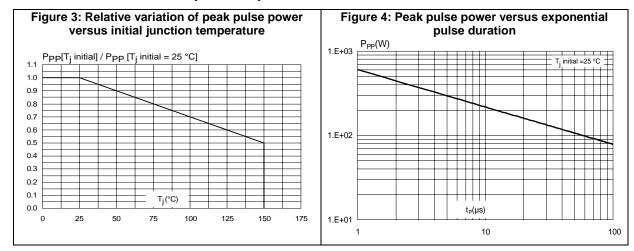
Symbol	Test conditions			Min.	Тур.	Max.	Unit
V <sub>BR</sub>	From pin 2 to pin 1	$I_R = 5 \text{ mA}, t_p < 50 \text{ ms}$		25.4	27.8	30.3	·
	From pin 1 to pin 2			17.1	18.9	20.3	
la	From pin 2 to pin 1	V <sub>RM</sub> = 24 V V <sub>RM</sub> = 15 V			1 50	nA	
I <sub>RM</sub>	From pin 1 to pin 2					50	
	From pin 2 to pin 1	I <sub>PP</sub> = 1 A				40	
VcL	From pin 2 to pin 1	I <sub>PP</sub> = 3 A				50	V
	From pin 1 to pin 2	I <sub>PP</sub> = 1 A	8/20 µs			25	V
	From pin 1 to pin 2	I <sub>PP</sub> = 5 A				35	
С	V <sub>R</sub> = 0 V, f = 1 MHz				16	20	pF
αT <sup>(1)</sup>	From pin 2 to pin 1					9.6	10 <sup>-4</sup> /°C
	From pin 1 to pin 2					8.8	10 7 C

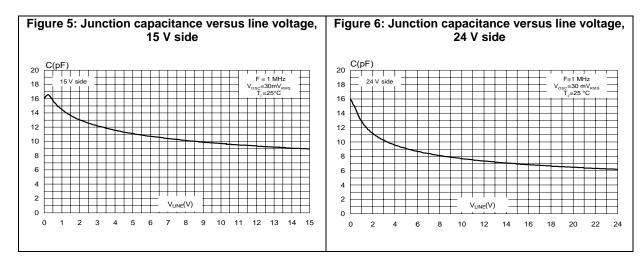
#### Notes:

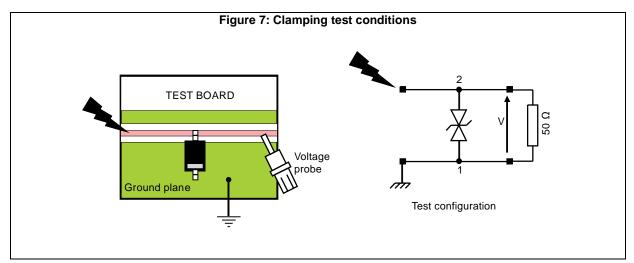
 $<sup>^{(1)}\</sup>Delta V_{BR}$  =  $\alpha T$  x ( $T_{amb}$  - 25) x  $V_{BR}$ (25° C)

Characteristics ESDLIN1524BJ

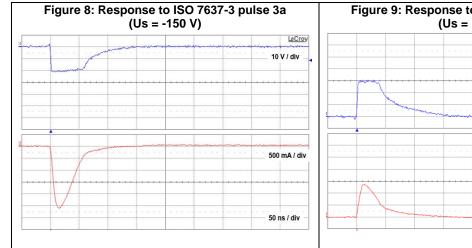
### 1.1 Characteristics (curves)

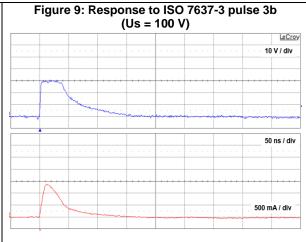


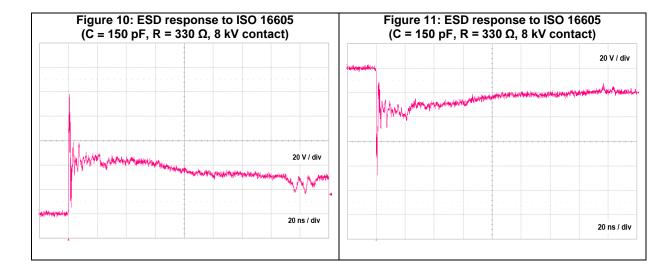




4/12 DocID12658 Rev 4







Package information ESDLIN1524BJ

# 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

- Epoxy meets UL94, V0
- Lead-free package

### 2.1 SOD323 package information

Figure 12: SOD323 package outline

A2

A1

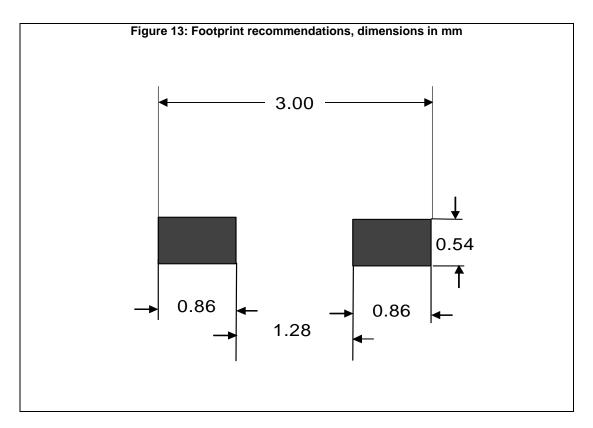
A1

C

Q1

Table 4: SOD323 package mechanical data

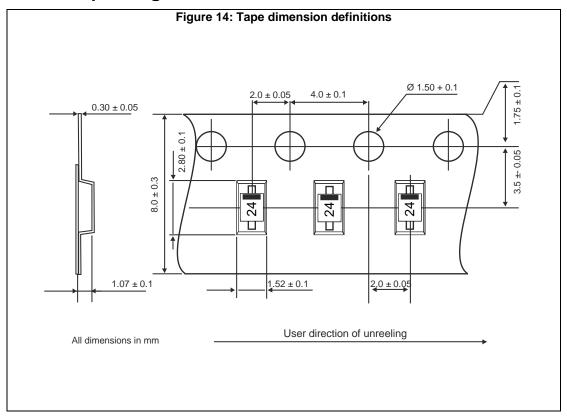
Table 4. 000020 package mediamoar data							
	Dimensions						
Ref.	Millir	neters	Inches				
	Min.	Max.	Min.	Max.			
А		1.17		0.046			
A1		0.10		0.004			
A2	0.93	1.01	0.037	0.040			
b	0.25	0.44	0.01	0.017			
С	0.10	0.25	0.004	0.01			
D	1.52	1.80	0.06	0.071			
Е	1.11	1.45	0.044	0.057			
HD	2.30	2.70	0.09	0.106			
L	0.10	0.46	0.004	0.02			
Q1	0.10	0.41	0.004	0.016			

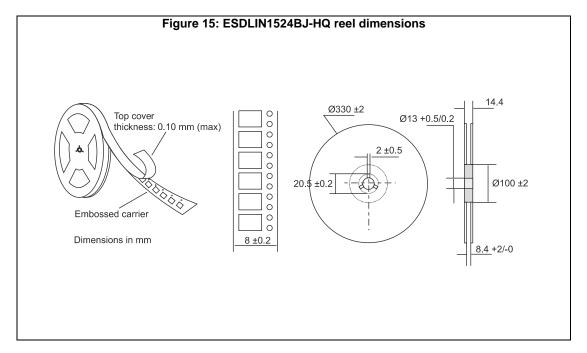




Package information ESDLIN1524BJ

# 2.2 SOD323 packing information

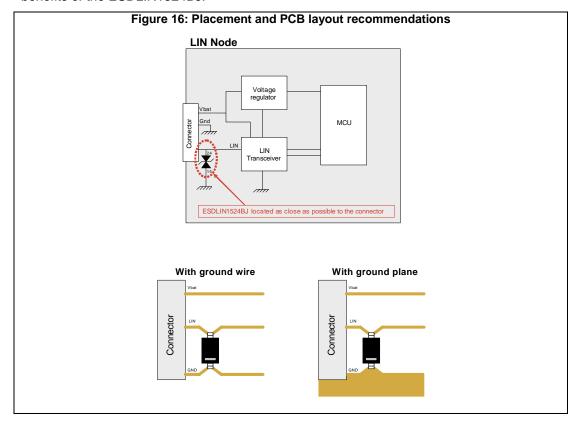




577

# 3 Placement and PCB layout recommendations

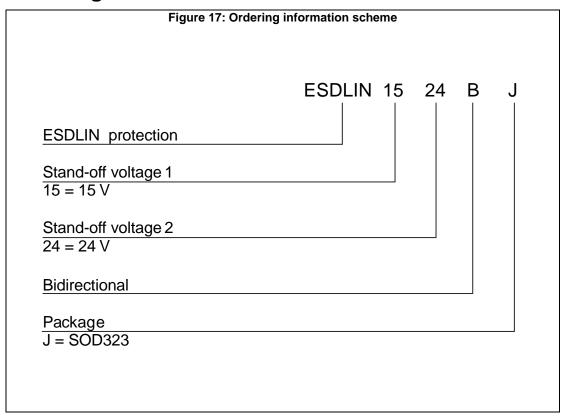
Below figure illustrates the PCB placement and layout recommendations for optimal benefits of the ESDLIN1524BJ.





Ordering information ESDLIN1524BJ

# 4 Ordering information



**Table 5: Ordering information** 

<del>_</del>					
Order code	Marking	Package	Weight	Base qty.	Delivery mode
ESDLIN1524BJ	24	CODOO	C	3000	Tono and vool
ESDLIN1524BJ-HQ	24	SOD323	5 mg	10000	Tape and reel

ESDLIN1524BJ Revision history

# 5 Revision history

Table 6: Document revision history

Date	Revision	Changes	
28-Aug-2006	1	Initial release.	
22-Sep-2006	2	Added Figure 6 Placement and layout recommendations	
18-Jan-2013	3	Updated Table 6. Added Figure 10 and Figure 11.	
17-Oct-2017	4	Updated title and cover page.  Updated Table 1: "Absolute maximum ratings (limiting values) Tamb = $25^{\circ}$ C" and Table 3: "Electrical characteristics (Tamb = $25^{\circ}$ C)".  Added Figure 8: "Response to ISO 7637-3 pulse 3a (Us = $150^{\circ}$ V)", Figure 9: "Response to ISO 7637-3 pulse 3b (Us = $100^{\circ}$ V)", Figure 10: "ESD response to ISO $16605$ ( C = $150^{\circ}$ pF, R = $330^{\circ}$ Q, 8 kV contact)" and Figure 11: "ESD response to ISO $16605$ ( C = $150^{\circ}$ pF, R = $330^{\circ}$ Q, 8 kV contact)".  Minor text changes to improve readability.	



#### **IMPORTANT NOTICE - PLEASE READ CAREFULLY**

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2017 STMicroelectronics - All rights reserved



单击下面可查看定价,库存,交付和生命周期等信息

>>STMicroelectronics(意法半导体)