

## Features

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on  $V_F$

## Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

## Applications

- Switch Mode Power Supplies
- Power Factor Correction
- Motor Drives

$V_{RRM}$	=	650 V
$I_F (T_c=135^\circ\text{C})$	=	14.5 A
$Q_c$	=	24 nC



TO-247-2

## Package

Part Number	Package	Marking
GC3D10065H	TO-247-2	GC3D10065



## Maximum Ratings ( $T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
$V_{RRM}$	Repetitive Peak Reverse Voltage	650	V		
$V_{RSM}$	Surge Peak Reverse Voltage	650	V		
$V_{DC}$	DC Blocking Voltage	650	V		
$I_F$	Continuous Forward Current	30 14.5 10	A	$T_c=25^\circ\text{C}$ $T_c=135^\circ\text{C}$ $T_c=153^\circ\text{C}$	Fig. 3
$I_{FRM}$	Repetitive Peak Forward Surge Current	46 31	A	$T_c=25^\circ\text{C}, t_p = 10 \text{ ms}$ , Half Sine Wave $T_c=110^\circ\text{C}, t_p = 10 \text{ ms}$ , Half Sine Wave	
$I_{FSM}$	Non-Repetitive Peak Forward Surge Current	90 71	A	$T_c=25^\circ\text{C}, t_p = 10 \text{ ms}$ , Half Sine Wave $T_c=110^\circ\text{C}, t_p = 10 \text{ ms}$ , Half Sine Wave	Fig. 8
$I_{FMax}$	Non-Repetitive Peak Forward Surge Current	860 680	A	$T_c=25^\circ\text{C}, t_p = 10 \mu\text{s}$ , Pulse $T_c=110^\circ\text{C}, t_p = 10 \mu\text{s}$ , Pulse	Fig. 8
$P_{tot}$	Power Dissipation	136.5 59	W	$T_c=25^\circ\text{C}$ $T_c=110^\circ\text{C}$	Fig. 4
$T_J, T_{stg}$	Operating Junction and Storage Temperature	-55 to +175	$^\circ\text{C}$		
	TO-220 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	

### Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$V_F$	Forward Voltage	1.5 2.0	1.8 2.4	V	$I_F = 10\text{ A}$ , $T_J = 25^\circ\text{C}$ $I_F = 10\text{ A}$ , $T_J = 175^\circ\text{C}$	Fig. 1
$I_R$	Reverse Current	12 24	60 220	$\mu\text{A}$	$V_R = 650\text{ V}$ , $T_J = 25^\circ\text{C}$ $V_R = 650\text{ V}$ , $T_J = 175^\circ\text{C}$	Fig. 2
$Q_C$	Total Capacitive Charge	24		nC	$V_R = 400\text{ V}$ , $I_F = 10\text{ A}$ $di/dt = 500\text{ A}/\mu\text{s}$ $T_J = 25^\circ\text{C}$	Fig. 5
C	Total Capacitance	460.5 44 40		pF	$V_R = 0\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$ $V_R = 200\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$ $V_R = 400\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$	Fig. 6
$E_C$	Capacitance Stored Energy	3.6		$\mu\text{J}$	$V_R = 400\text{ V}$	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

### Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.1	$^\circ\text{C}/\text{W}$	Fig. 9

### Typical Performance

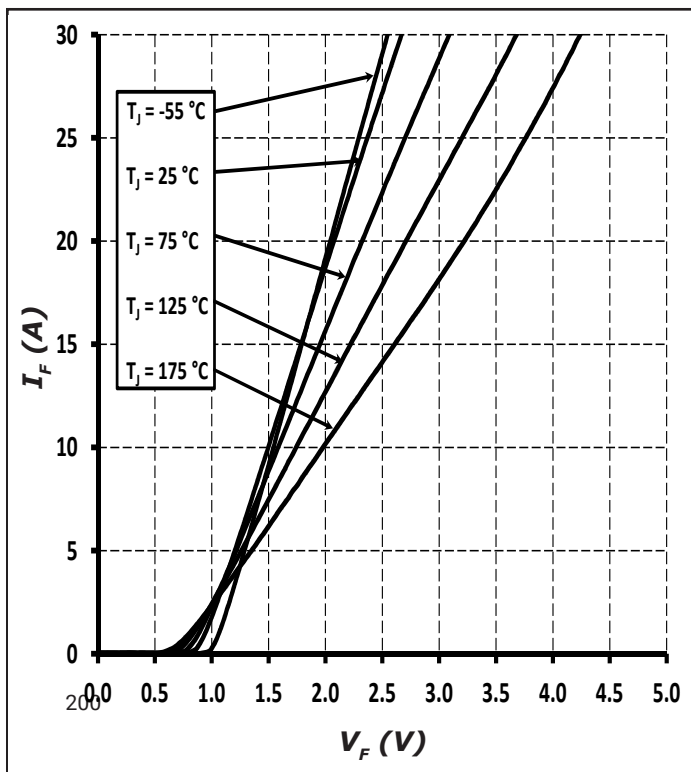


Figure 1. Forward Characteristics

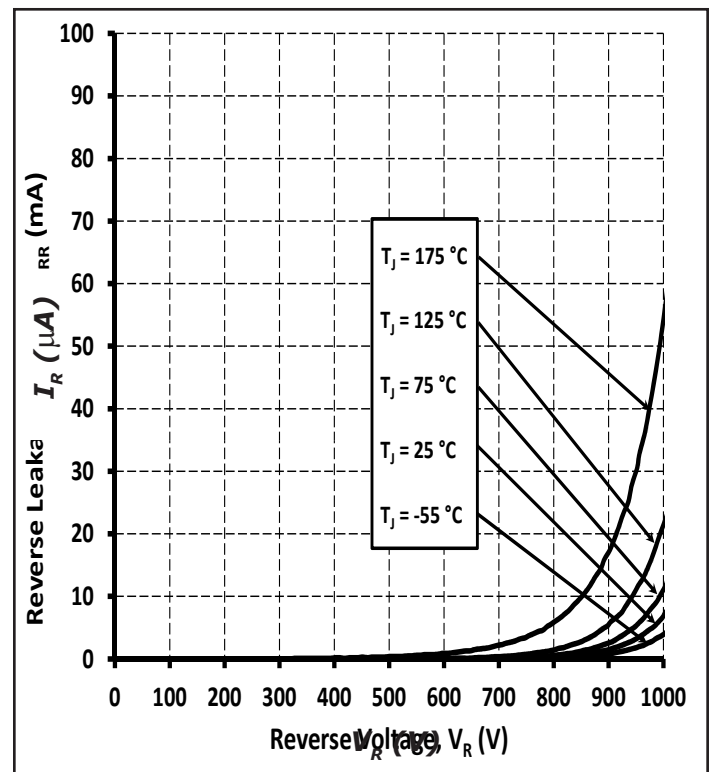


Figure 2. Reverse Characteristics

Typical Performance

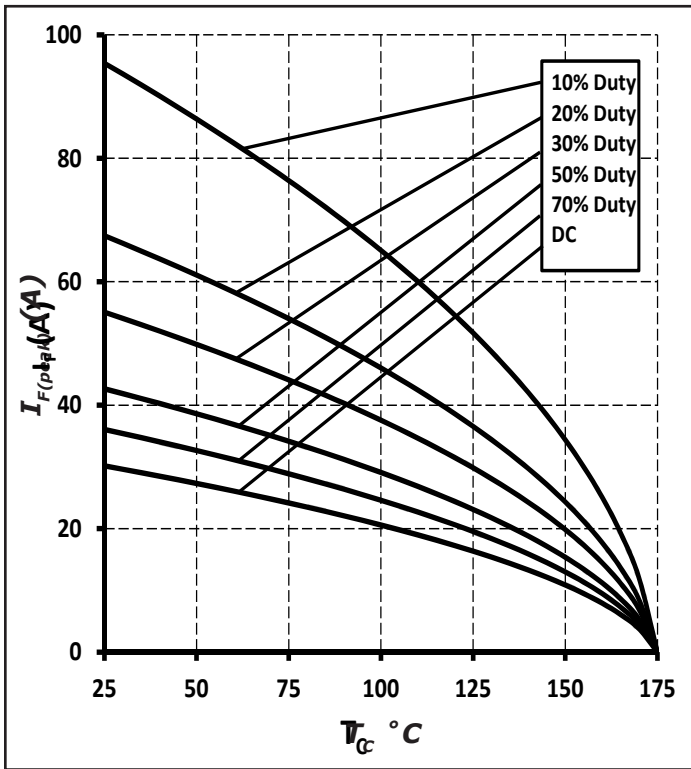


Figure 3. Current Derating

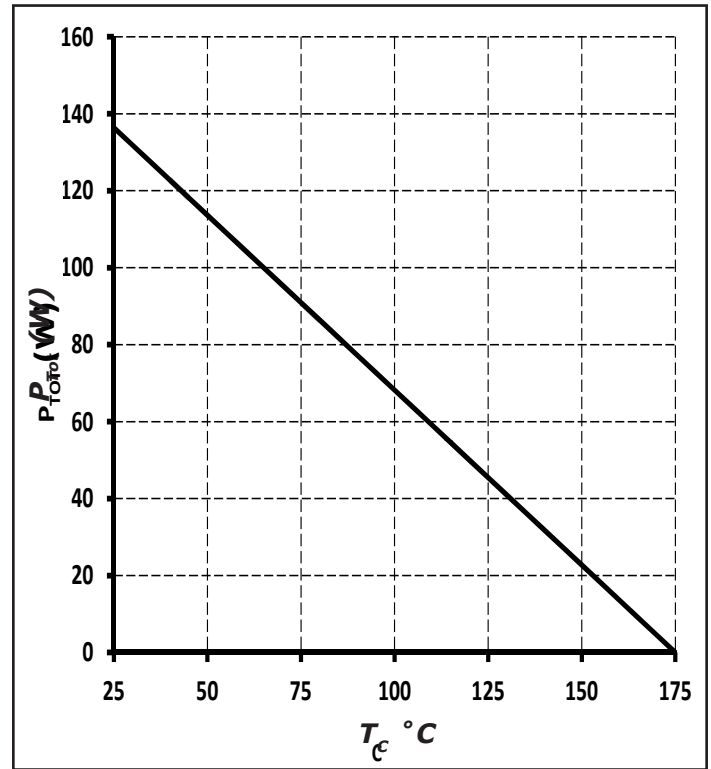


Figure 4. Power Derating

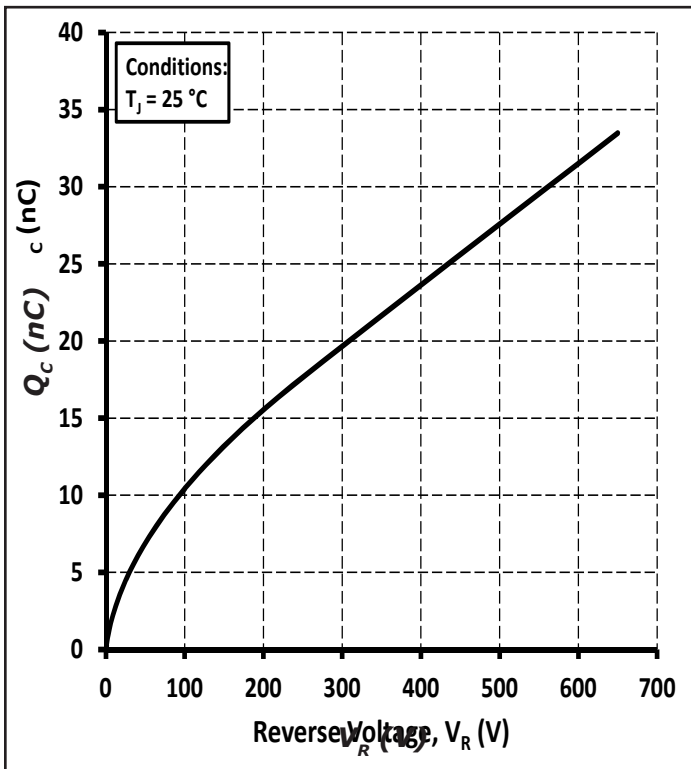


Figure 5. Total Capacitance Charge vs. Reverse Voltage

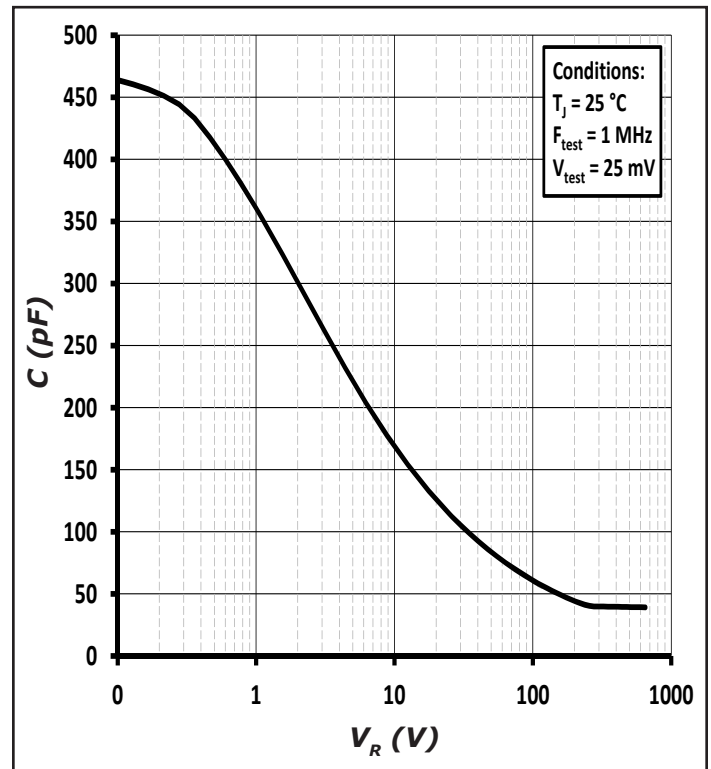


Figure 6. Capacitance vs. Reverse Voltage

Typical Performance

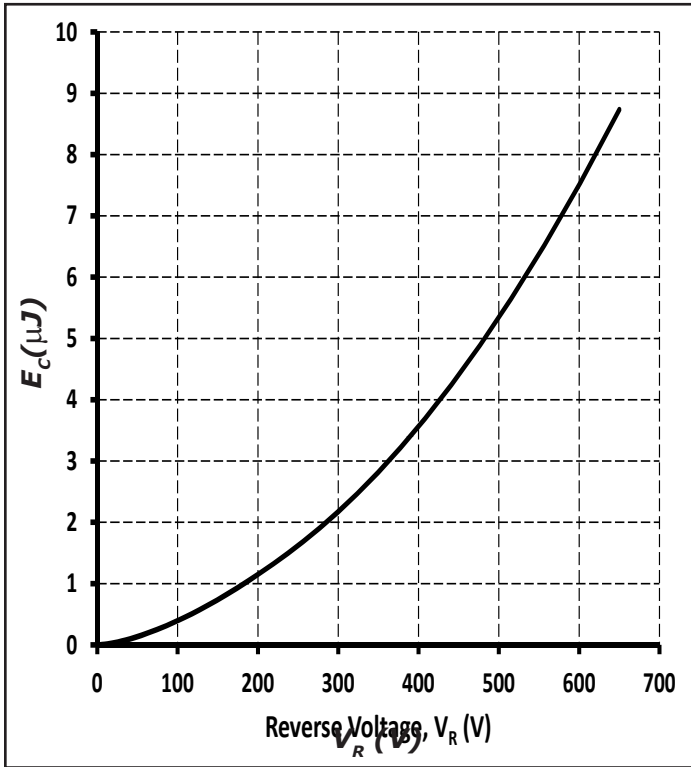


Figure 7. Capacitance Stored Energy

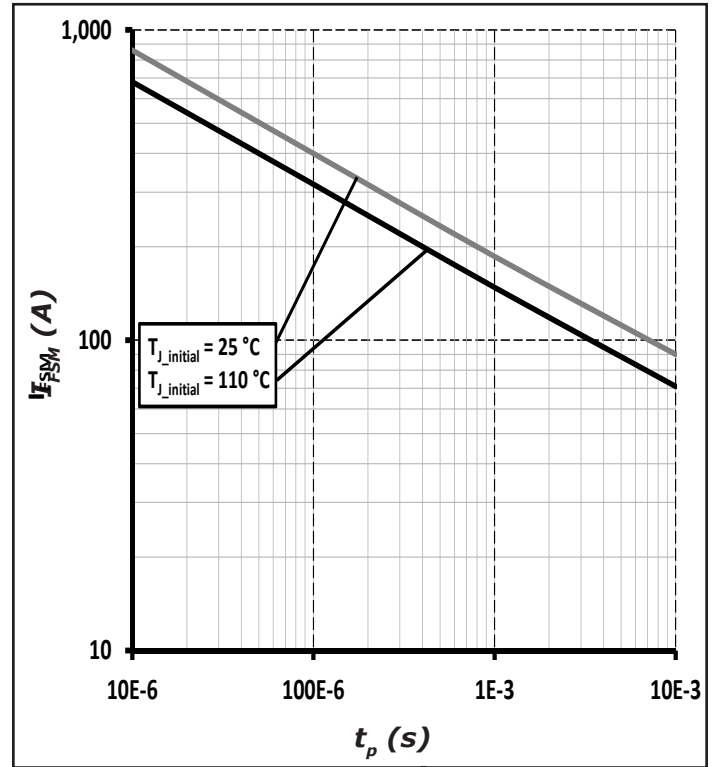


Figure 8. Non-repetitive peak forward surge current versus pulse duration (sinusoidal waveform)

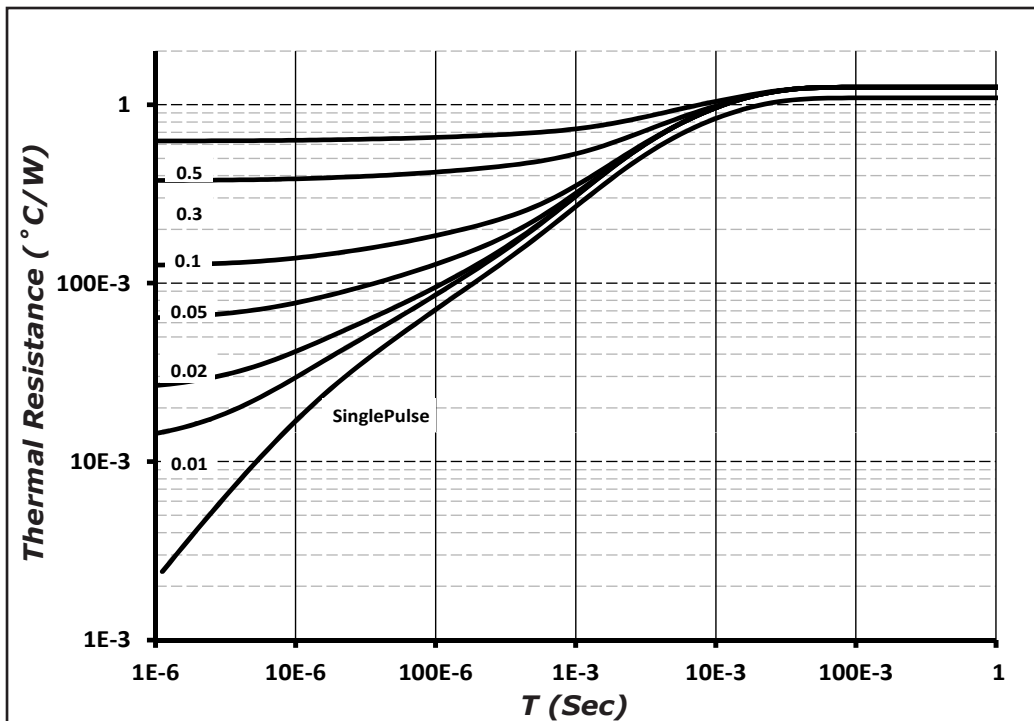
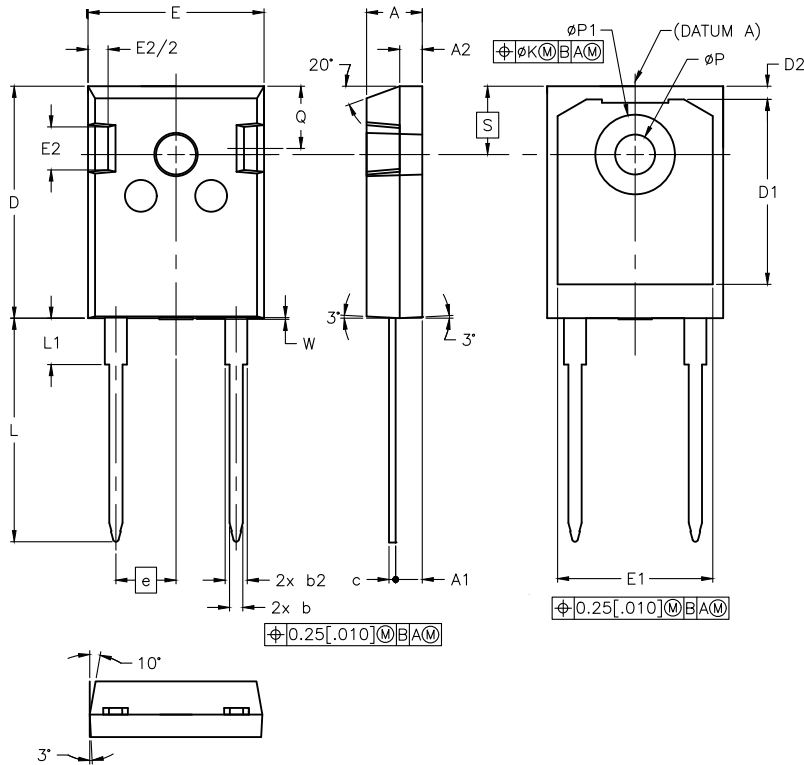


Figure 9. Transient Thermal Impedance

## Package Dimensions

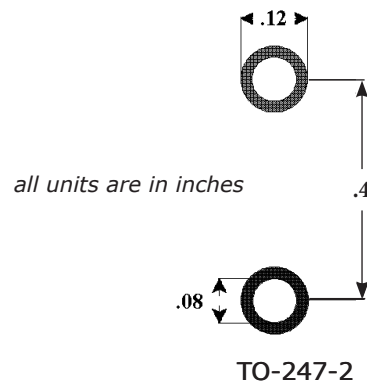
Package TO-247-2



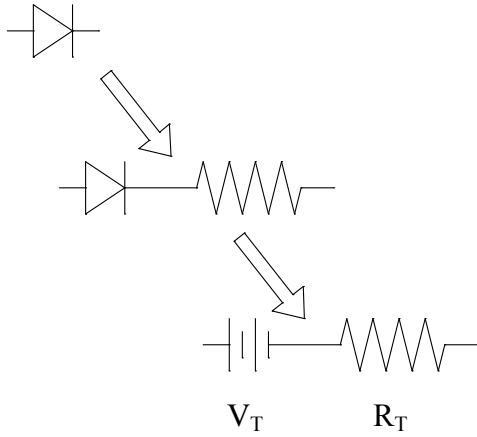
POS	Inches		Millimeters	
	Min	Max	Min	Max
A	.190	.205	4.70	5.31
A1	.087	.102	2.21	2.59
A2	.059	.098	1.50	2.49
b	.039	.055	0.99	1.40
b2	.065	.094	1.65	2.39
c	.015	.035	0.38	0.89
D	.819	.845	20.80	21.46
D1	.515	-	13.08	-
D2	.020	.053	0.51	1.35
E	.620	.640	15.49	16.26
E1	.530	-	13.46	-
E2	.135	.157	3.43	3.99
e	.214		5.44	
ØK	.010		0.25	
L	.780	.800	19.81	20.32
L1	-	.177	-	4.50
ØP	.140	.144	3.56	3.66
ØP1	.278	.291	7.06	7.39
Q	.212	.244	5.38	6.20
S	.243		6.17	
W	-	.006	-	0.15



## Recommended Solder Pad Layout



**Diode Model**



$$V_{f_T} = V_T + I_f * R_T$$

$$V_T = 0.94 + (T_J * -1.3 * 10^{-3})$$

$$R_T = 0.044 + (T_J * 4.4 * 10^{-4})$$

Note:  $T_J$  = Diode Junction Temperature In Degrees Celsius,  
valid from 25°C to 175°C

单击下面可查看定价，库存，交付和生命周期等信息

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