Taiwan Semiconductor



### **FEATURES**

• Glass passivated chip junction

SEMICONDUCTOR

- Ideal for automated placeme
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant

TAIWAN

• Halogen-free according to IEC 61249-2-21

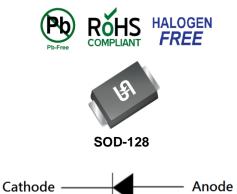
### **APPLICATIONS**

- DC to DC converter
- Switching mode converters and inverters
- General purpose

### **MECHANICAL DATA**

- Case: SOD-128
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.027g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
١ <sub>F</sub>	1	А	
V <sub>RRM</sub>	200 - 1000	V	
I <sub>FSM</sub>	30	А	
T <sub>J MAX</sub>	150	°C	
Package	SOD-128		
Configuration	Single die		



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)							
PARAMETER	SYMBOL	S1DFS	S1GFS	S1JFS	S1KFS	S1MFS	UNIT
Marking code on the device		S1DFS	S1GFS	S1JFS	S1KFS	S1MFS	
Repetitive peak reverse voltage	V <sub>RRM</sub>	200	400	600	800	1000	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	140	280	420	560	700	V
Forward current	١ <sub>F</sub>			1			А
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	30			A		
Junction temperature	TJ	-55 to +150			°C		
Storage temperature	T <sub>STG</sub>	-55 to +150			°C		



THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R <sub>θJL</sub>	29	°C/W	
Junction-to-ambient thermal resistance	R <sub>eJA</sub>	82	°C/W	
Junction-to-case thermal resistance	R <sub>eJC</sub>	30	°C/W	

Thermal Performance Note: Units mounted on PCB (5mm x 5mm Cu pad test board)

ELECTRICAL SPECIFICATIONS ( $T_A = 25^{\circ}C$ unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
	$I_F = 0.5A, T_J = 25^{\circ}C$		0.91	1.0	V
Forward voltage <sup>(1)</sup>	$I_F = 1.0A, T_J = 25^{\circ}C$	V <sub>F</sub>	0.99	1.1	V
	$I_F = 0.5A, T_J = 125^{\circ}C$		0.78	0.87	V
	$I_F = 1.0A, T_J = 125^{\circ}C$		0.85	0.95	V
Reverse current @ rated $V_R^{(2)}$	T <sub>J</sub> = 25°C	I <sub>R</sub>	-	1	μA
	T <sub>J</sub> = 125°C		-	50	μA
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	9	-	pF

#### Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

# ORDERING INFORMATION

ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING
S1xFS	SOD-128	14,000 / Tape & Reel

Notes:

1. "x" defines voltage from 200V(S1DFS) to 1000V(S1MFS)



## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Fig.1 Forward Current Derating Curve

100

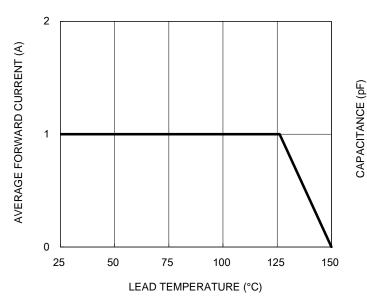
10

1

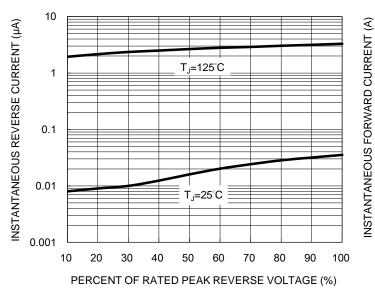
0.1

1

f=1.0MHz Vsig=50mVp-p



#### **Fig.3 Typical Reverse Characteristics**



**Fig.4 Typical Forward Characteristics** 

10

**REVERSE VOLTAGE (V)** 

100

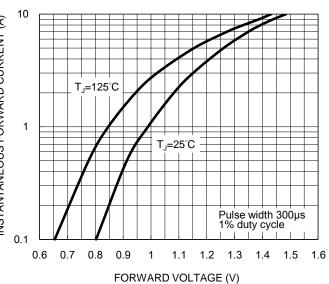
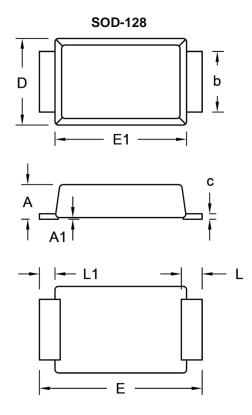


Fig.2 Typical Junction Capacitance

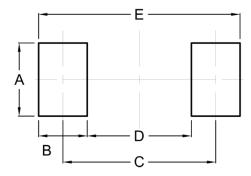


## **PACKAGE OUTLINE DIMENSIONS**



DIM.	Unit (mm)		Unit	inch)	
	Min.	Max.	Min.	Max.	
A	0.90	1.10	0.035	0.043	
A1	0.00	0.10	0.000	0.004	
b	1.60	1.90	0.063	0.075	
с	0.10	0.22	0.004	0.009	
D	2.30	2.70	0.091	0.106	
E	4.40	5.00	0.173	0.197	
E1	3.60	4.00	0.142	0.157	
L	0.40	0.80	0.016	0.031	
L1	0.30	0.60	0.012	0.024	

## SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.10	0.083
В	1.40	0.055
С	4.40	0.173
D	3.00	0.118
E	5.80	0.228

# **MARKING DIAGRAM**



P/N = Marking Code

YW = Date Code

= Factory Code F



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